



SystemC CCI WG

Elaboration Time Parameter Example

Udaya Ranga L.V., Texas Instruments Inc.
February 2011



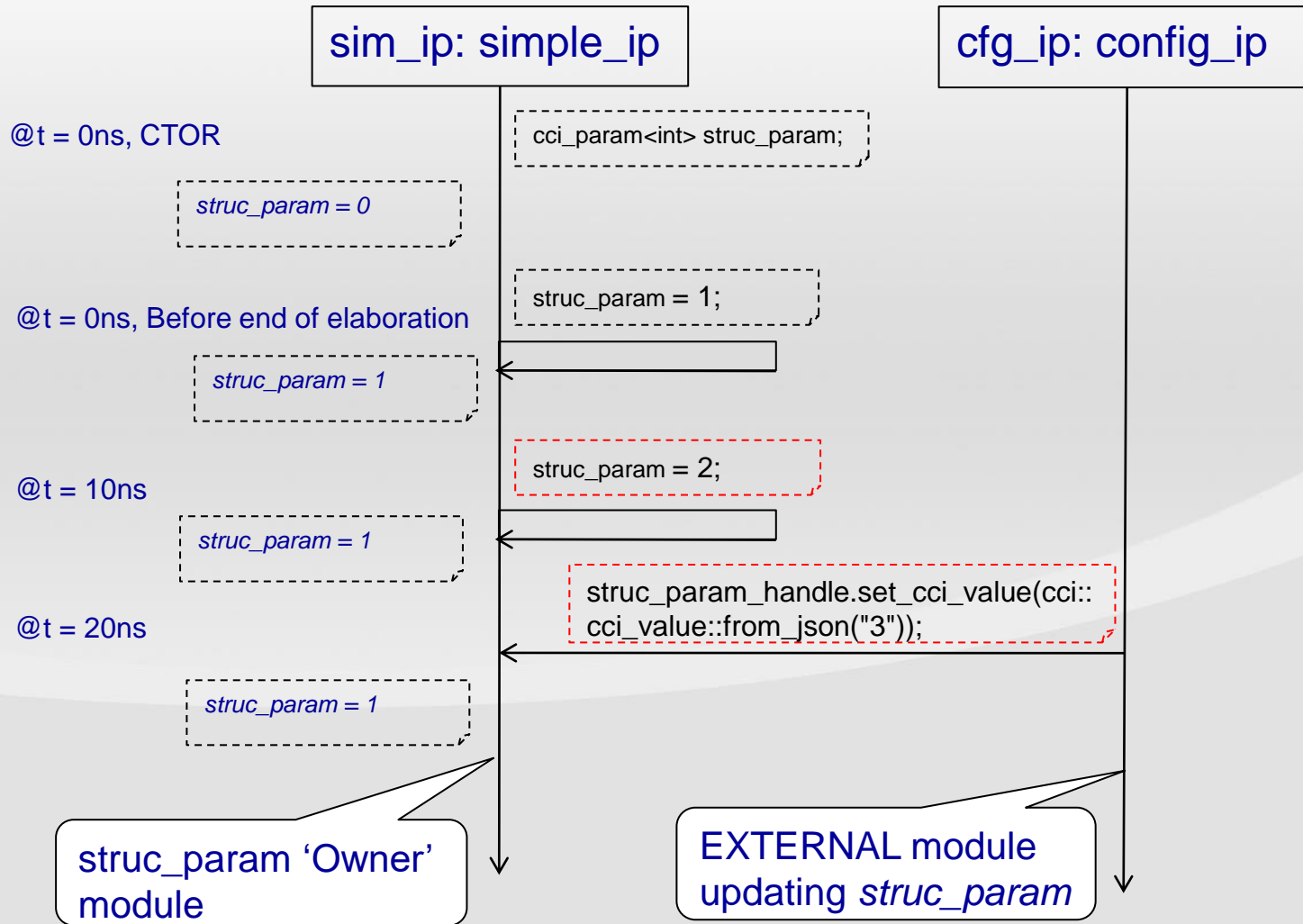
Elaboration-Time Parameters

Use of structural parameters:

- **Handing of invalid (post-elaboration) updates to structural parameter values.**
- **Demonstrate how models that finalize their structure prior to `end_of_elaboration` should lock their elaboration-time parameters.**

Example Sequence Diagram

Demonstrates how models that finalize their structure prior to `end_of_elaboration` should lock their elaboration-time parameters.



Expected Output

(ex03_Elab_Time_Param.log)

SystemC Simulation

Info: sim_ip: @0 s, Ctor: Default value of sim_ip.struc_param is 0

Info: sc_main: Begin Simulation.

Info: sim_ip: @0 s, Before_End_Elab: Set value of struc_param to 1

Info: sim_ip: @0 s, Before_End_Elab: Value of struc_param changed to 1

Info: sim_ip: @10 ns, execute: Set value of struc_param to 2

Error: /Accellera/CCI/SET_PARAM_FAILED: Parameter locked.

In file: <removed by verify.pl>

In process: sim_ip.execute @ 10 ns

Info: sim_ip: @10 ns, execute: Value of struc_param unchanged 1

Info: cfg_ip: @20 ns, execute: [EXTERNAL] Set value of sim_ip.struc_param to 3

Error: /Accellera/CCI/SET_PARAM_FAILED: Param locked.

In file: <removed by verify.pl>

In process: cfg_ip.execute @ 20 ns

Info: cfg_ip: @20 ns, execute: [EXTERNAL] Value of sim_ip.struc_param remains unchanged 1

Info: sc_main: End Simulation.

Info: sim_ip: Dtor: Current value of sim_ip.struc_param is 1