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## Assignment 2

BRAC University

Semester: Fall 2023

Course No: CSE251	Marks:
Course title: Electronic Devices and Circuits	Submission Date: 24/10/2023
Faculty: TMT	

- For the circuit given below, draw the output waveform across the resistor [The green line is used to represent the 5V DC source] [CO2]: [Marks: 12]

Here,  $V_{sat} = 10V$ .

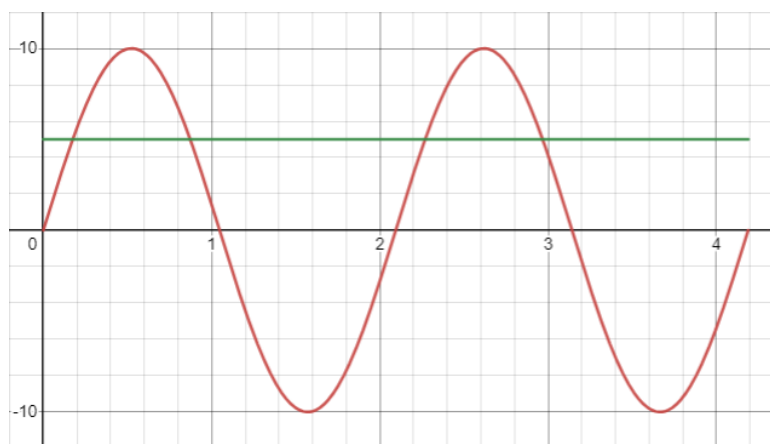
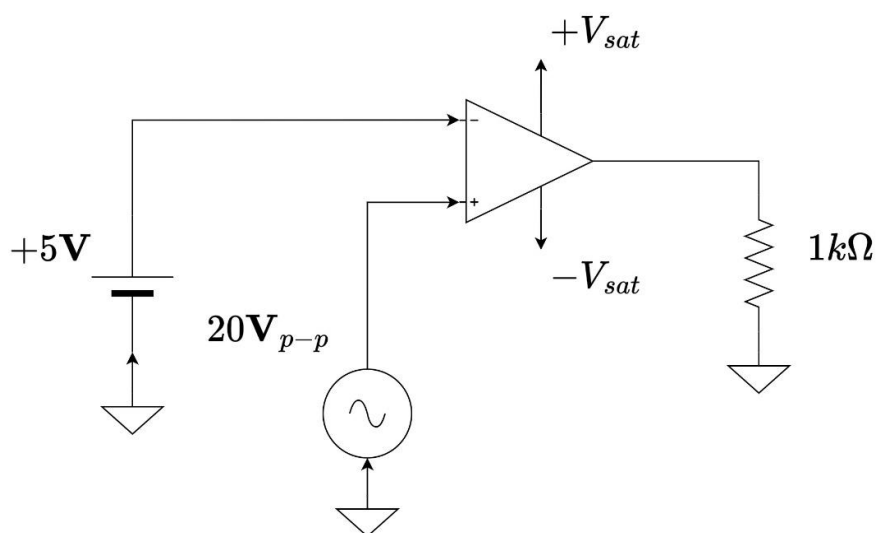
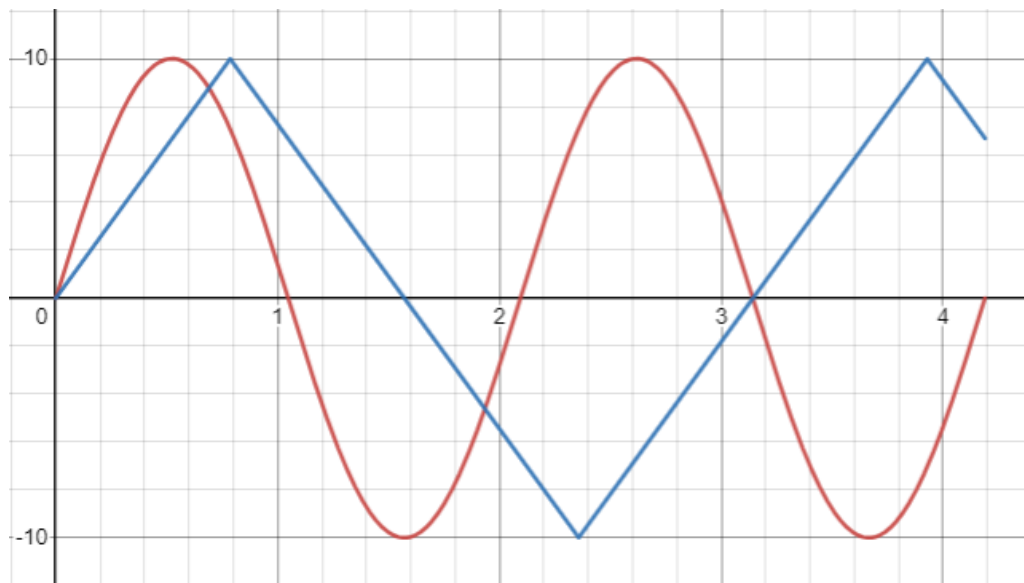
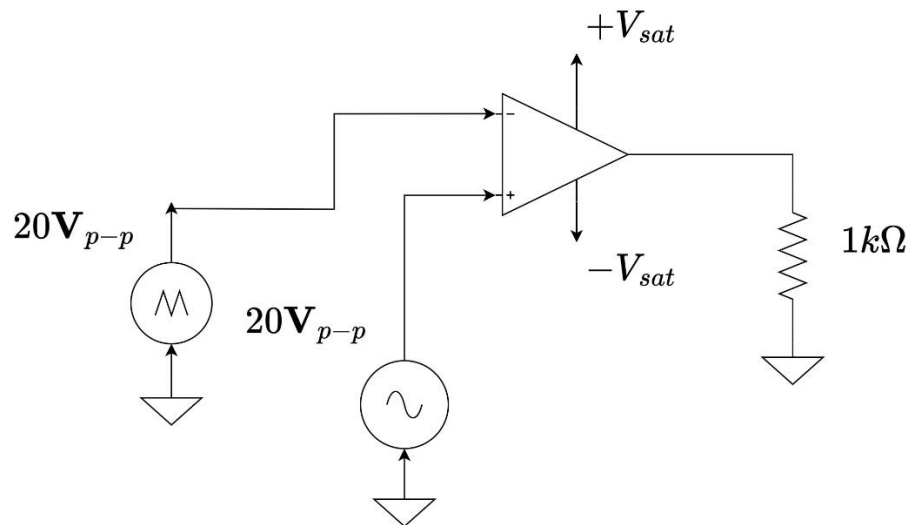


Fig: Waveform of 20 Vp-p source

2. Now a triangular wave is replaced instead of the DC source and the two waveforms are given below together [The red curve is the sinusoidal source while the blue curve is the triangular source output] [CO2]:  
[Marks: 18]



[Hint: for a comparator:

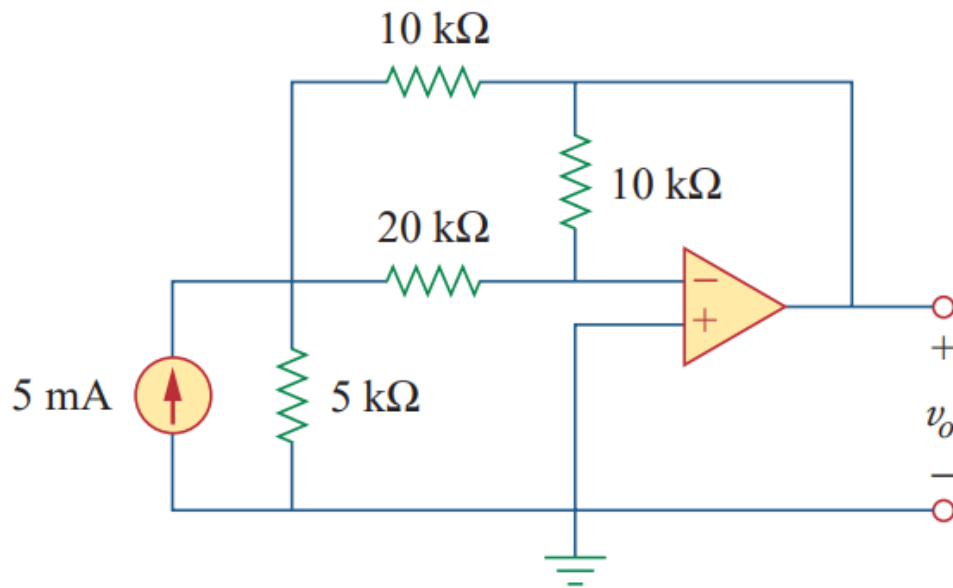
When  $v_2 > v_1$ : output is  $+V_{sat}$

When  $v_2 < v_1$ : output is  $-V_{sat}$

Where,  $v_2$  is the output of the noninverting terminal and  $v_1$  is the output of the inverting terminal]

3. Determine the output voltage,  $v_o$  [CO1]:

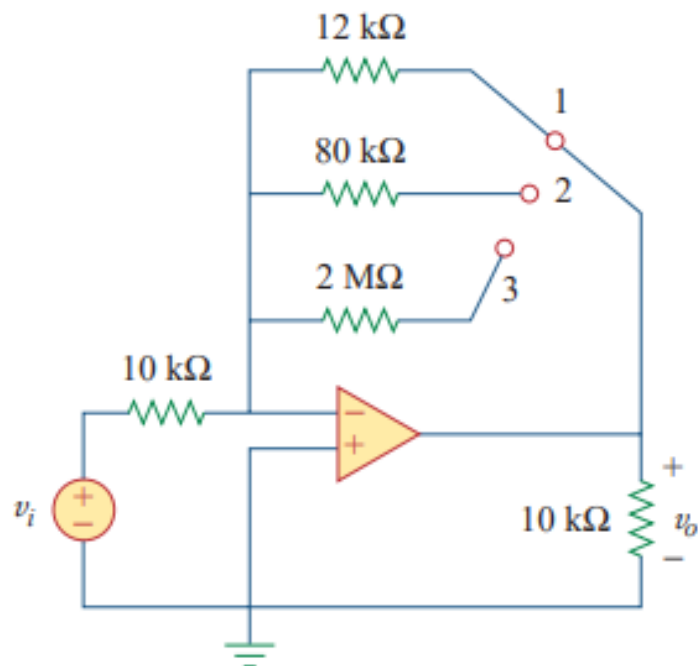
[Marks: 6]



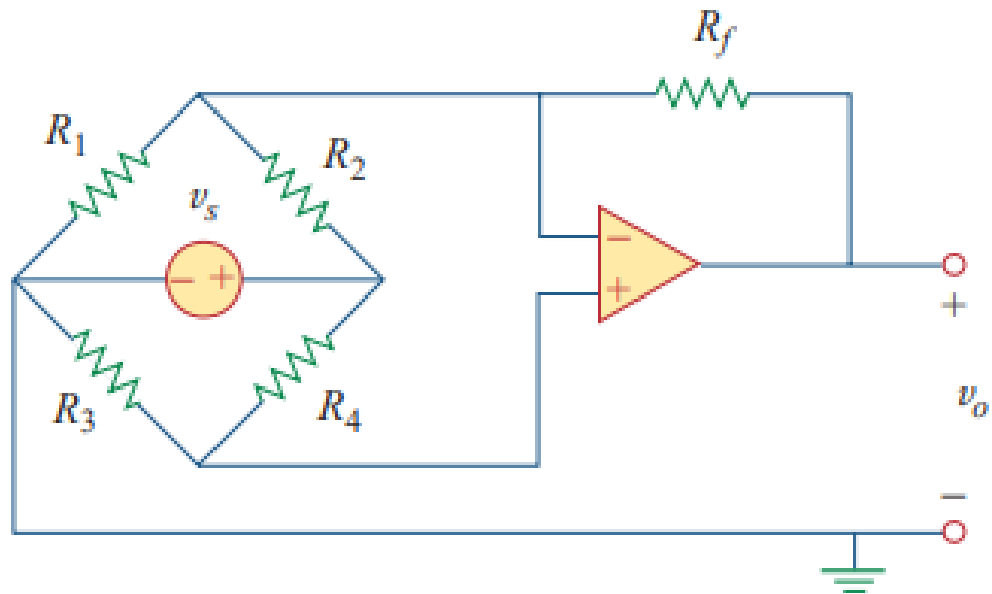
4. Calculate the gain,  $A = v_o/v_i$  for the following circuit when the switch is in [CO1]:

[Marks: 3x4 = 12]

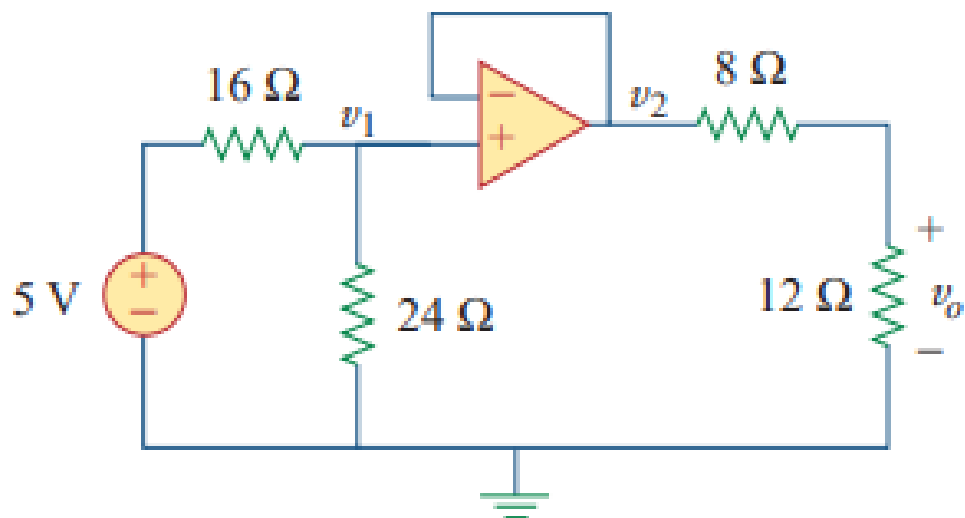
- (i) Position 1
- (ii) Position 2
- (iii) Position 3



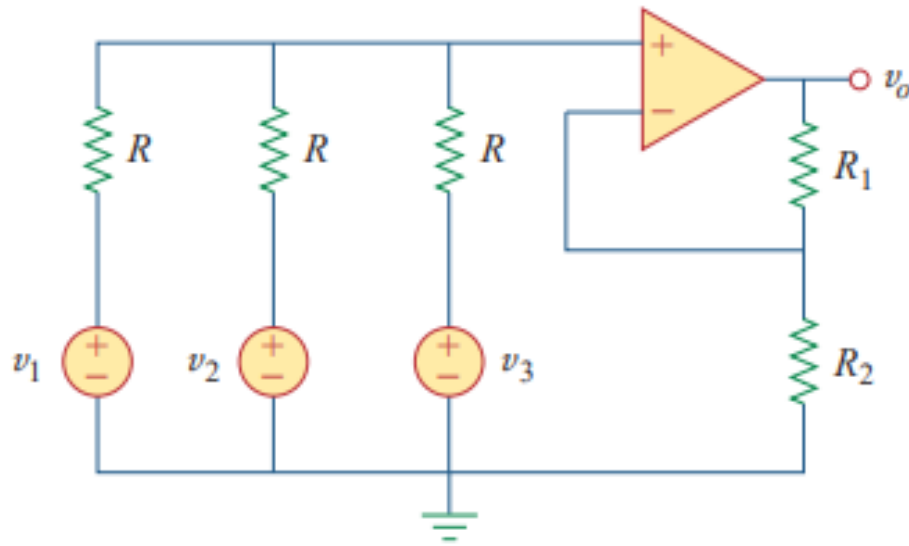
5. For the following circuit, find the expression for the gain,  $A = v_o/v_i$ . Then calculate the gain when  $R_1 = R_3 = 20$  kilo-ohms,  $R_2 = 50$  kilo-ohms and  $R_4 = 100$  kilo-ohms [CO1]: [Marks: 18]



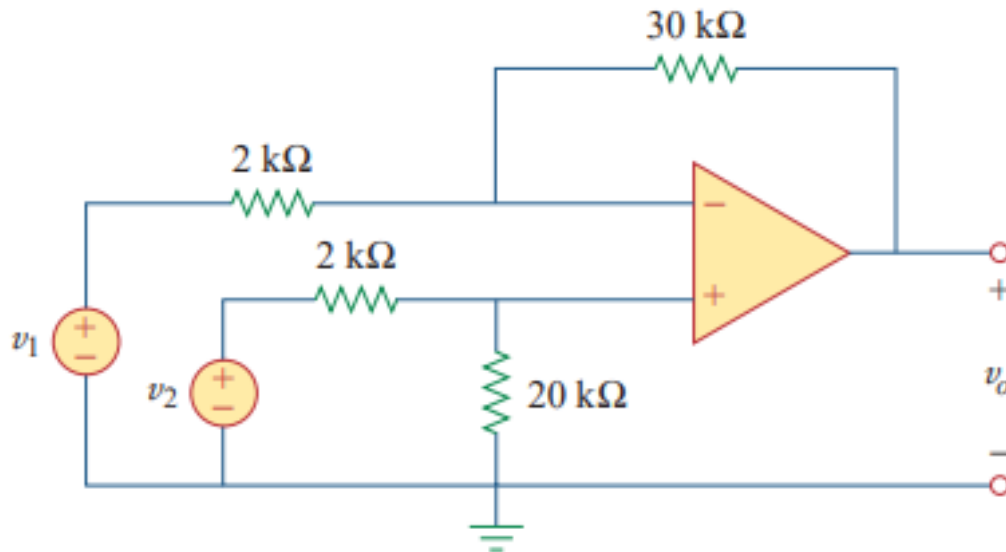
6. Find  $v_o$  in the op amp circuit [CO1]: [Marks: 6]



7. Find the output voltage  $v_o$  for the circuit. Assume  $v_1 = 1\text{V}$ ,  $v_2 = 2\text{V}$ ,  $v_3 = 3\text{V}$ . and  $R = 1\text{kilo-ohm}$ ,  $R_1 = 10\text{ kilo-ohm}$  and  $R_2 = 20\text{ kilo-ohm}$ . [CO1] [Marks: 16]



8. Find the output voltage,  $v_o$  for the following circuit. Given that,  $v_1 = 1\text{V}$  and  $v_2 = 2\text{V}$ . [CO1] [Marks: 14]



[Hints for solving op-amp circuits with closed loop feedback:

Make the assumptions as shown in the class:

- Current through the op amp input terminals are zero
- Voltage of the two input terminals are assumed to be equal

Best of Luck!]