LV target: vreg , base: reg , off: imm

```
Behavior:
- addr = base + offset
- target = [addr + 511: addr]
```

SV base: reg , off: imm , target: vreg

```
Behavior:
- addr = base + offset
- [addr + 511: addr] = target
```

VADD dst: vreg , a: vreg , b: vreg

```
Behavior:
- FOR j = 0 to 15
-     i = j * 32
-     dst[i + 31: i] =a[i + 31: i] + b[i + 31: i]
- ENDFOR
```

LSRV dst: vreg , src: vreg , shift: imm

```
Behavior:
- FOR j = 0 to 15
-     i = j * 32
-     IF shift > 31
-         dst[i + 31: i] = 0
-     ELSE
-         dst[i + 31: i] = ZeroExt(src[i + 31: i] >> shift)
-     ENDIF
- ENDFOR
```

VPERM dst: vreg , a: vreg , idx: vreg

```
Behavior:
- FOR j = 0 to 15
-     i = j * 32
-     id = idx[i + 3: i] * 32
-     dst[i + 31: i] = a[id + 31: id]
- ENDFOR
```