

LBUI dst: wreg , base: wreg , idx: wreg

Behavior:

- addr = base + idx
- target = [addr + 7: addr]

FMLA add: wreg , lut_base: wreg , idx: wreg , shift: imm

Behavior:

- index = idx << shift[4: 0]
- addr = lut_base + index
- lut = [addr + 31: addr]
- add = add + lut

LBSI dst: wreg , base: wreg , idx: wreg

Behavior:

- addr = base + idx
- target[31] = [addr + 7]
- target[6: 0] = [addr + 6: addr]

FMAL add_base: wreg , idx: wreg , shift: imm , lut: wreg

Behavior:

- index = idx << shift[4: 0]
- addr = add_base + index
- add = [addr + 31: addr]
- [addr] = add + lut

LHUI dst: wreg , base: wreg , idx: wreg , shift: imm

Behavior:

- offset = idx << shift[4: 0]
- addr = base + offset
- target = [addr + 15: addr]

LWI dst: wreg , base: wreg , idx: wreg , shift: imm

Behavior:

- offset = idx << shift[4: 0]
- addr = base + offset
- target = [addr + 31: addr]

LHSI dst: wreg , base: wreg , idx: wreg , shift: imm

Behavior:

- offset = idx << shift[4: 0]
- addr = base + offset
- target[31] = [addr + 15]
- target[14: 0] = [addr + 14: addr]

LDI dst: dreg , base: wreg , idx: wreg , shift: imm

Behavior:

- offset = idx << shift[4: 0]
- addr = base + offset
- target = [addr + 63: addr]