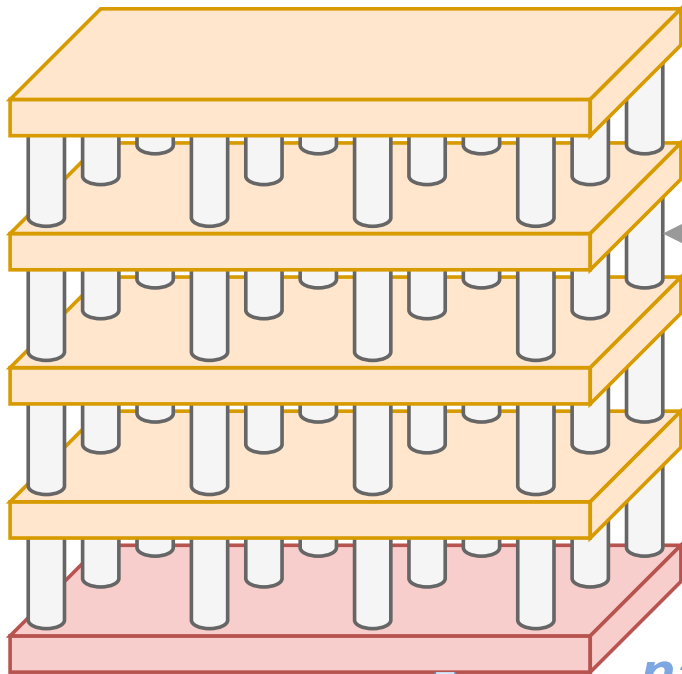


DRAM Layers



wide channel with
Through-Silicon Vias

Logic Layer

narrow
Memory Channel

CPU

