FAISAL SAEED

ELECTRICAL ENGINEER

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- Meticulous professional possesses excellent reporting skills. Experienced in multiple phases of professional development and equipped with an impetuous passion for discovering and devising ways to excel in evolving industries.
- Credible instrumentation and control engineer with extensive knowledge of designing, developing, installing, and managing engineering systems, machinery, and processes.

EXPERIENCE

Graduate Assistant

GHULAM ISHAQ KHAN INSTITUTE OF ENGINEERING SCIENCES & TECHNOLOGY

Jan 2020 - Present | Topi, Swabi

- Research into the Optimization of Laser-Induced Graphene (LIG) system parameters for printed electronic devices.
- The project on the Django framework is being worked on to create Back-end websites.
- The future objectives involve dealing with the integrated projects of Computer Vision, Website Development, and IoT.

Electronics/Electrical Trainee

INTERNATIONAL TEXTILE LIMITED

Jun 2017 – April 2018 | Karachi, Pakistan

- To be a team player engaged in the load shed control and industrial management system.
- It was being a part of the team in the process of controlling temperature and humidity in the weaving area.
- It had a group participant with all electrical power sections on installing, activating, and distributing the electrical power of the 2 MW Jenbacher engine.

2 Years on Job Training as Avionics Apprentice

PAKISTAN INTERNATIONAL AIRLINES

Jan 2015- Jan 2017 | Karachi, Pakistan`

- In the Electrical & Avionics Lab, I became familiarized with digital technology and electronic instrument systems
- A320's electrical power and avionics systems were comprehended.

PROJECTS

- Final Year Project: Delta PLC was utilized in an automatic filling plant (Control Parameters: Temperature and Volume).
- Auto Star Delta Panel was designed to manage massive pumps which move hot water out from the piston engine to the cooling towers and conversely.
- Designed the logic hardware for a conveyer belt system to carry items from one place to another.
- Multi-axis Numerically controlled Diode Laser programmed with Arduino Microcontroller and TB6560 drivers for graphene production on polyimide for foldable sensing devices.

EDUCATION

MS in Electrical Engineering

GIKI ENGINEERING SCIENCES & TECHNOLOGY

Jan 2020 - Present

- Completed academic coursework.
- Being the Lab Engineer, I have efficiently accomplished Instrumentation and Control labs.
- Presently undergoing Research.

B.E. in Industrial Electronics

NED UET Karachi (IIEE-2016)

TRAININGS & CERTIFICATIONS

Chip Design Verification Essentials

NUST (SINES)

- Verilog, System Verilog (SV), Data Types of SV.
- OOPs in SV, Interface, Layered Testbench.
- Implementation of Layered Testbench on AHB-Lite slave with defined constraints and functional coverages.
- UVM Basics using Cadence Xcelium Tools through Linux.

System Verilog for Verification Part-1: Fundamentals (Completed)

UDEMY

System Verilog for Verification Part-2: Projects (In-Progress)

UDEMY

Implementing Layered Test Bench on following projects

- FIFO
- SPI
- UART
- I2C
- APB_RAM

Learning UVM Testbench with Xilinx Vivado (In-Progress)

UDEMY