Digital Logic Trainer

LEONCIO, TIMOTHY D.

MACAPAGAL, FAITHLIX MARK M.

MALIWAT, JEROME R.
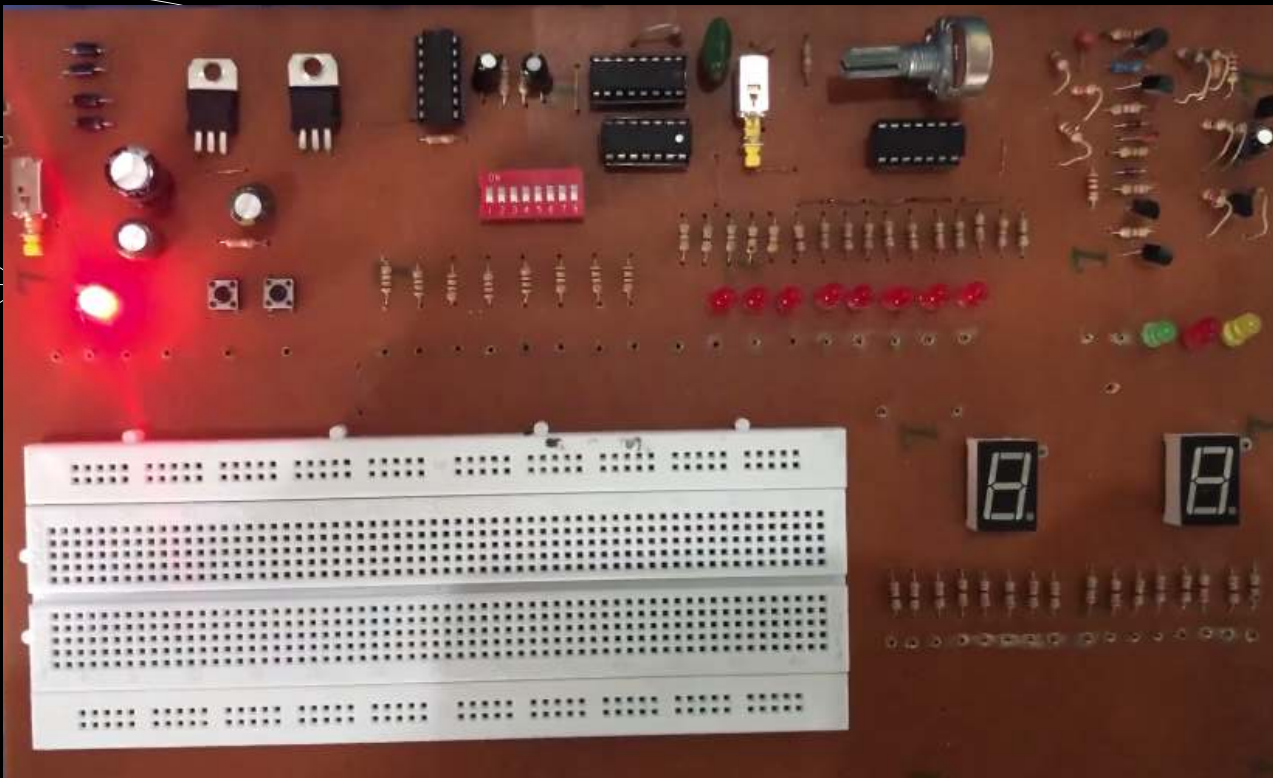
MIRANDA, AYRA G.

OLALIA JHONLY A.

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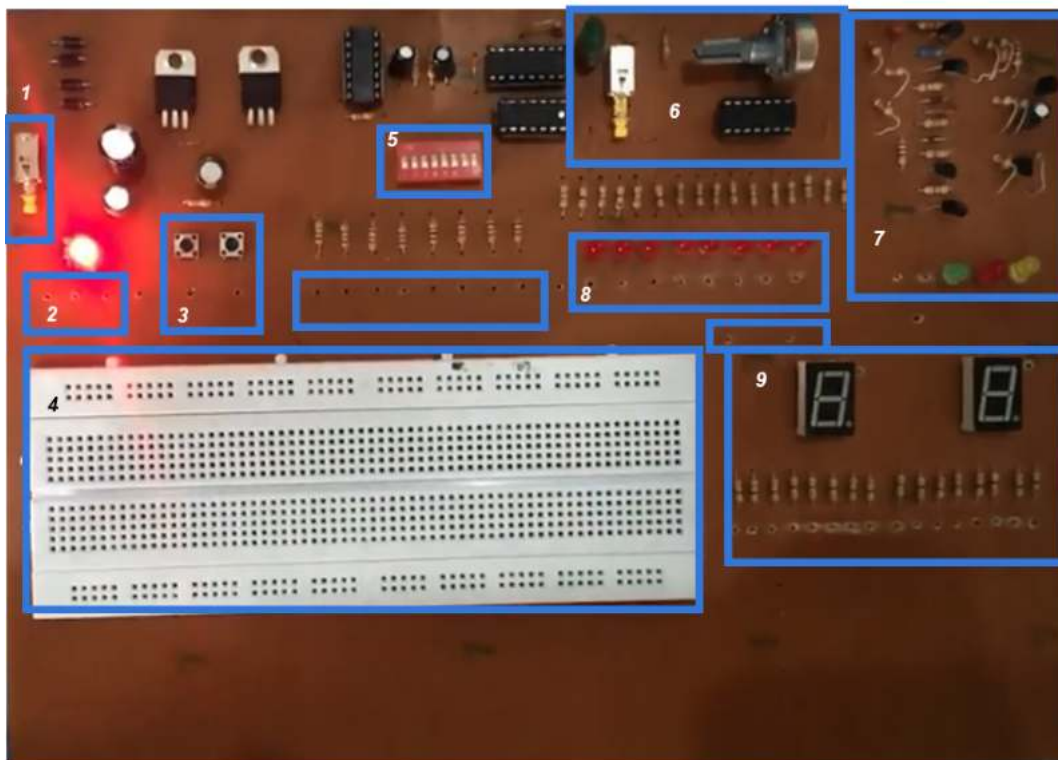# DESIGN MAIN MANUAL

1. **POWER SWITCH**
2. **DC OUTPUT**
3. **PULSE SWITCH**
4. **BREADBOARD**
5. **DATA SWITCH (LOGIC SWITCH)**
6. **CLOCK GENERATOR**
7. **LOGIC PROBE**
8. **DATA INPUT**
9. **EXTENSION 5V DC OUTPUT**
10. **SEVEN SEGMENT DIGITAL DISPLAY**

Circuit Designing

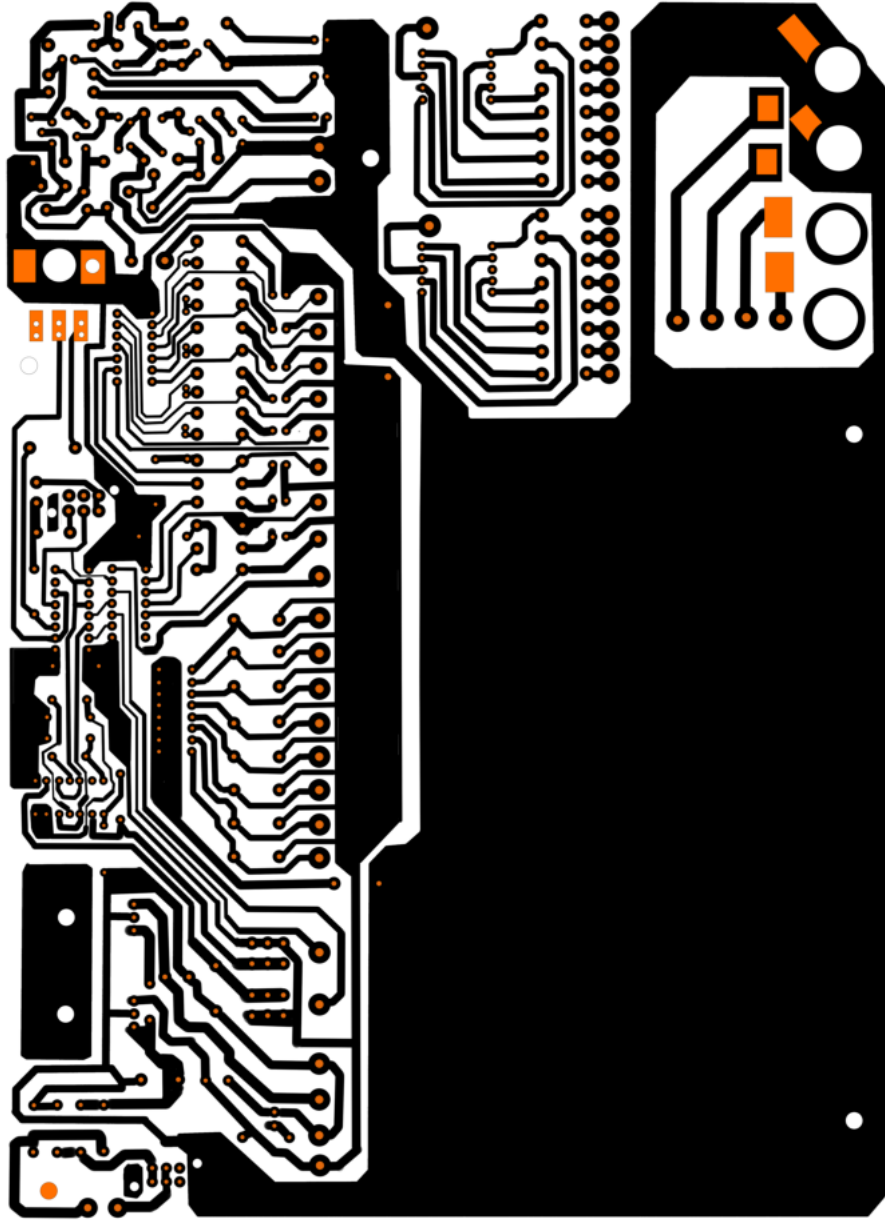Fabrication of the Digital Logic Trainer using computer software.

Fabrication of the Digital Logic Trainer using computer software.
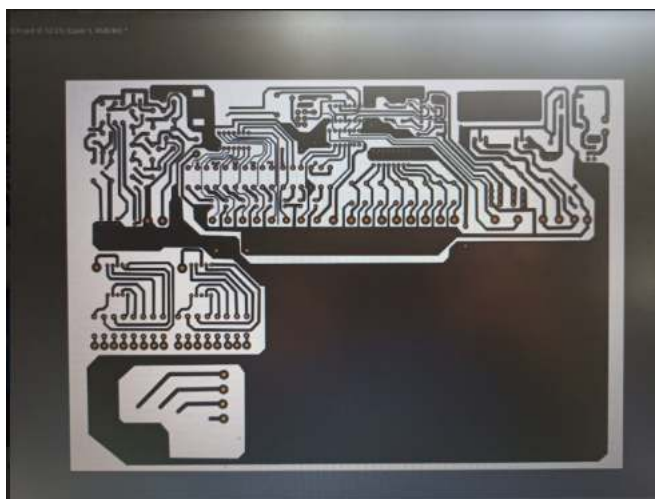
Circuit Design

Development Process
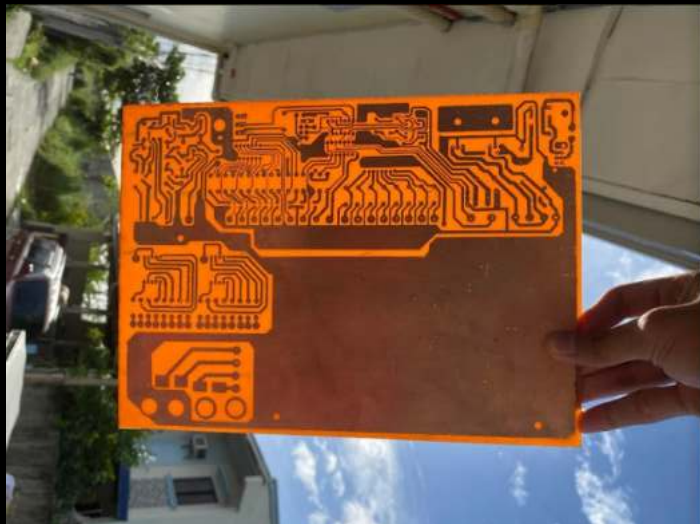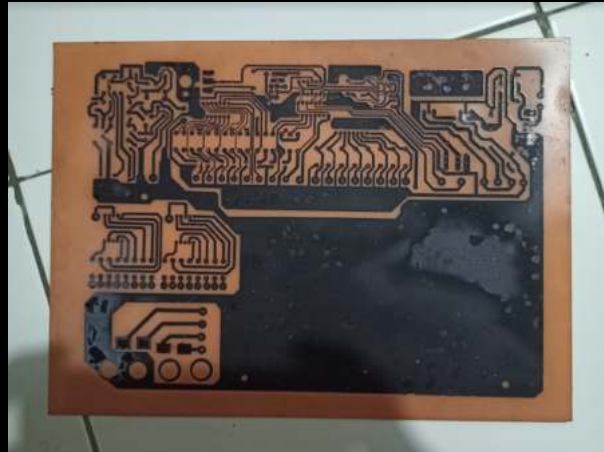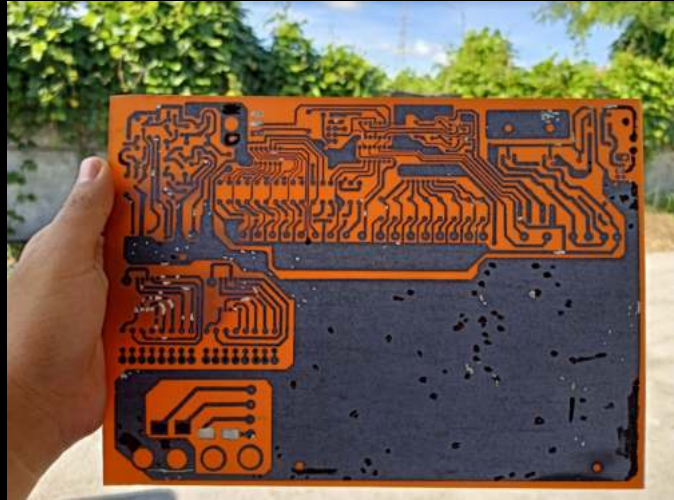
01
MATERIAL FINDING

Development Process

02

PCB ETCHING

Development Process

03

PCB
DRILLING

Development Process

04

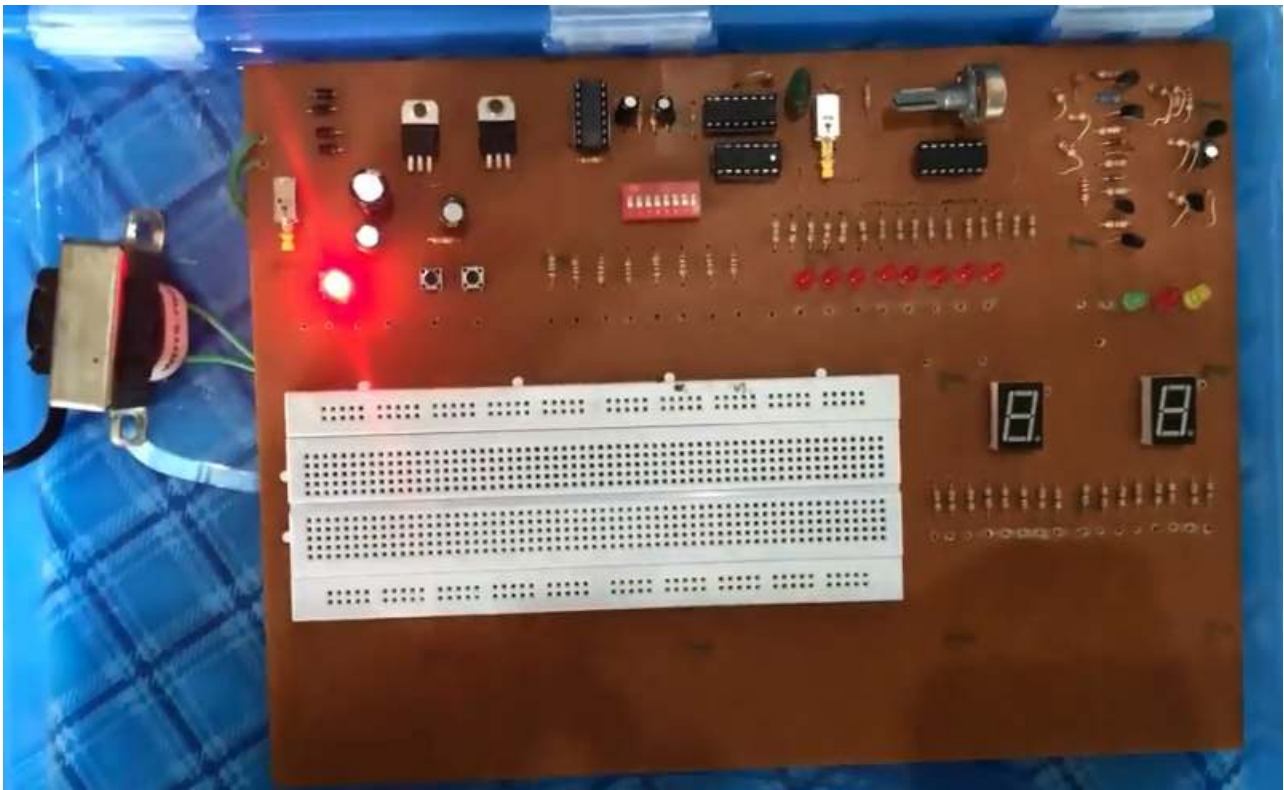### SOLDERING

DIGITAL LOGIC TRAINER
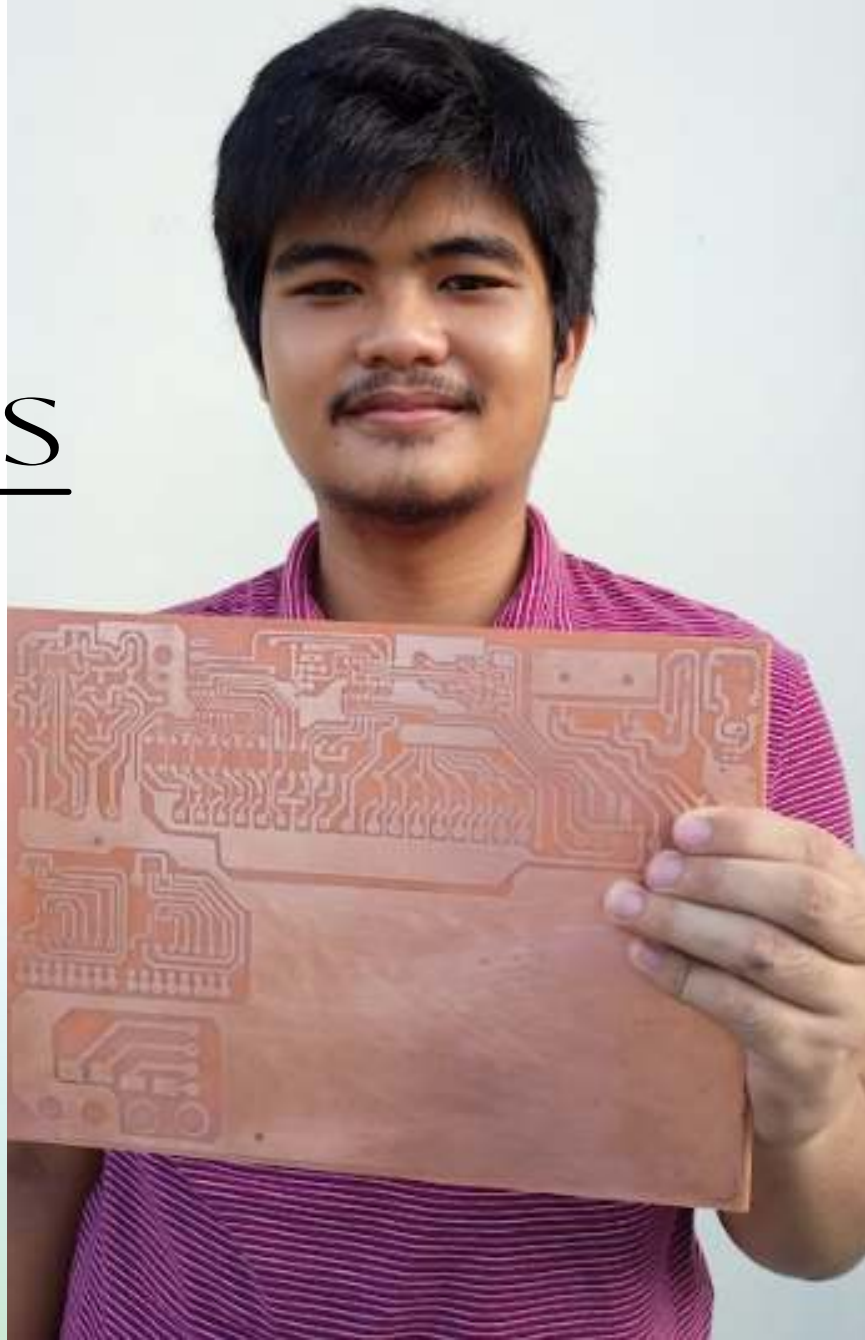
DIGITAL LOGIC TRAINER

CONTRIBUTIONS

LEONCIO, TIMOTHY D.

SOLDERING
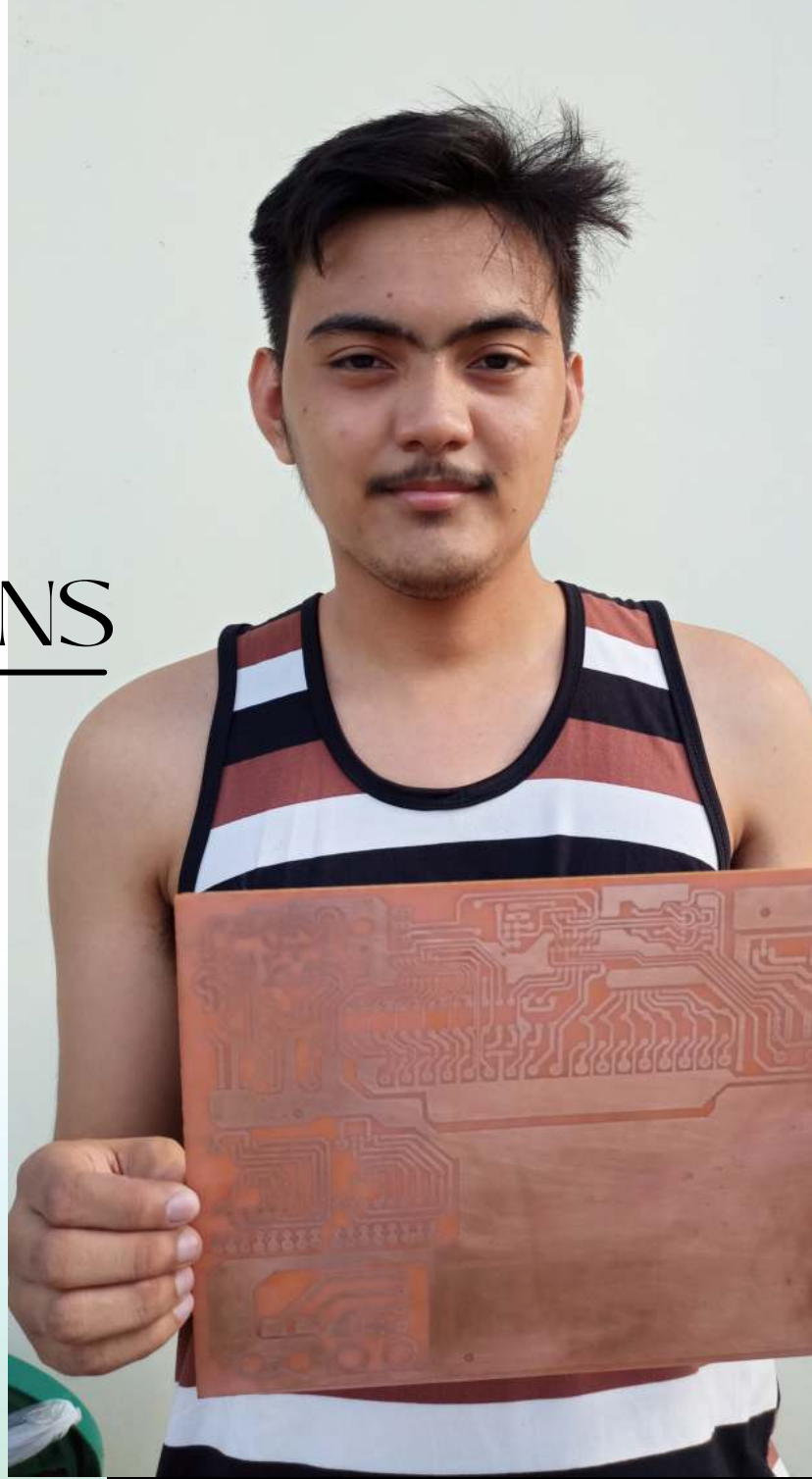TROUBLESHOOTING
PCB DRILLING
PCB ETCHING

CONTRIBUTIONS

MACAPAGAL, FAITHLIX MARK M.

CIRCUIT DESIGN DESIGNER
MATERIAL FINDER
TROUBLESHOOTING
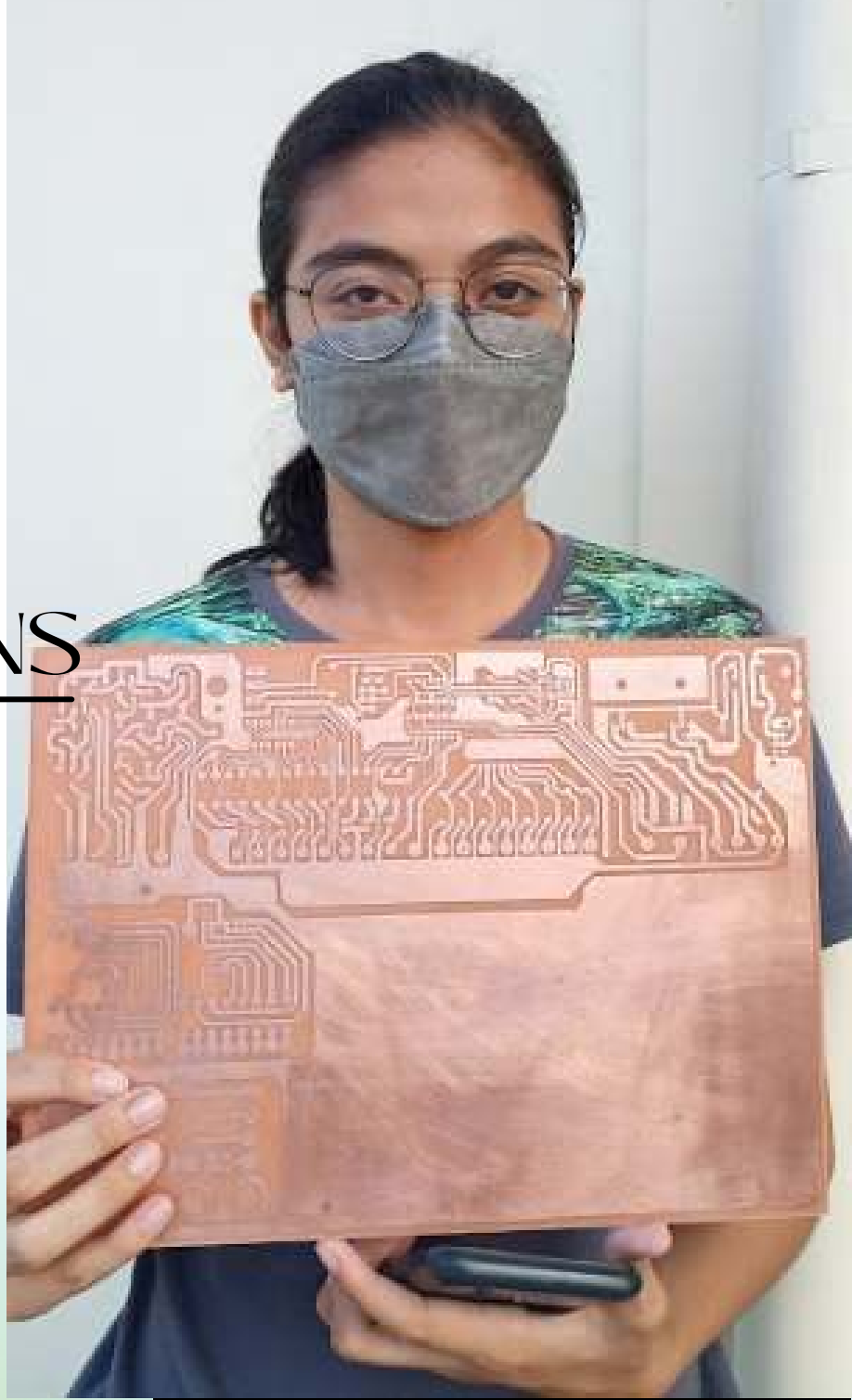PCB DRILLING
FILE EDITOR

CONTRIBUTIONS

MALIWAT, JEROME R.

CIRCUIT DESIGN DESIGNER
MATERIAL FINDER
TROUBLESHOOTING
HEAD OF MATERIAL FINDING
PCB ETCHING

CONTRIBUTIONS

MIRANDA, AYRA G.

MATERIAL FINDER
PCB ETCHING
RECORD KEEPER
OUTPUT FILLER

CONTRIBUTIONS

OLALIA, JHONLY A.

MATERIAL FINDER
PCB ETCHING
RECORD KEEPER
TREASURER
PCB DRILLING

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LANCEGO ELECTRONICS

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GROUP 6 DIGITAL LOGIC TRAINER