

Faithlix Mark Macapagal

ELECTRONICS ENGINEER | ELECTRONICS TECHNICIAN

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ABOUT

I graduated with the degree, Bachelor of Science in Electronics Engineering and a passion for creative problem-solving. During my academic journey, I developed strong skills in programming, electronics engineering, electronic designs, and data analysis through coursework and hands-on projects. During my final year, I led a team project that involved studying an algorithm that can cluster multipath components (Application in Communication Engineering), which resulted in improvement of results and successful thesis defense. I'm eager to learn and apply my knowledge and enthusiasm in a professional setting. I'm actively seeking entry-level opportunities with the role in software engineering.

EXPERIENCE

Pampanga Rural Electric Service Cooperative, Inc. | Internship | July 2023

- Electronics Engineering Intern
- Assist on customer's account using company's database and assist on the IT department of the cooperative (debugging, basic database programming, and computer hardware servicing).

RELEVANT PROJECTS

k-Deep Autoencoder-Based Clustering of Channel Model Generated Multipath Components

- Modified adapted Machine learning Algorithm for application on one of the major problems in communications which are the multipaths.
- Used Python language efficiently in modifying and coding of the algorithm which become a way on enhancing programming skills.
- Analyzed data using MATLAB statistical tools and graphical representations of data.
- Repo: <https://github.com/FaithlixMark/k-DAE-C2CM-and-IMT-2020-Mastercode>

Data Cleaning Project using MySQL

- Involves practice in cleaning data from a dataset to improve the readability, remove redundancy, and updating data ready for data analysis using MySQL.
- Removal of duplicates, standardized the data, analysis of null values or blank values, and removal of unrelated columns.
- Repo: <https://github.com/FaithlixMark/Data-Cleaning-Project-MySQL>

Data Analysis Project Using MySQL

- This project is the continuation of Data Cleaning Project. This focuses on exploration and analyzing the cleaned data from past projects. It highlights subquery, CTEs, and advanced applications of MySQL.
- Repo: https://github.com/FaithlixMark/Data_Analysis_Project_SQL

Classroom Occupancy Monitoring System Using Wireless Communications in DHVSU Electronics Engineering Department

- Made a classroom monitoring system using RFID technology which provides real-time status if a specific room is occupied or not.
- Developed skills in programming microcontrollers for embedded systems using C++ language.
- Enhances skills on Java script in programming the system's GUI.

Logic Trainer

- Adapted a design of a logic trainer for a university project.
- Enhances Electronics Engineering skills (soldering, circuit design/debugging, and logic circuits)
- Became the team leader of the project

SKILLS

Programming Languages: Python, C++, MATLAB

Languages: English and Filipino
Interests: AI Models, Software Development, Full stack development, Communication Engineering, Electronics Engineering, Data Analysis, Quality Assurance.
Technical: MySQL, Object-oriented programming, Git, Microsoft Office, Mathematics

LICENSE

- **PRC Electronics Engineer License** (October 2024)
- **PRC Electronics Technician License** (October 2024)

EDUCATION

Don Honorio Ventura State University| *Bachelor of Science in Electronics Engineering* | 2020-2024

- Magna Cum Laude
- CHED Scholar
- Board of Director of Institute of Electronics Engineering of the Philippines (IECEP) (2021-2024)
- Regional 1st Runner up Quizzer of Institute of Electronics Engineering of the Philippines (IECEP) (2022-2023)
- Regional Champion Quizzer of Institute of Electronics Engineering of the Philippines (IECEP) (2023-2024)
- National 5th Placer Quizzer of Institute of Electronics Engineering of the Philippines (IECEP) (2023-2024)
