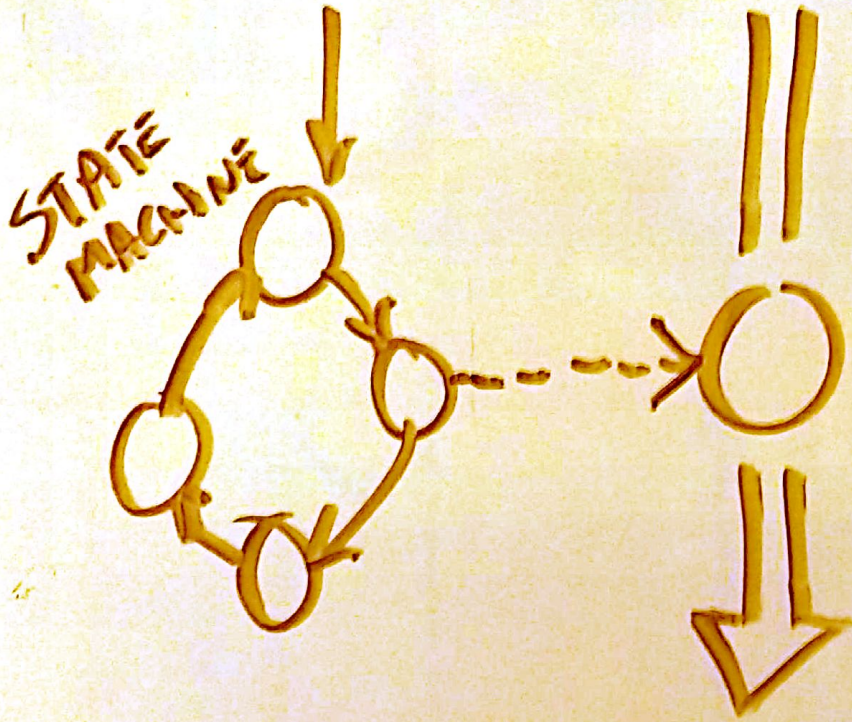


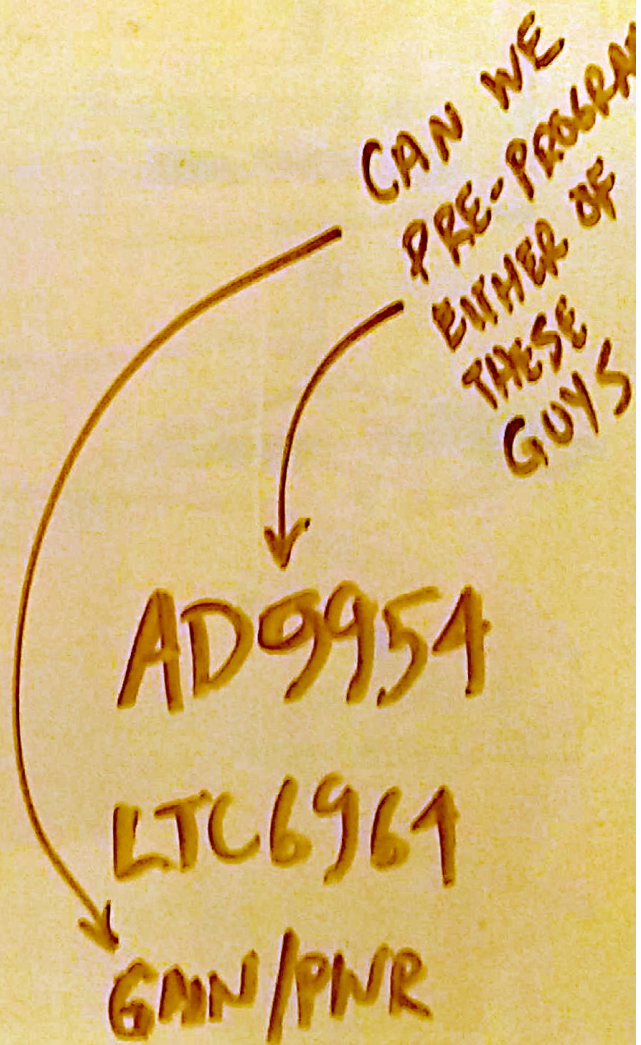


S4



TIME BASE

LATENCY ASSOC  
W/ EACH SPI TARGET  
"SCHEDULING" ?



ONLY 1  
SPI BUS?