COMP110: Principles of Computing
4: Logic and memory

# Learning outcomes

- Distinguish the basic types of logic gate
- ▶ Use logic gates to build simple circuits
- ► Explain how computer memory works

# Logic gates

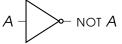
# Boolean logic

- ▶ Works with two values: True and False
- ► Foundation of the **digital computer**: represented in circuits as **on** and **off**
- ► Representing as 1 and 0 leads to **binary notation**
- ► One boolean value = one **bit** of information
- Programmers use boolean logic for conditions in if and while statements

### Not

NOT A is True if and only if A is False

Α	NOT A
FALSE	TRUE
TRUE	FALSE



### And

#### A AND B is True if and only if **both** A **and** B are True

Α	В	A and $B$
FALSE	FALSE	FALSE
FALSE	TRUE	FALSE
TRUE	FALSE	FALSE
TRUE	TRUE	TRUE



#### Or

# A OR B is TRUE if and only if either A or B, or both, are TRUE

Α	В	A and $B$
FALSE	FALSE	FALSE
FALSE	TRUE	True
TRUE	FALSE	TRUE
TRUE	TRUE	TRUE



What is the value of

A AND (B OR C)

when

A = TRUE

 $B = \mathsf{FALSE}$ 

 $C = \mathsf{TRUE}$ 

What is the value of

(NOT 
$$A$$
) AND ( $B \cap C$ )

when

 $A = \mathsf{TRUE}$ 

 $B = \mathsf{FALSE}$ 

 $C = \mathsf{TRUE}$ 

For what values of A, B, C, D is

A and not B and not  $(C ext{ or } D) = T$ rue

?

What is the value of

A or not A

7

What is the value of

A and not A

?

What is the value of

A or A

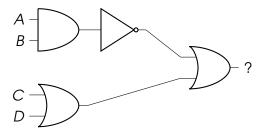
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What is the value of

 $\boldsymbol{A}$  and  $\boldsymbol{A}$ 

?

What expression is equivalent to this circuit?



# Writing logical operations

Operation	Python	C family	Mathematics
NOT A	not a	!a	$\neg A$ or $\overline{A}$
A and $B$	a <b>and</b> b	a && b	$A \wedge B$
A or $B$	a <b>or</b> b	a    b	$A \lor B$

Other operators can be expressed by combining these

#### Exclusive Or

# A XOR B is TRUE if and only if either A or B, but not both, are TRUE

Α	B	A and $B$
FALSE	FALSE	FALSE
FALSE	TRUE	TRUE
TRUE	FALSE	TRUE
TRUE	TRUE	FALSE

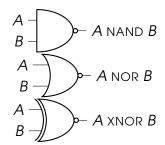


How can  $A \times B$  be written using the operations AND, OR, NOT?

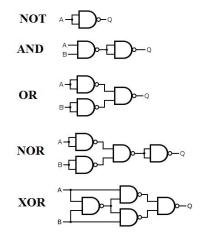
# Negative gates

NAND , NOR , XNOR are the **negations** of AND , OR , XOR

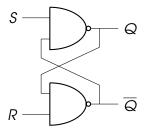
A NAND B = NOT (A AND B)A NOR B = NOT (A OR B)A XNOR B = NOT (A XOR B)



# Any logic gate can be constructed from NAND gates



#### What does this circuit do?



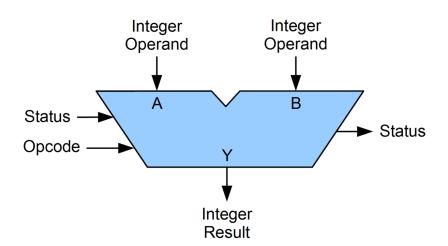
- ► This is called a NAND latch
- ▶ It "remembers" a single boolean value
- Put a few billion of these together (along with some control circuitry) and you've got memory!

# NAND gates

- All arithmetic and logic operations, as well as memory, can be built from NAND gates
- So an entire computer can be built just from NAND gates!
- ▶ Play the game: http://nandgame.com
- NAND gate circuits are Turing complete
- ► The same is true of NOR gates

# Arithmetic Logic Unit

# Arithmetic Logic Unit



# Arithmetic Logic Unit

- Important part of the CPU
- ► Inputs:
  - Operand words A, B
  - Opcode
  - Status bits
- ► Outputs:
  - Result word Y
  - Status bits
- Opcode specifies how Y is calculated based on A and B

# **ALU** operations

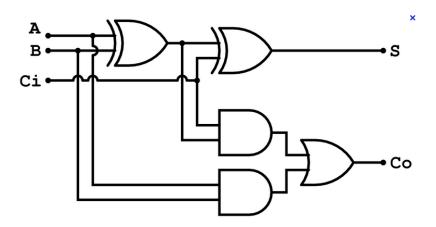
#### Typically include:

- Add with carry
- Subtract with borrow
- ▶ Negate (2's complement)
- ► Increment, decrement
- ► Bitwise AND, OR, NOT, ...
- ► Bit shifts

# Adding 3 bits

Α	В	С	A+B+C
0	0	0	00
0	0	1	01
0	1	0	01
0	1	1	10
1	0	0	01
1	0	1	10
1	1	0	10
1	1	1	11

# 1-bit adder

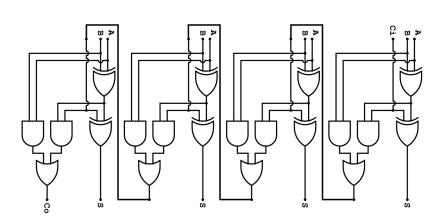


### How does the 1-bit adder work?

#### Exercise:

- ▶ Write down the boolean expressions for S and Co
- Draw a truth table for these
- Compare the truth table to the addition table on a previous slide

# *n*-bit adder



# Exercise Sheet ii

Due **next Tuesday!**