



教育部獎狀

Ministry of Education

2021 Integrated Circuits Computer Aided Design Contest Award

國立清華大學 資訊工程學系 樊明勝

作品: Verilog Simulation Optimization via Instruction Reduction

榮獲2021國際積體電路電腦輔助設計(CAD)軟體製作競賽 定題組 特優

特頒此狀 以資鼓勵

This award is presented to Ming-Bang Fan from the Department of Computer Science at National Tsing Hua University for winning First Place for Outstanding Excellence in the Verilog Simulation Optimization via Instruction Reduction division of the Integrated Circuits Computer Aided Design Contest at the 2021 International Conference on Computer Aided Design.

部長潘文忠

Wen Chung Pan

Wen-Chung Pan
Minister of Education
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中華民國

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年 12 月 19 日