

## ECE 3710 Lab 0 – Fall 2017

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Due Date: Week of September 11 at the beginning of your lab section (20 points)

### About Lab Reports

Lab reports are used for two purposes. One is to show your employer/grader what you have done. The other purpose of a lab report is to serve as a reference manual in your future.

1. The lab description should be used as a template for the lab report
2. Write down what steps were needed to perform a task.
3. Take screenshots or pictures of procedures or results.
4. Describe issues and solutions to them.
5. Any other necessary knowledge – this is not an all-inclusive list of required items. You should fill the lab report as full as necessary to show design decisions and procedures.
6. Sections in the lab report should include: Objectives, Overview, Preparation, Procedures and Results, and Conclusion.

### Objectives

The student should become familiar with the functions and capability of the Tektronix MDO3014 Mixed Domain Oscilloscope and the Tiva C Series TM4C123GH6PM Microcontroller Data Sheet.

### Overview

This lab serves as an introduction to logic analyzers (used for debugging and verification) and provides practice in reading datasheets. Proficiency in reading datasheets/technical literature and utilizing the information provided therein is an essential skill for an engineer to possess.

### Preparation

1. Download the TM4C123GH6PM data sheet from the lab supplements page on the course wiki.
2. Download MDO3000 Series Mixed Domain Oscilloscopes User Manual from the lab supplements page on the course wiki.
3. Review the “XYZs of Oscilloscopes” primer available on the lab supplements page on the course wiki.
4. Purchase a lab kit from the ECE Store.

## Procedure

1. Using the TM4C123GH6PM data sheet, answer the following questions. TIP: To efficiently move through the data sheet, have the table of contents open on the left or right hand side of the PDF viewer.
  - a. A memory map is a convenient way to depict the memory addresses of registers and descriptions of registers.
    - i. Which section describes Cortex-M4's memory map?
    - ii. What table number indicates the memory map of Cortex-M4 processor?
  - b. Each Peripheral has a specific memory address, what is the base memory address of GPIO\_A (general input/output port A)?
  - c. Each peripheral has a number of registers that control its functionality. For a given peripheral, it's often easier to use the offset of a register with respect to the peripheral base address rather than the absolute address of the register. A peripheral's registers and their associated offsets are usually summarized in a register map.
    - i. Which section describes the General-Purpose Input / Output (GPIO) register map?
    - ii. What is the table number for the GPIO register map?
  - d. The register map table in the TI documentation contains the name, offset, and description of a register. Which register corresponds to the direction of a GPIO port? Write down the name of this register, its memory offset, and the absolute address for port A of this register.
  - e. Click the page number of the GPIODIR register on the register map table.
    - i. What binary value corresponds to an output?
    - ii. If pins 0, 4, and 7 were inputs, but the rest were outputs, what would the state of this register (in binary values)?
    - iii. What would be the state of this register using hexadecimal values?
  - f. Which timer is located at memory address 0x4003.2000?
  - g. Find the register map for General Purpose Timers. Which register is located at offset 0x068? Provide a brief description of this register.

- h. On this board, a few pins are wired together. Record the pins that are wired together. (Hint: Look in the Tiva™ C Series TM4C123G LaunchPad Evaluation Board User's Guide found on the Lab Supplements page)

## 2. Introduction to the MDO3014 Mixed Domain Oscilloscope

- a. Read through the following sections of the MDO3000 Series Mixed Domain Oscilloscopes User Manual:
  - i. Get Acquainted with the Instrument (pages 36—53)
  - ii. Appendix C: P6316 General-Purpose Logic Probe Information (pages 203—207)
  - iii. Setting Up Digital Channels to Using MagniVu (pages 79—82)
  - iv. Trigger Setup (pages 87—93)
  - v. Analyze Waveform or Trace Data (pages 122—130)
  - vi. Save and Recall Information (pages 160—166)
- b. Answer the following questions:
  - i. Name three potential uses for the logic analyzer.
  - ii. A properly configured trigger will remove guesswork and chance from capturing the output behavior of a signal.
    - 1. Write down how you would configure the trigger to capture on a falling edge.
    - 2. Write down how you would configure the trigger to capture on a rising edge.
  - iii. How do you change the sample period of the logic analyzer?
  - iv. Change the sample period to 200ns.

## 3. Set Up the Board

- a. Open Keil uVision.
- b. Download the Lab 0 zip file, unzip, and open SP.uvproj under 'blinky' directory.
- c. Build the project. Project -> Build.
- d. Plug in the microcontroller (use the debug port). There is a switch near the top left that will turn it on.
- e. Flash the project to the board. Flash -> Download.
- f. Press the RESET button located on the microcontroller.

- g. The program is now running.
    - i. What is the program doing?
    - ii. Press SW1 and observe the behavior of the microcontroller.
    - iii. Now press SW2 and observe. Estimate the rate at which the lights are flashing.
  - h. Use the logic analyzer and triggers to measure the rate of the flashing light. DO NOT connect the logic analyzer without talking with the TA first. Your board will burn out. Hint: The light power line is connected to pin F1.
    - i. In order to connect the logic analyzer to your board, the Logic Probe must be connected.
    - ii. Change the logic analyzer settings (Hint, refer to the 'Setting Up Digital Channels' section of the scope user manual). Change the format so the analyzer only looks at pin 0 of the pod kit
    - iii. Use the logic analyzer to obtain the timing for SW1. (Hint: use falling edge trigger)
      - 1. Obtain screenshot of the logic analyzer timing verification.
      - 2. Draw the waveform.
      - 3. Record the amount of time between flicker states (Hint: try both an automatic measurement and the cursors [section 'Taking Manual Measurements with Cursors' pages 132–136])
    - iv. Use the logic analyzer to obtain the timing for SW2. (Hint: use rising edge trigger)
      - 1. Obtain screenshot of the logic analyzer timing verification.
4. Using the Analog Comparator of the TM4C123GH6PM Microcontroller
- a. The analog comparator will convert a voltage to a digital value. Read about these in chapter 19 of the datasheet.
  - b. Open the project in the 'comparator' folder.
  - c. The project contains code which will turn on a light when the voltage on analog comparator C1 (found in Table 19-1) rises above a certain level. What are the pin assignments for this configuration?
  - d. Chapter 19 gives information about the configuration of this peripheral. Which register stores the information about internal reference?

- e. Use the system viewer to Select Peripherals -> System Viewer -> COMP to show the analog comparator registers. Run the program and observe the configuration of the register found in step d.
- f. Using the information from the above register, determine the RNG value and the VREF value.
- g. Finally, determine the actual internal voltage reference which will activate the light.
- h. Test your answer with a voltage source. To avoid damage to your board, start the voltage at 0.