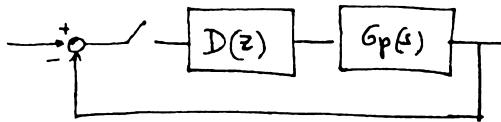


## Compensation (Section 8.3)

In some cases using a P controller (ie  $D(z) = k$ ) is not sufficient to achieve the performance specifications (sometimes even to stabilize the system)

In these cases we need to add one or more dynamic blocks to modify (hopefully improve) the behavior of the system. This process is called compensation.



22-141 50 SHEETS  
22-142 100 SHEETS  
22-144 200 SHEETS



Assume for the time being that we use only first order compensators. Then

$$D(z) = k \left( \frac{z - z_0}{z - z_p} \right)$$

depending on the relative location of the pole and zero of the compensator we get different effects

- Review of compensation for continuous time systems:

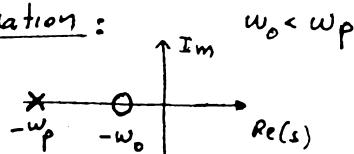
Recall from ECE 580 that for continuous time systems we have 2 basic types of compensation:

$$D(s) = A \left( \frac{1 + s/w_0}{1 + s/w_p} \right) \quad \text{where} \quad A = \text{DC gain}$$

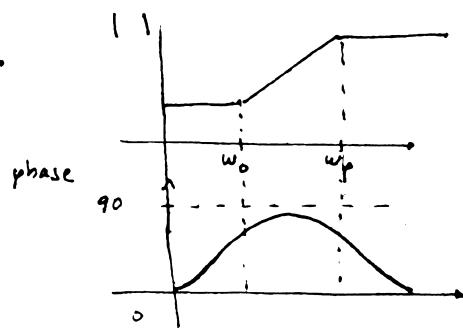
$w_0, w_p$  location of zero & pole respectively

- Phase lead compensation:

Root Locus diagram



Bode plot:



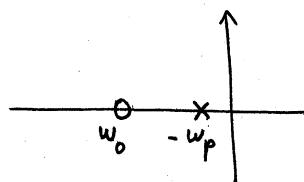
← adds phase to the system, hence the name phase lead.

## Effects of phase lead:

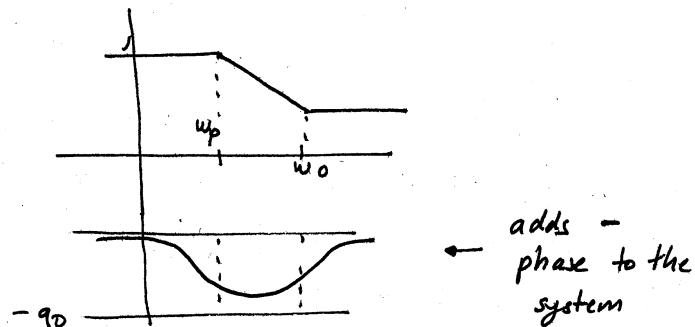
- Moves RL towards the left  $\Rightarrow \left\{ \begin{array}{l} \text{stabilizing} \\ \text{yields faster systems} \\ \text{improves transient} \end{array} \right.$

Potential drawback: increases high frequency gain

- Phase lag compensation:



$$w_0 > w_p$$



## Effects of phase lag:

Since it adds negative phase, it reduces the phase margin (equivalently, it moves the R.L. towards the right)

$\Rightarrow$  destabilizing effect

Phase lag: improves steady state but  
degrades stability

To minimize the destabilizing effect, usually  $w_p$  and  $w_0$  are chosen very small, i.e. the pole & zero are located both very close to the origin (but  $w_0 \approx 10 w_p$ ). Need to make sure that this does not introduce a very slow mode

- Use of phase lead & lag compensators:

- Use phase lead when the RL needs to be moved towards the left to improve stability and/or transient
- Use phase lag when it is necessary to improve steady state

Care must be exercised when using phase lag to keep the destabilizing effects to a minimum

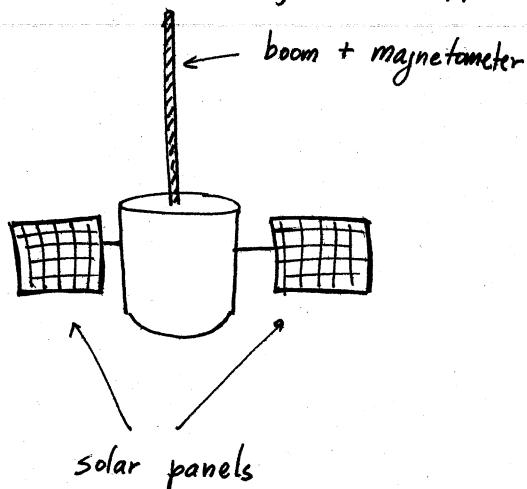
- If you need to improve both transient & steady state you can combine a phase lead and a phase lag stage, leading to a lead-lag compensator.
- Compensation of discrete time systems:

Q: How do we use these tools in discrete time systems?

A: (1) Use a bilinear transformation:  $z = \frac{1 + \frac{wT}{2}}{1 - \frac{wT}{2}}$  to go to the "w-plane" and carry out the design there  
(2) Alternative: derive the discrete time equivalent of phase lead and phase lag

- Example of compensation in the w-plane

Consider the problem of designing an attitude controller for a satellite with flexible appendages

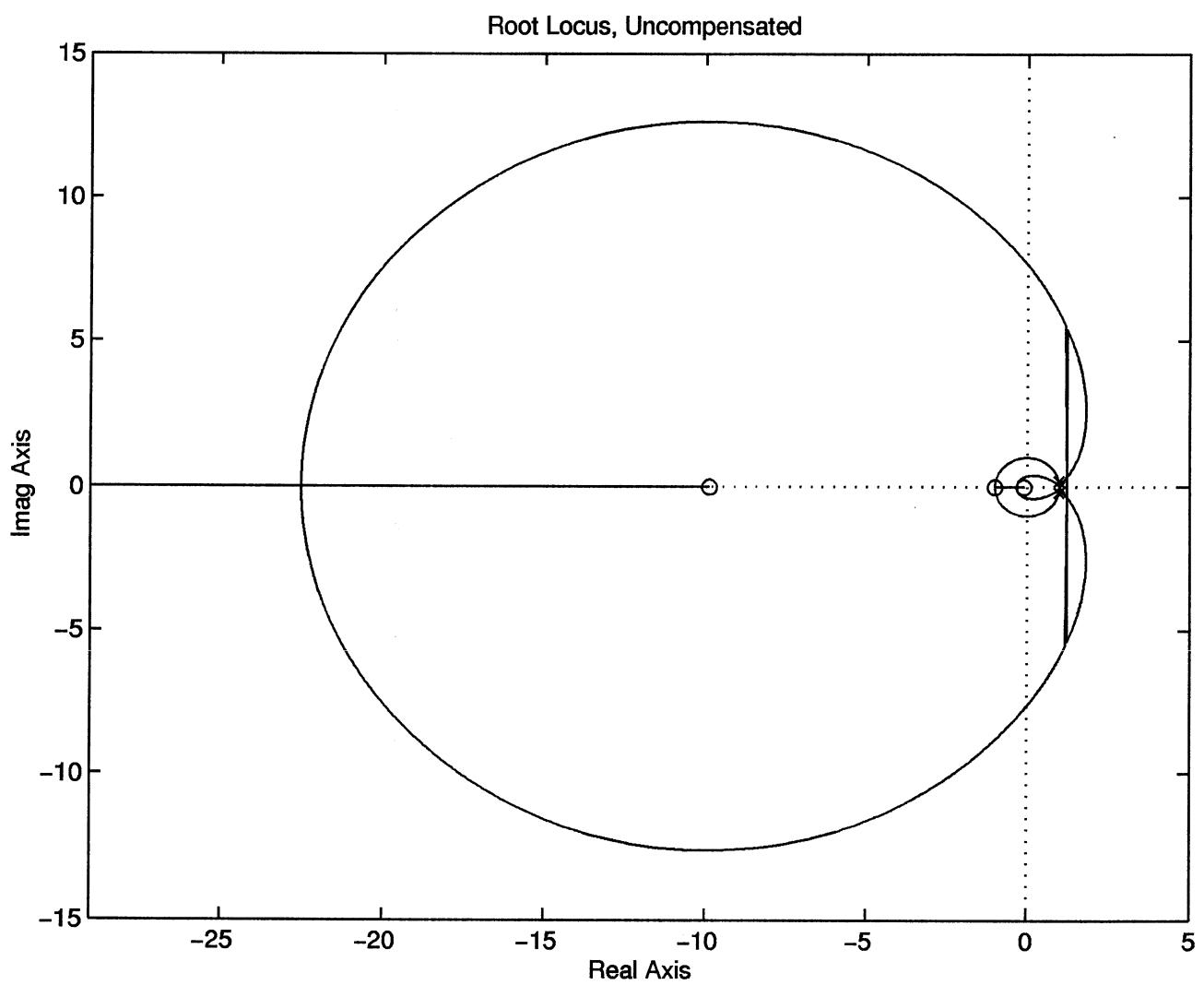


Due to the presence of the solar panels and the magnetometers the satellite cannot be modeled as a rigid body: we need to include at least the first flexible mode:

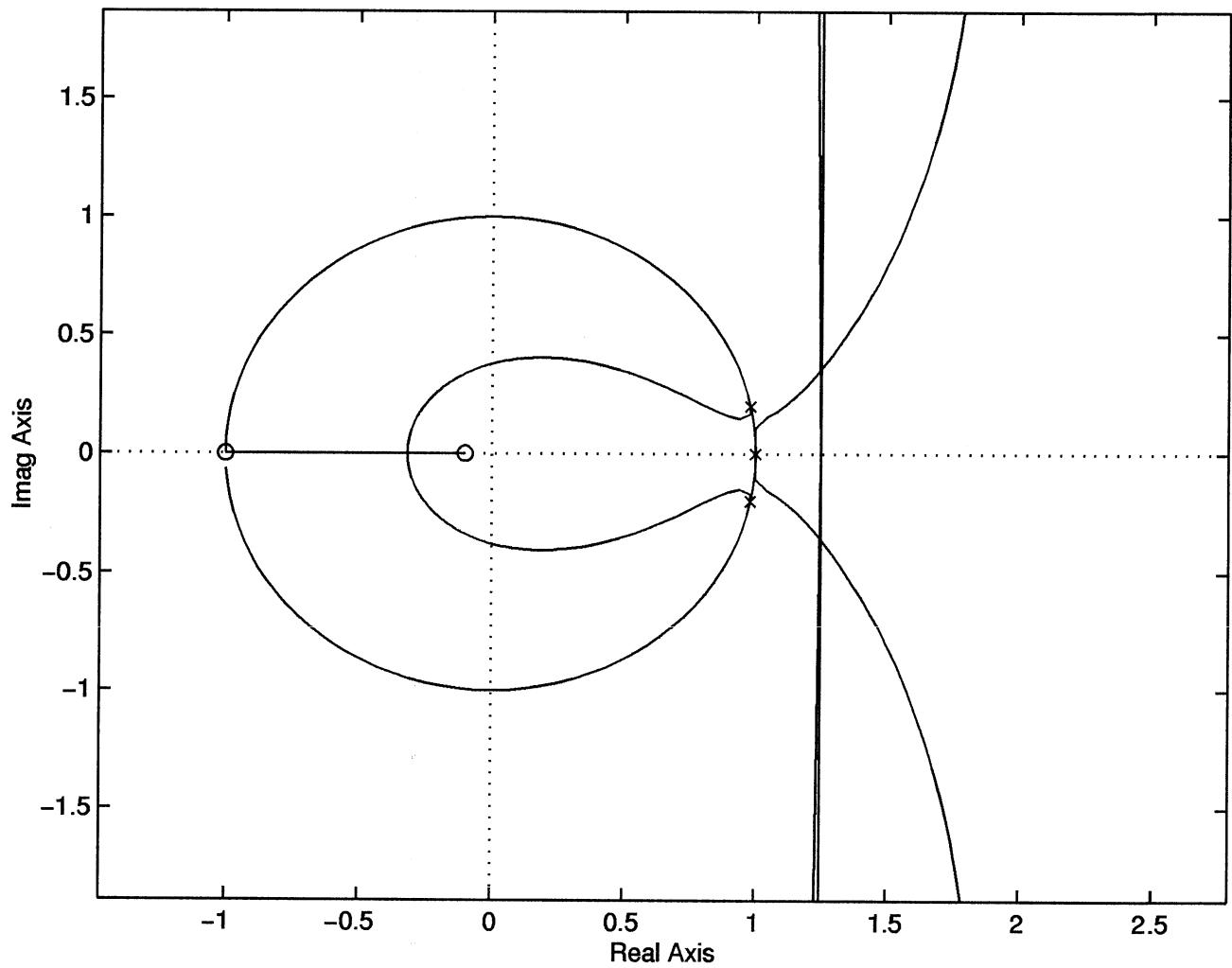
$$\Rightarrow G_p(s) = \frac{1}{s^2(s^2+1)}$$

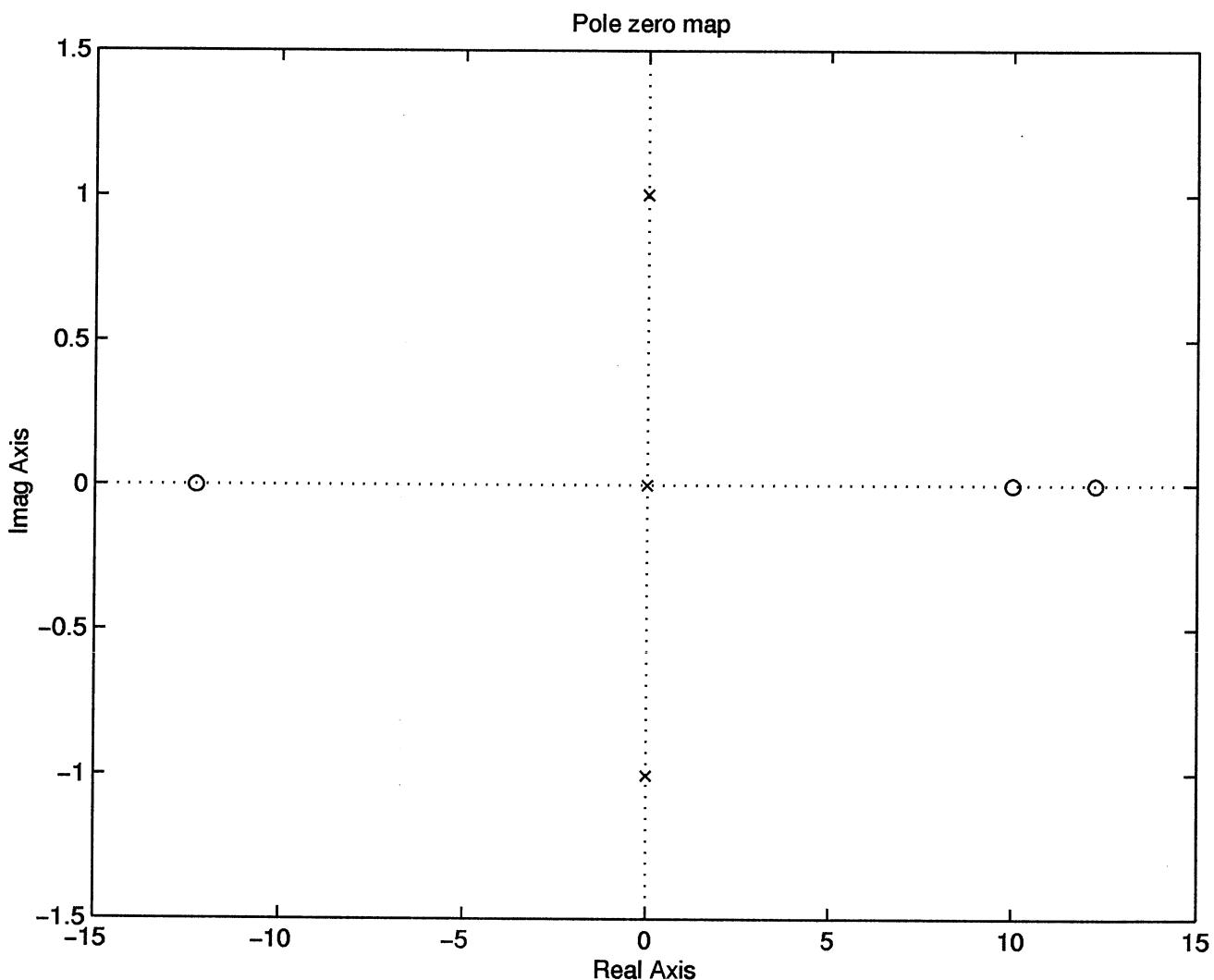
rigid body mode      first mode  
(freq  $\sim 1 \omega_3$ )

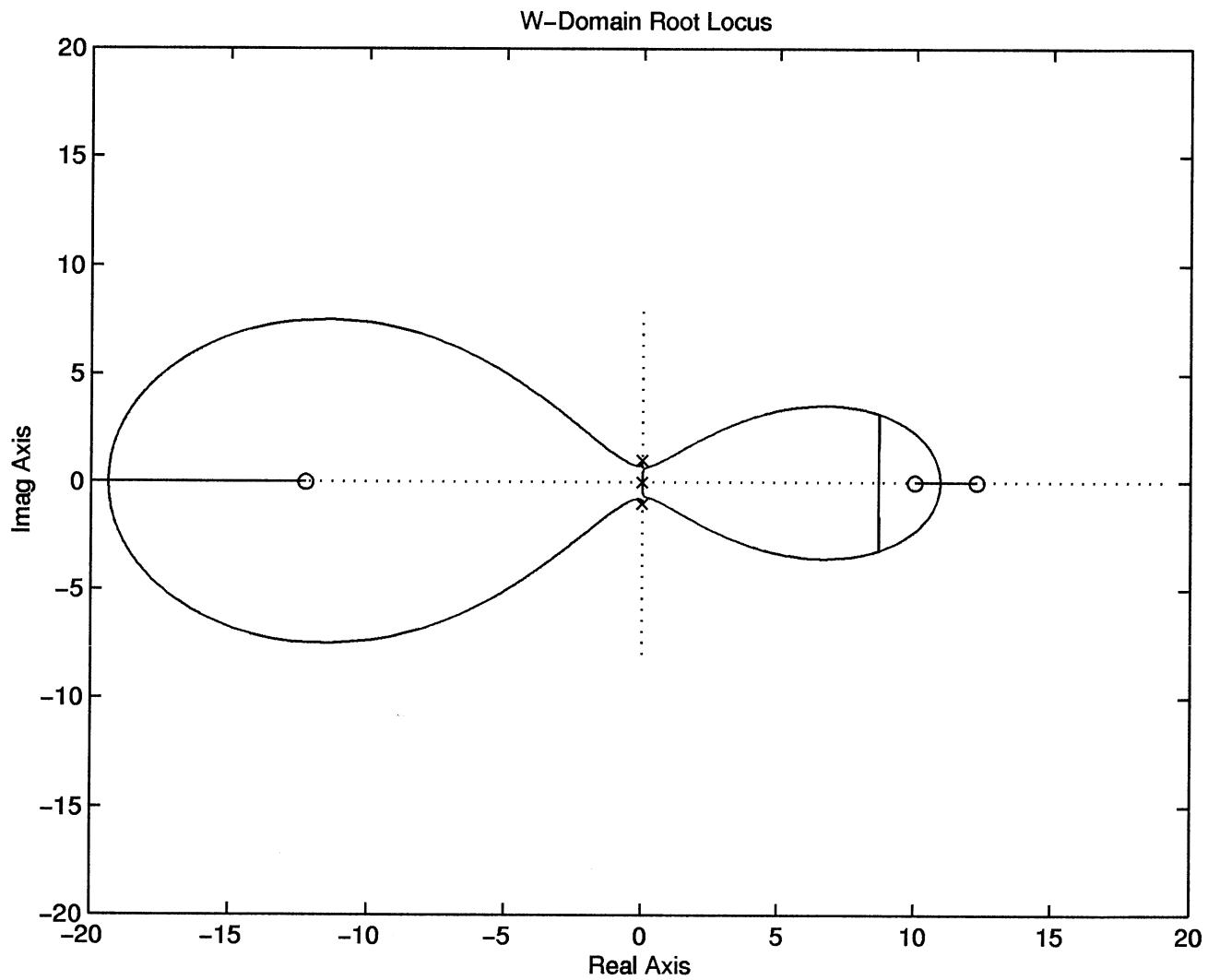
Goal: design a position controller to keep the satellite pointed at a desired location (with zero steady state error)

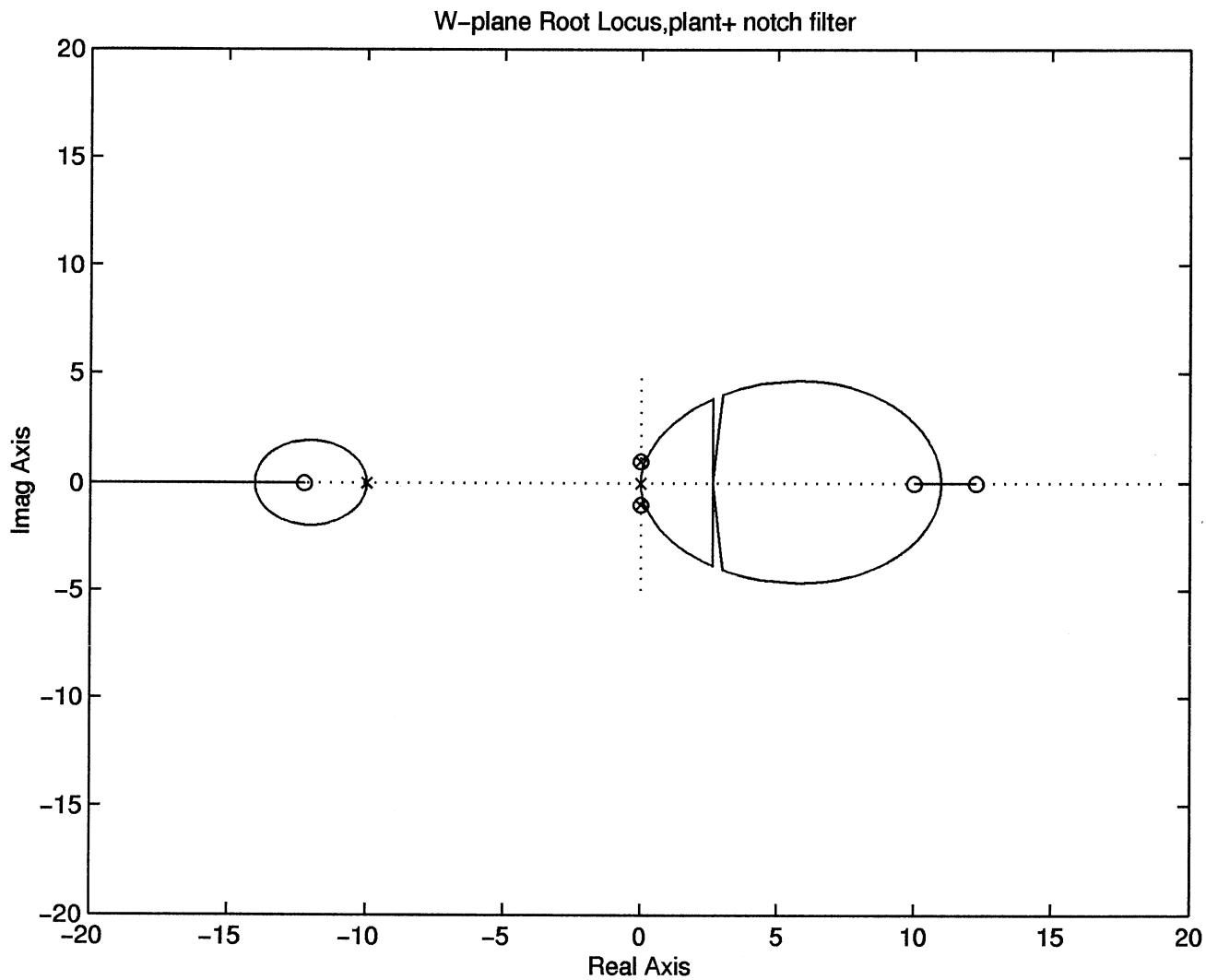


Root Locus, Uncompensated

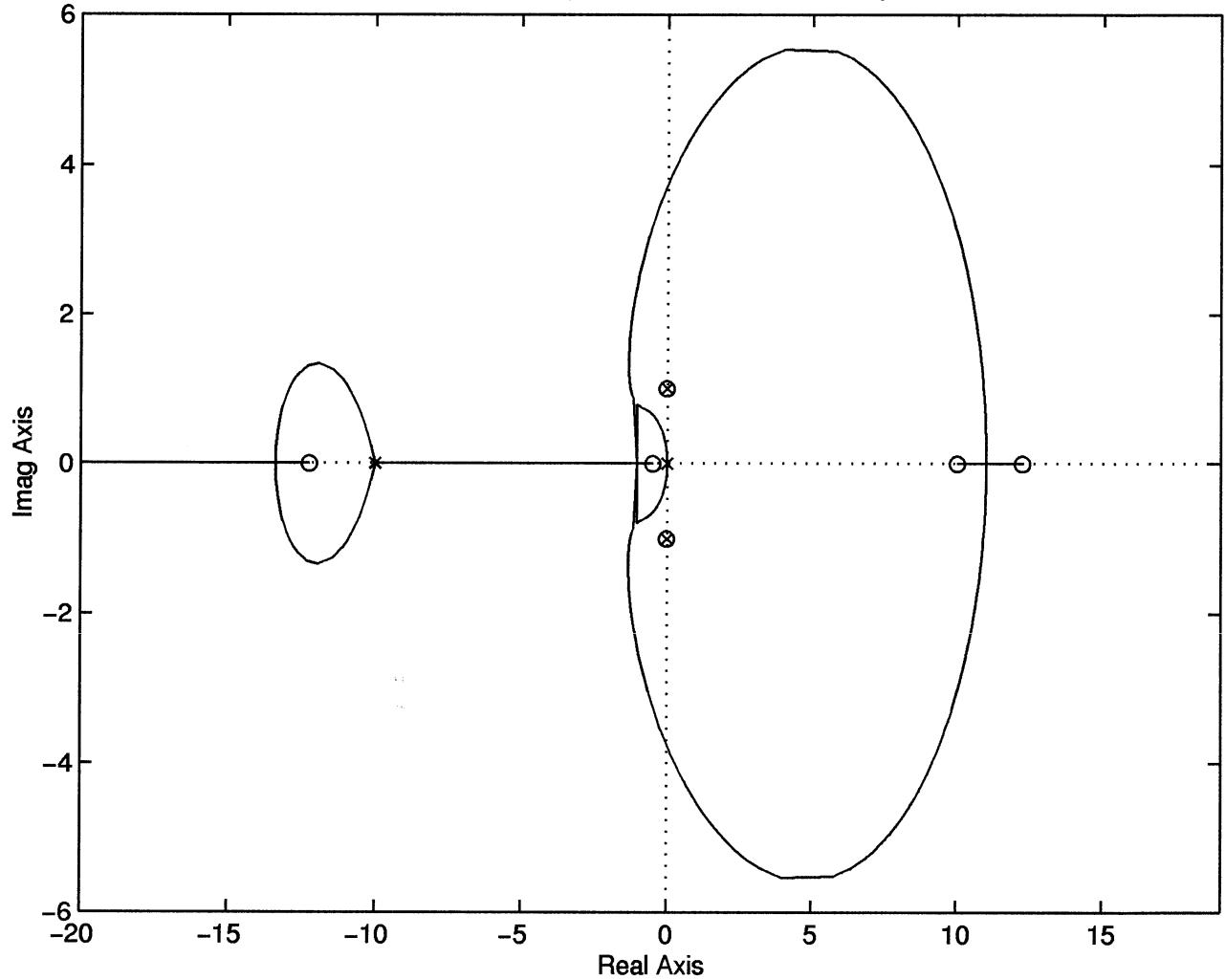




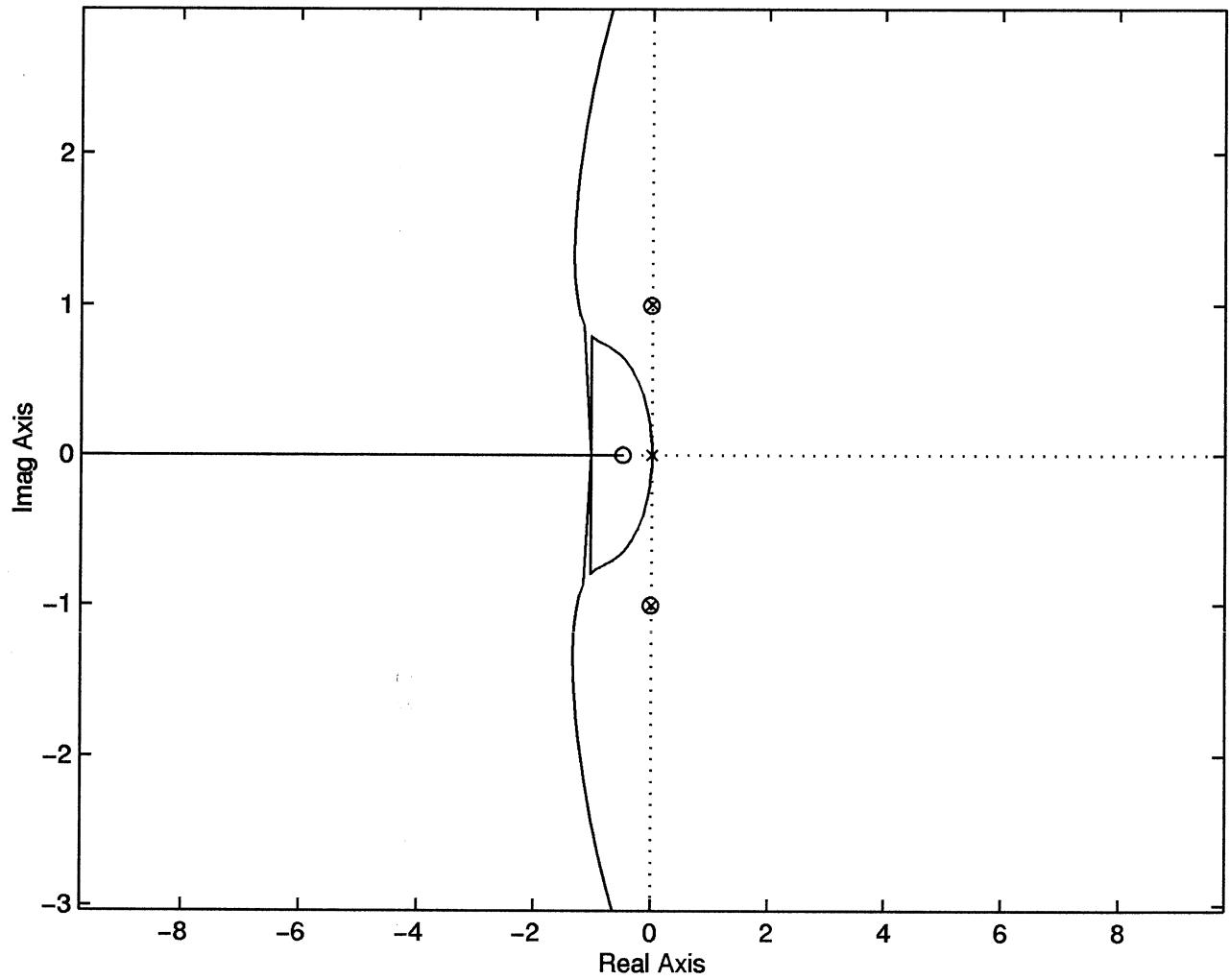




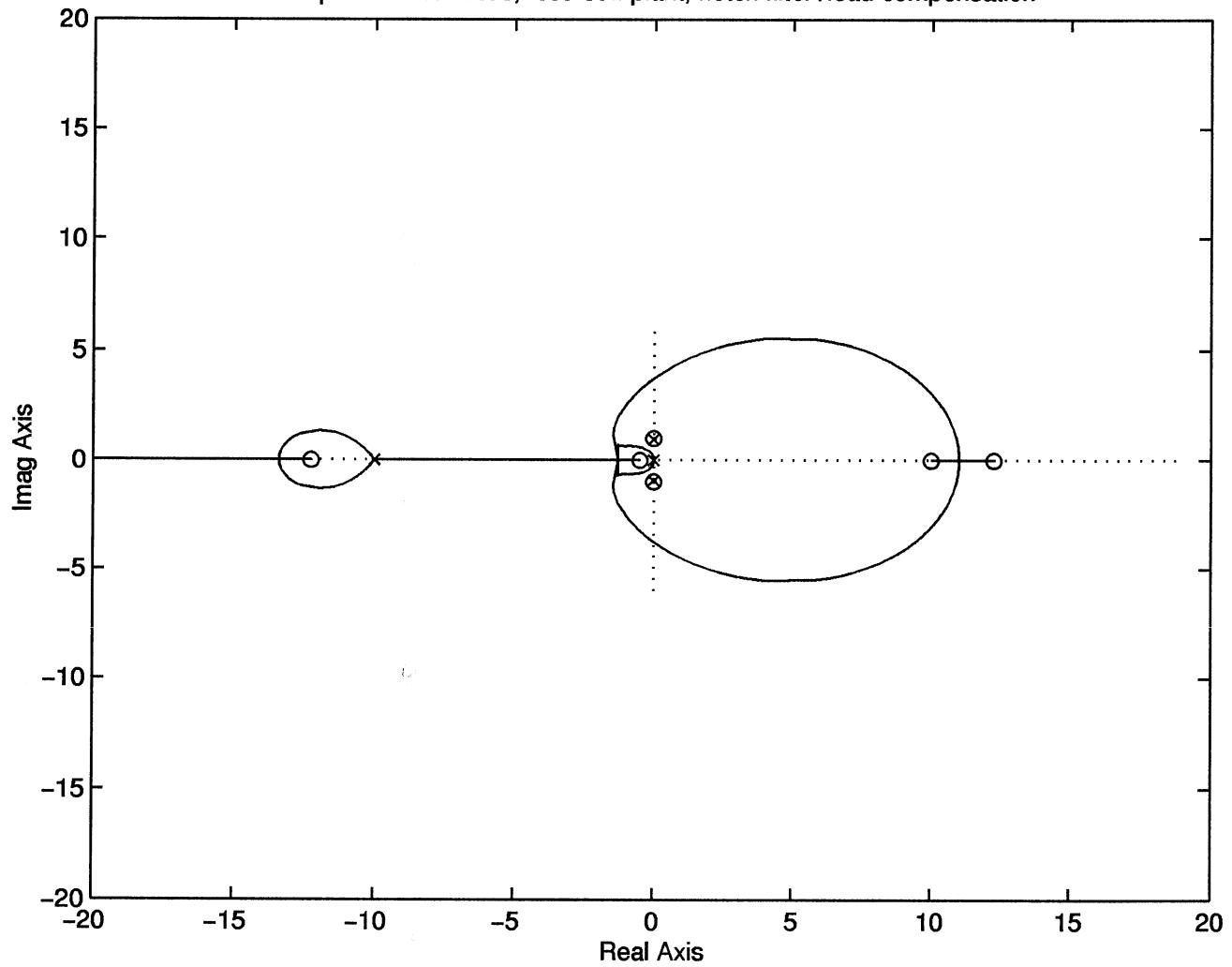
W-plane Root Locus, plant+ notch filter+lead compensation



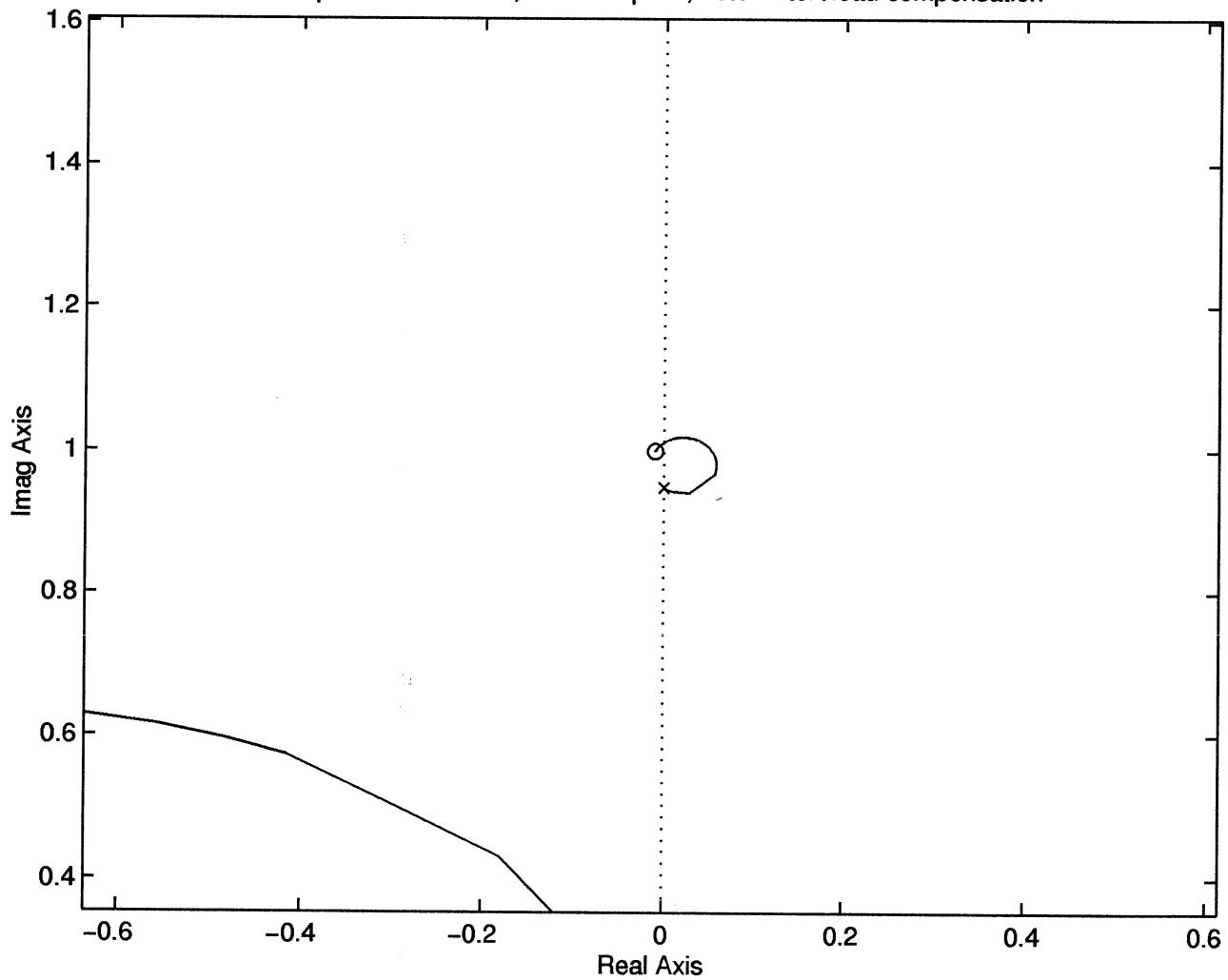
W-plane Root Locus, plant+ notch filter+lead compensation



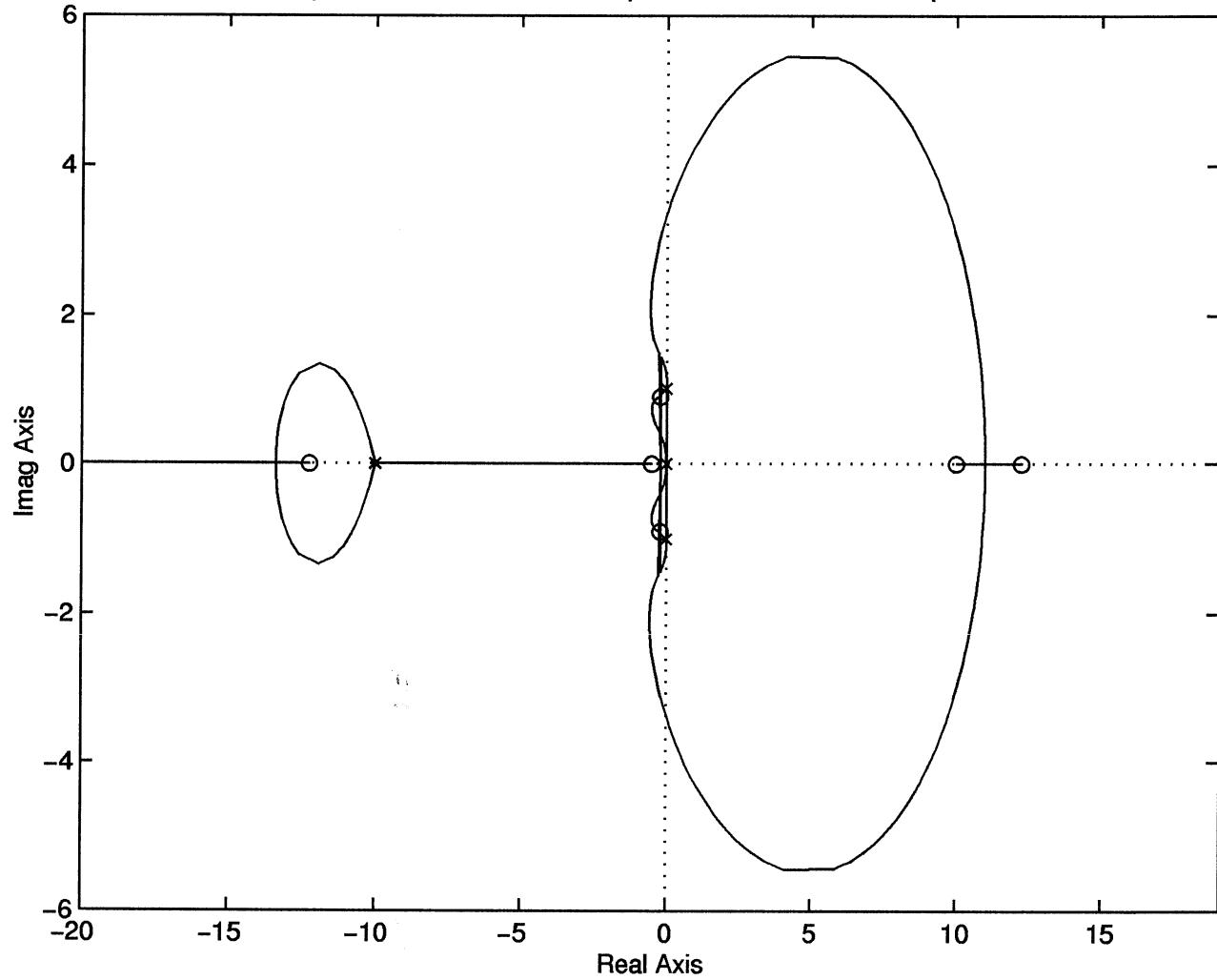
W-plane Root Locus,Less Stiff plant, notch filter+lead compensation



W-plane Root Locus,Less Stiff plant, notch filter+lead compensation



W-plane Root Locus,Less Stiff plant, notch filter+lead compensation



W-plane Root Locus, Less Stiff plant, notch filter+lead compensation

