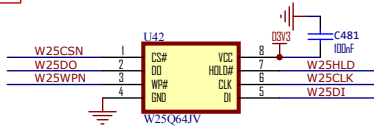
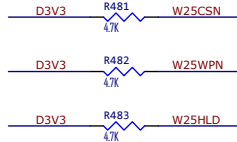




STM8S105K6 MODE

D3V3  
D3V3  
GND  
LEVL1  
LEVL2  
SCL  
SDA  
PINT  
GND  
+12V



LED-PWR  
LED-SYS  
LED-ERR  
OLED-RST  
OLED-EN  
LREFFWM  
SYSRST  
EC11A  
EC11B  
EC11K

ADC 12Bit

CMT1S  
VMT1S  
NTC1S  
CMT2S  
VMT2S

GPI LOGIC

PRT1S  
PRT2S  
DCP1  
DCP2

GPO LOGIC

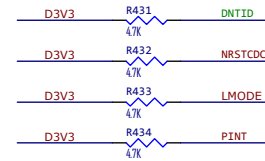
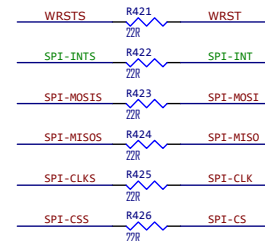
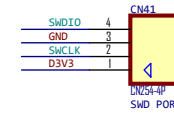
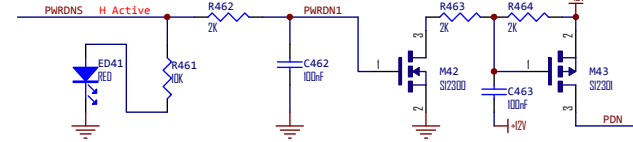
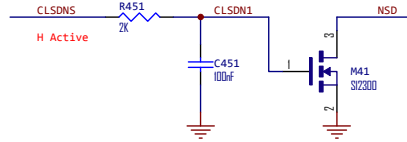
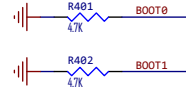
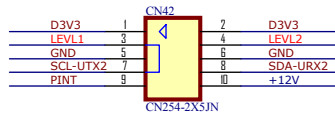
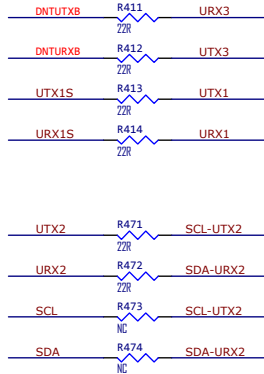
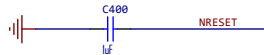
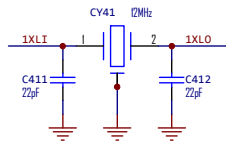
PWRDN  
CLSDN  
PWRDMS  
CLSDMS

W5500 SPI

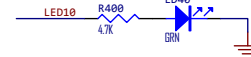
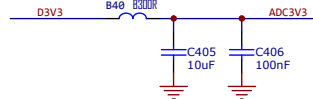
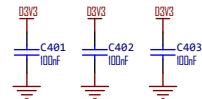
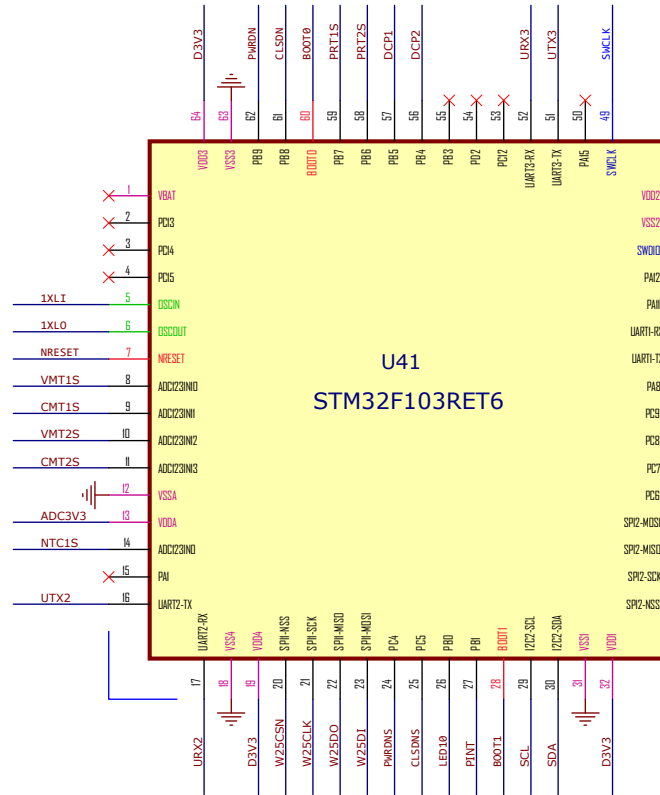
WRST  
SPI-INT  
SPI-MOSI  
SPI-MISO  
SPI-CLK  
SPI-CS

UTX1S  
URX1S  
DNTUTXB  
DNTURXB

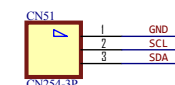
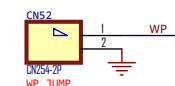
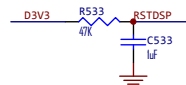
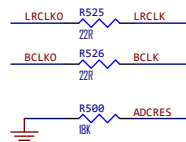
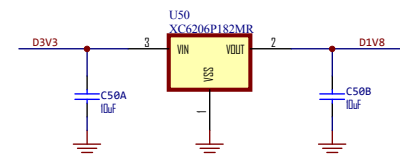
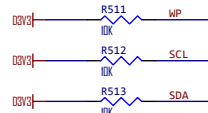
DNTID  
NRSTDC1



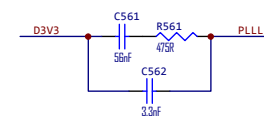
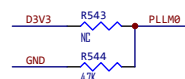
50V/V  
17.5A/V



Title		M06000SP	
Size	Number	Revision	V1.0
A3	P.04		
Date:	5-Jan-2020	Sheet of	
File:	C:\P955E-008\M06000SP-W00\M06000SP-W00.dbs	Drawn By:	



CN50		CN254-2X6	
D3V3	1	2	DNTID
NRSTDC	3	4	LMODE
DNTUTXB	5	6	DNTURX
DNT-RSD	7	8	DNT-TS
DNTLRCK	9	10	DNTBCL
DNTMCLK	11	12	GND



DLT.L+R=170x2mS@0~8191  
DLT.L+R=STEP50=2mS@48K  
0x02~0x05写入后 0x08写1加载生效  
读写验证: 0x03 0x05写FF后读出校验再清零

TPA5050



Title				M06000SP			
Size A3		Number P.05		Revision		VI.0	
Date: 5-Jan-2020				Sheet of			
File: C:\PSSS-000\M06000SP-V000\M06000SP-V000.dwg				Drawn By			