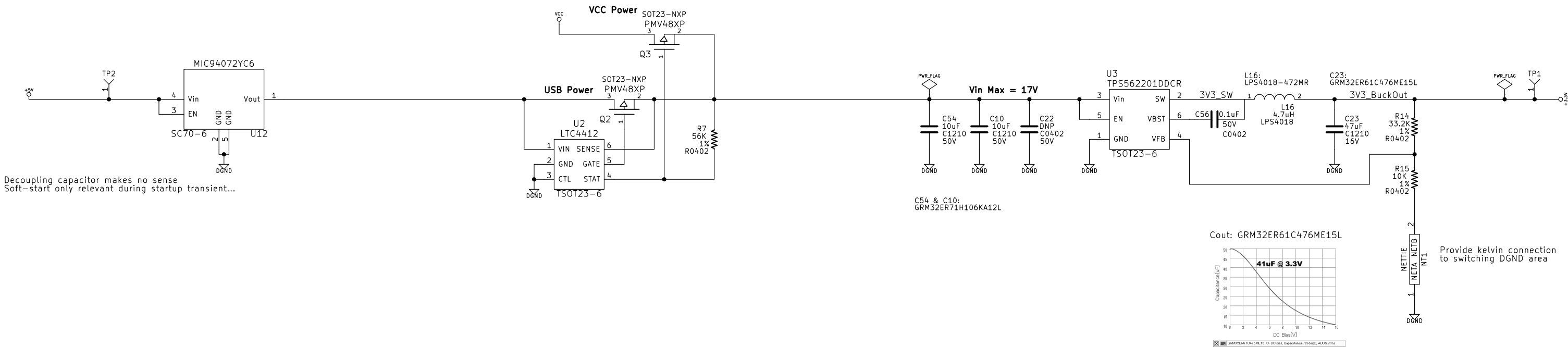


USB Soft-Start

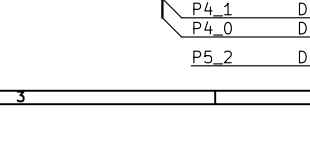
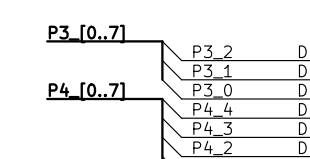
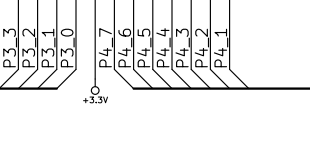
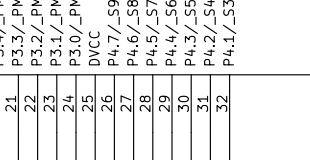
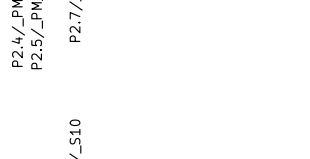
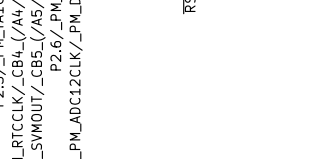
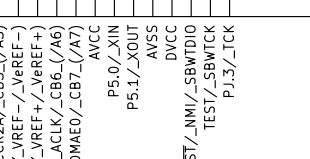
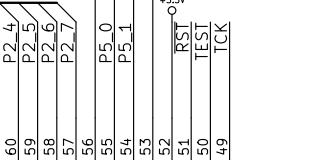
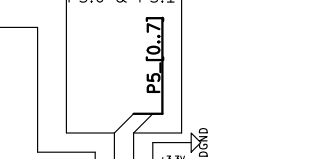
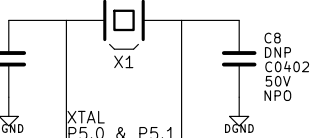
Ideal Diode Controller

3.3V Buck Regulator – 0.5A

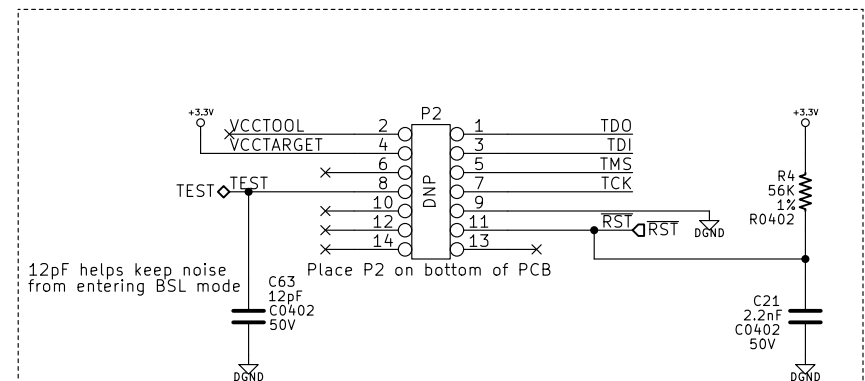


32.768KHz Watch Crystal

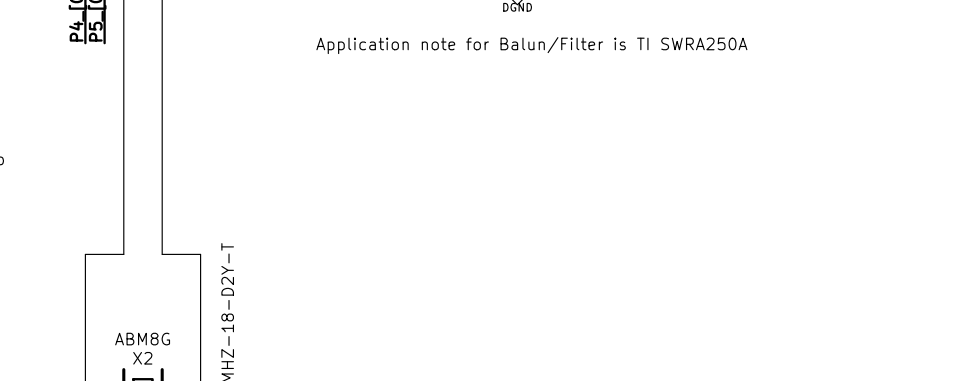
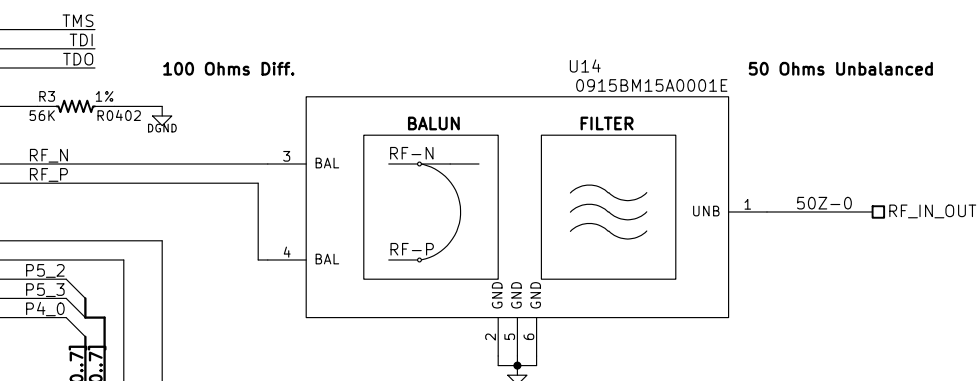
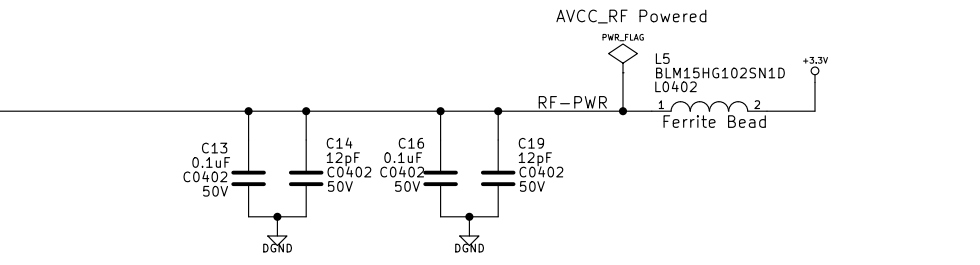
ST3215SB32768H5HPWAA



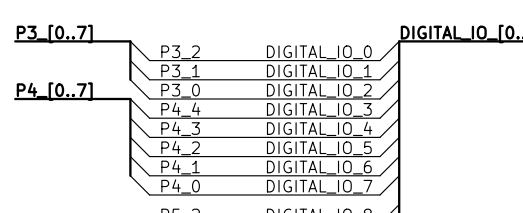
JTAG



http://processors.wiki.ti.com/index.php/File:JTAG_4_wire_pinout.JPG



3.3V External GPIO Interface

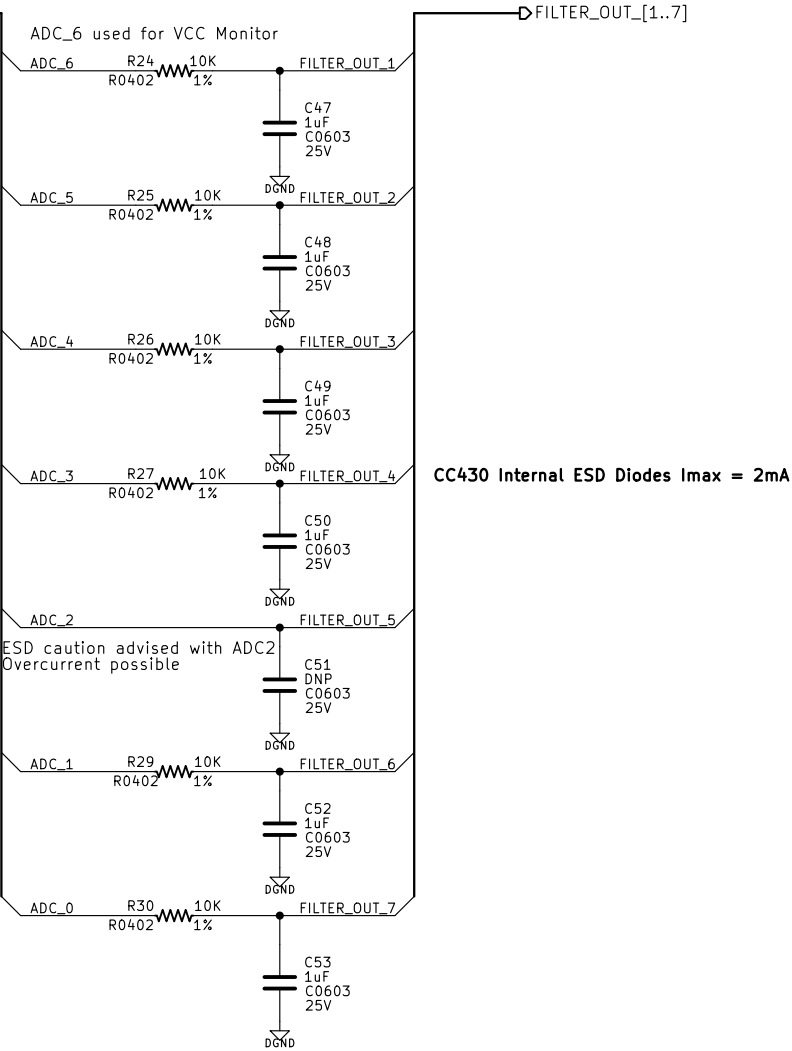


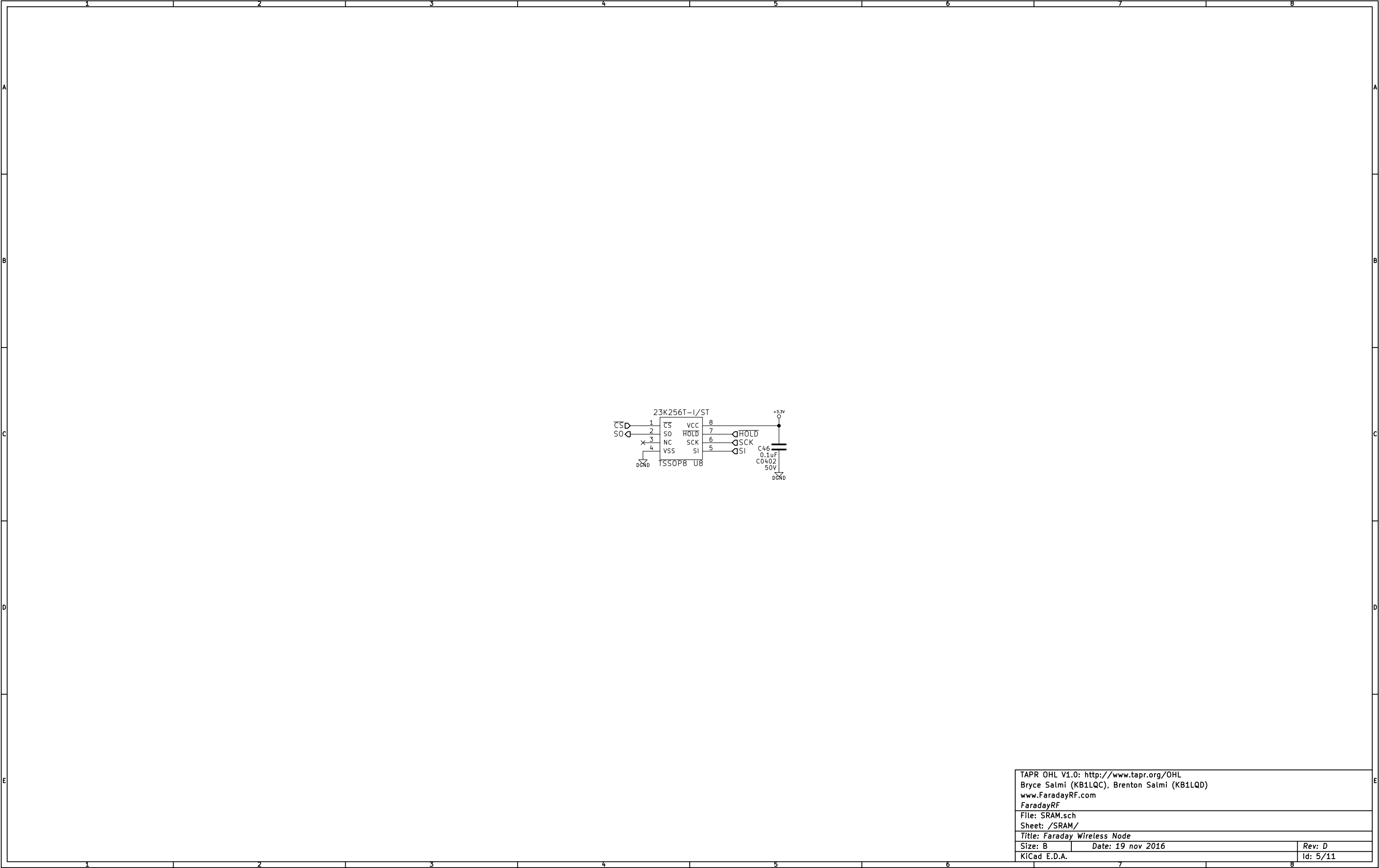
16 Hz ADC Filters

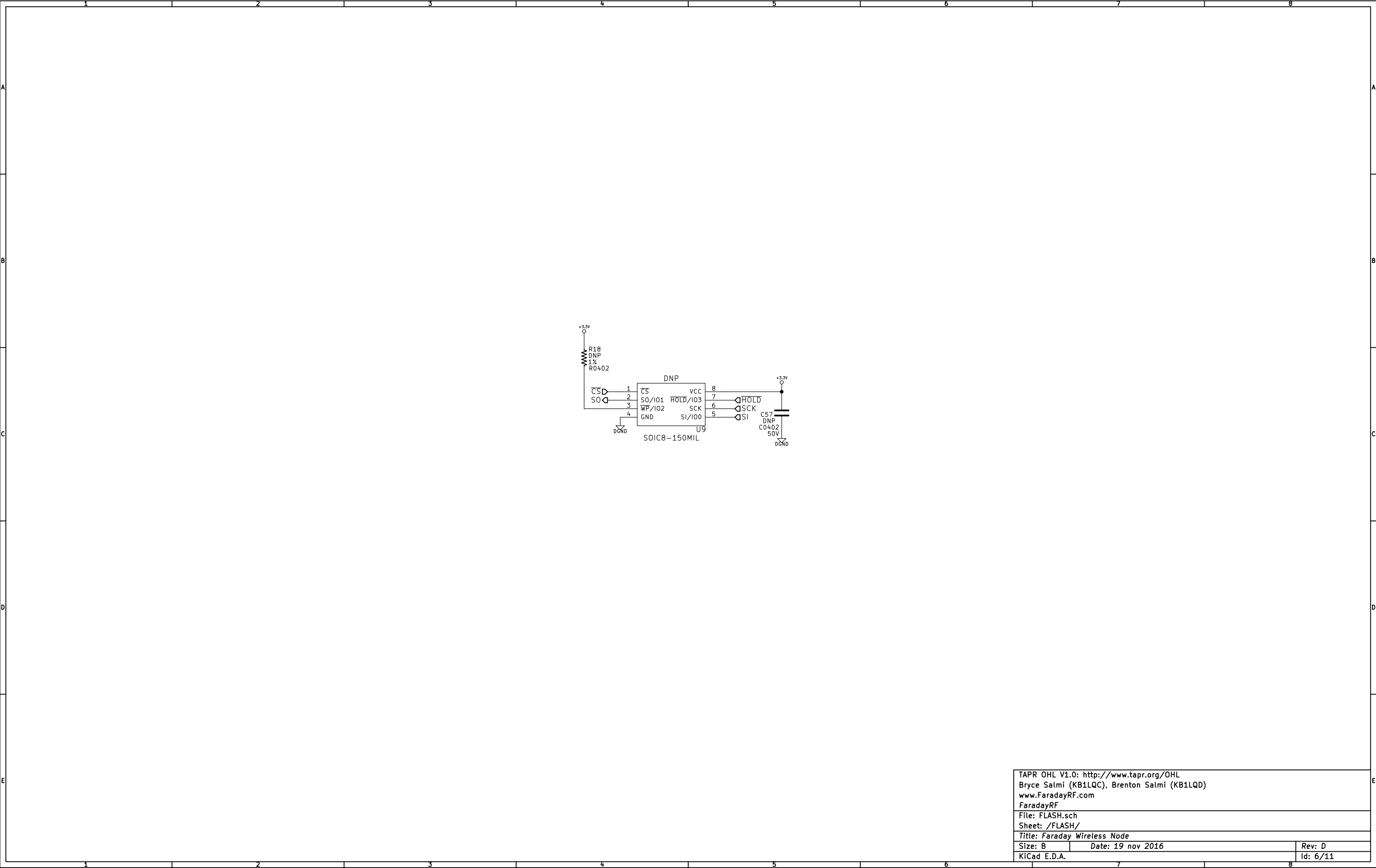
Filter channels flipped for routing!

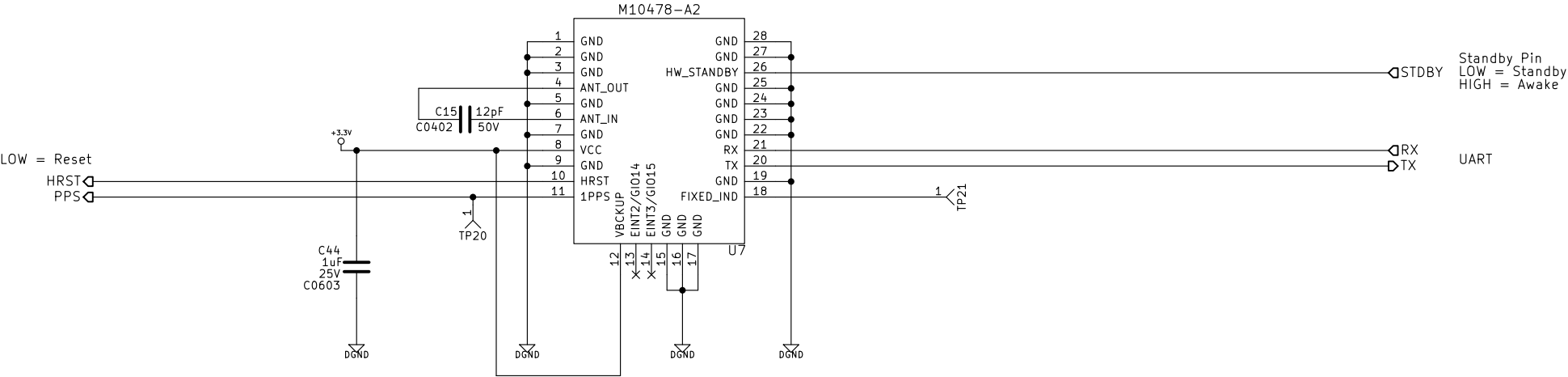
ADC_6 = P2.1
ADC_5 = P2.2
ADC_4 = P2.3
ADC_3 = P2.4
ADC_2 = P2.5
ADC_1 = P2.6
ADC_0 = P2.7

ADC2 intended for:
* Microphone input
* External voltage reference

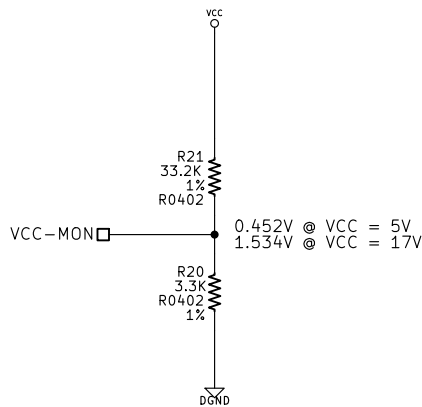




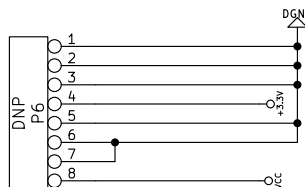




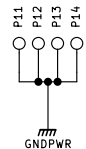
GPS can be DNP'd for cheap version w/ passives soldered
allowing for customer to buy GPS afterwards if needed



Power

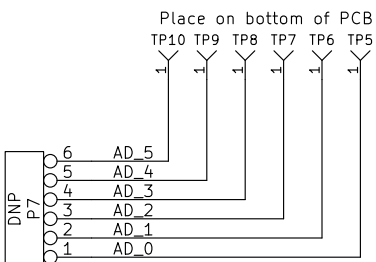


Mounting Holes 125 mil

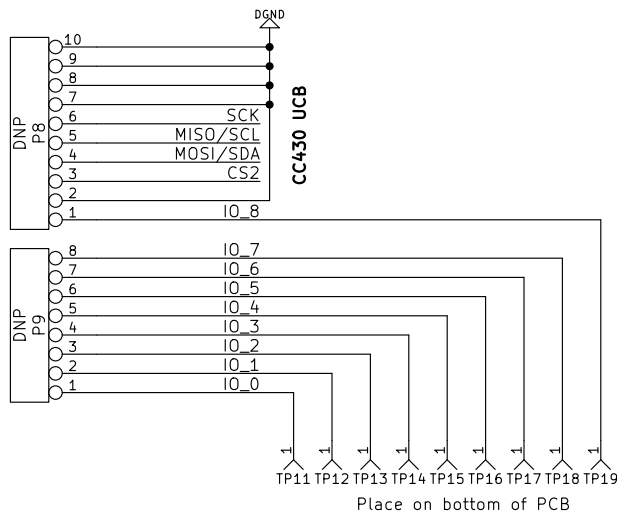


Connected to USB shield

Analog In



Digital



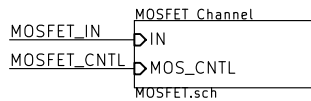
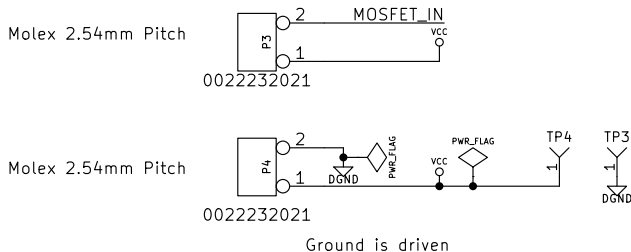
CONN_IO_[0..8] IO_[0..8]

CONN_Analog[0..5] AD_[0..5]

CONN_MOSI/SDA MOSI/SDA
CONN_MISO/SCL MISO/SCL
CONN_SCK SCK
CONN_CS CS2

MOSFET_CNTL MOSFET_CNTL

Ext. Power/MOSFET



TAPR OHL V1.0: http://www.tapr.org/OHL		
Bryce Salmi (KB1LQC), Brenton Salmi (KB1LQD)		
www.FaradayRF.com		
FaradayRF		
File: connector.sch		
Sheet: /Shield Connector/		
Title: Faraday Wireless Node		
Size: B	Date: 19 nov 2016	Rev: D
KiCad E.D.A.		Id: 10/11

