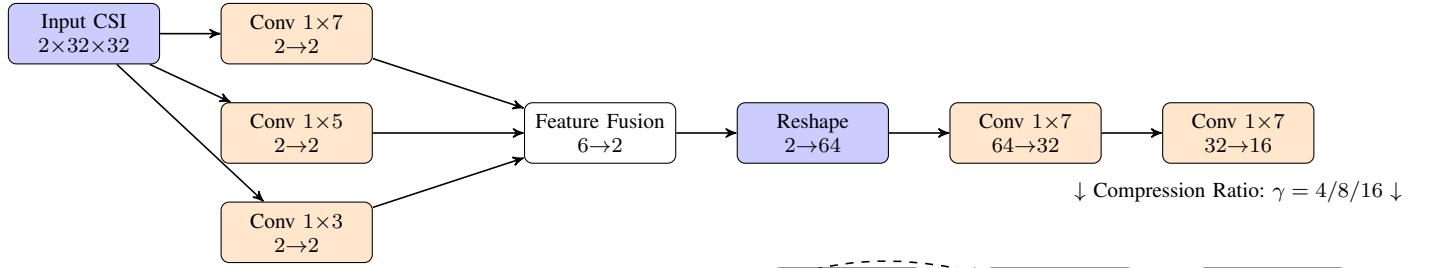


Encoder Stage



Decoder Stage

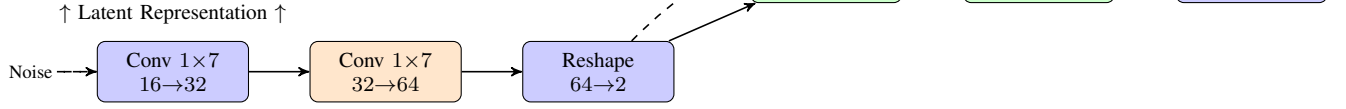


Fig. 1. Complete CLLWCsiNet architecture: (Upper) Encoder stage with multi-scale feature extraction and latent space compression. (Lower) Decoder stage with reconstruction and denoising refinement. The γ denotes compression ratio options.