Instruction	RTL	Registers						Memory		
		A	**B**	**PC**	**IR**	**MAR**	**MBR**	**12 (X)**	**13 (Y)**	**14 (Z)**
Initial	-	??	??	00	20	??	??	00	41	F4
LOAD Z (21)	MAR ← 14 MBR ← M[MAR] A ← MBR PC ← PC + 2 IR ← M[PC]	**F4**	??	**02**	**10**	**14**	**F4**	00	41	F4
MOV B, A (10)	B ← A PC ← PC + 2 IR ← M[PC]	F4	**F4**	**04**	**21**	14	F4	00	41	F4
LOAD Y (21)	MAR ← 13 MBR ← M[MAR] A ← MBR PC ← PC + 2 IR ← M[PC]	**41**	F4	**06**	**50**	**13**	**41**	00	41	F4
SUB B, A (50)	B ← B - A PC ← PC + 2 IR ← M[PC]	41	**B3**	**08**	**31**	13	41	00	41	F4
STORE X (31)	MAR ← 12 MBR ← A M[MAR] ← MBR PC ← PC + 2 IR ← M[PC]	41	B3	**0A**	**00**	**12**	**41**	**41**	41	F4

For each instruction in the program, above, fill out the entire row of register and memory values after that instruction executes. Highlight in bold any values that have changed as a result of this current instruction.