Instruction	RTL	Registers						Memory		
		A	**B**	**PC**	**IR**	**MAR**	**MBR**	**12 (X)**	**13 (Y)**	**14 (Z)**
Initial	-	??	??	00	20	??	??	E8	1A	00
LOAD Y (20)	MAR ← 13 MBR ← M[MAR] A ← MBR PC ← PC + 2 IR ← M[PC]	**1A**	??	**02**	**10**	**13**	**1A**	E8	1A	00
MOV B, A (10)	B ← A PC ← PC + 2 IR ← M[PC]	1A	**1A**	**04**	**20**	13	1A	E8	1A	00
LOAD X (20)	MAR ← 12 MBR ← M[MAR] A ← MBR PC ← PC + 2 IR ← M[PC]	**E8**	1A	**06**	**40**	**12**	**E8**	E8	1A	00
ADD A, B (40)	A ← A + B PC ← PC + 2 IR ← M[PC]	**02**	1A	**08**	**30**	12	E8	E8	1A	00
STORE Z (30)	MAR ← 14 MBR ← A M[MAR] ← MBR PC ← PC + 2 IR ← M[PC]	02	1A	**0A**	**00**	**14**	**02**	E8	1A	**02**

For each instruction in the program, above, fill out the entire row of register and memory values after that instruction executes. Highlight in bold any values that have changed as a result of this current instruction.