5	4	3	2		1.		
Table of Contents					Revisions &	Change	≟ Loq
1 Title				Rev	Description	Date	Approved
2 Block Diagram				Х1	Initial Draft	10/23/13	RON KIM
3 K22F MCU				X2	Adding SGTL5000 Audio Codec	11/06/13	RON KIM
4 OpenSDA INTERFACE 5 I/O Headers and Power Supply	-			A	Release to production prototype	11/15/13	RON KIM
1,0 headeld and lower bapping	1			В	Release to production Pilot	04/15/14	RON KIM
				С	Audio Jack changed	04/16/14	RON KIM
				D	Net naming updated	05/28/14	RON KIM D

FREEDOM K22F

freescale		Automotive, Industrial & Multi- Market Solutions Group 6501 William Cannon Drive West Austin, TX 78735-8598					
Designer: RAFAEL DEL REY	Drawing 1	Title:	Classification:	_{FCP:}	FIUO:	PUBI:	x
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Drawn by: RAFAEL DEL REY Approved: RON KIM	Page Title		Number	H-28164 PDF:	SPF-28164		Re D

- 1. Unless Otherwise Specified:
 - All resistors are in ohms, 5%, 1/8 Watt All capacitors are in uF, 20%, 50V
 - All voltages are DC
 - All polarized capacitors are aluminum electrolytic
- 2. Interrupted lines coded with the same letter or letter combinations are electrically connected.
- 3. Device type number is for reference only. The number varies with the manufacturer.
- 4. Special signal usage:
 - _B Denotes Active-Low Signal
 - <> or [] Denotes Vectored Signals
- 5. Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.









