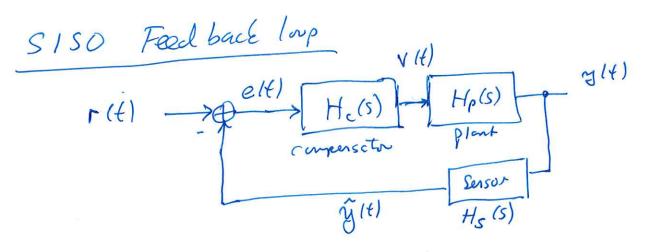
Lecture notes on Disihl Corbl Unit 5

References: O Unit 5 notes posted on DZL O Texbook Nie Chapter 13

Recommend that you work though these as we go though this materal.



1/1/ input reference

c(t) - eva

VIH - plant dove

y(t) - plant output

y (t) - measurement of y (t)

- v svally approximate y (t) = y (t)

ideal sersor feedback.

Digital Sersor Feedback

I discrete tree scaped and quarked version of the signal y(t).

For the present ignore the transfer function Ho(5) or set Ho(5) = 1

Mk - s Mk 'drop the notation'

$$\forall k = \forall (t)$$

$$t = kT_s$$

Ts - Sampling +, me

Ng (K)

(K-1) Ts KTs (K+1) Ts (K+2) Ts 2

Yx is discrete time sapled representil of He continuous time sign (y (t)

Yk is further quantized in amplitude by the ADC. Represented by randing off the real value sample into an integer.

Suppose we have a guarkentar step of Δ then $Y_K = \Delta \Gamma$ and $(Y(KT_S))$

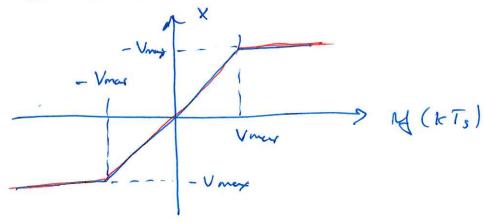
(discrete valued) yk 20 to 20

Real Signal quarters butter have a saturation level.

Upper limit V max lower limit - V max Model of salvatin:

This operation limits the output to within

- V max to V max



Saturation of ADC, DAC, sensors and compansature outputs is on issue for contact systems.

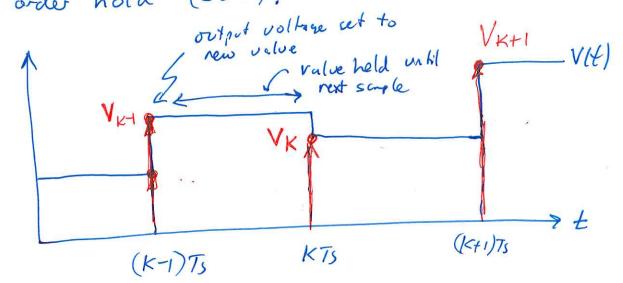
Overall Model of ADC

DAC model (Digital to Analog Convertor)

We will have a compensation output that is

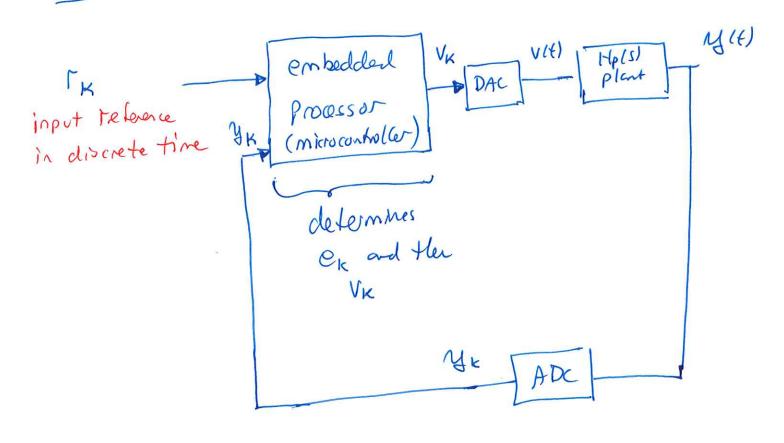
a discrete time saple denoted by V_K DAC maps this in to a continuous time some(, $V_K - DAC - V(H)$

Generally we assure a model of the DAC as a 'Zero order hold' (ZOH).



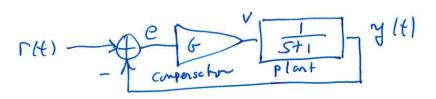
V(t) = V floor (t/s)

eg. 75 = 0.1 seconds t = 1.01 seconds $V(t) = V_{10}$



Mixed sisral loop with docrete the signals in Mixed sisral loop with docrete the signal in Plant, micro comboller, ADC, DAC. Continuous the Signal in Plant,

Digital Proportional Feedback loop example



Suppose we have basis Leed back lap of proporhaid feed back as in this example.

We want to implement this with a distribut

$$V(t) = G e_{floor}(\frac{t}{75}) = V_{floor}(\frac{t}{75})$$

We can put this into a Simulak simulation as shown in the unit 5 notes.

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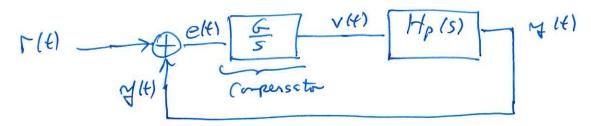
TOTAL STATE OF THE SHARE S

Zott blocks are specified will a sarphy time of Ts

In this simple example all the processor really does is the update of

where TK and MK are inputs.

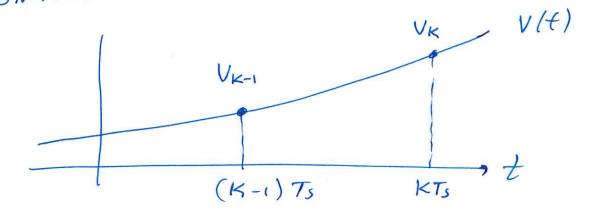
Consider rest the compensation consisting of on integrator,



Converting into a discrete the compensation we read to implement $V(t) = G \left(eltidt' \right)$

To convert this into a discrete the representation of the integral operation start will converting this into a D.E.Q. by taking destration of both sides.

Now consider on Euler approximation has the destrative of v(t).



$$V_K = V(KT_5)$$

$$\frac{dV}{dt}\bigg|_{t=K7s} \sim \frac{V_{K} - V_{K-1}}{Ts}$$

Herce He recursin equition of

Now we have the operation for the embedded combiler to do.

er = Tr - yr

VK = VK+ + TS G-CK

which can be trivially impremeted by He contoller.

Now consider concerting this mts a 2 transfum
represent. He 2-transfum does not help in cleheming
the microconfoller code but is use ful for analysis of
the loop.

Take the recursin equation $V_{K} = V_{K-1} + T_{S} G e_{K}$ and transfor to 2 domai.

All we reed is the identity of $V_K \iff V(Z)$

VK+n () V(Z)Z

Hence $V(z) = V(z)z' + T_s G E(z)$

You may have to review some of you notes for previous couses regarding & transfer identifies. if VK => V(2) this reas

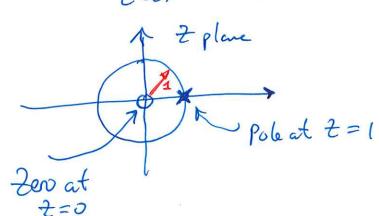
$$V(2) = \sum_{k=-\infty}^{\infty} V_k 2^{-k}$$

note then that her VK+n = - (k+n) 1 = - (k+n) 2 = - (k+n) 2 = - (k+n) 2 = - (k+n) 2 V(2)

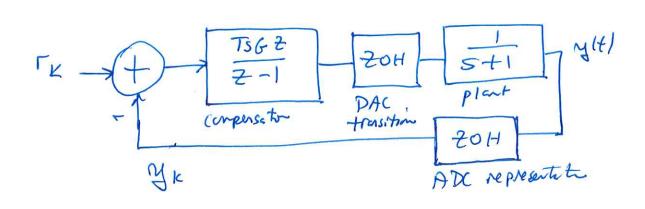
(). VK+n (7 2° V(7)

So back to V(2) = V(2) = + Ts & E(2)

We have
$$\frac{V(2)}{E(2)} = \frac{T_s G}{1 - 2^{-1}} = \frac{T_s G^2}{2 - 1}$$



Simulink model



To get a rose accorte represent at the DAC and ADC operation we may want to add in limiter and granketing.

We also have a "fixed-point" model of He processor evaluate of the difference equations Which can be built into the 7 trans bu representation.

Disrete Tim appositution of DEQ

Note He DEQ in Mis case wes $\frac{dV(t)}{dt} = Gelt$

while we approxated as

VK - VK-1 = 6 e K

But Mrs is an approximation and not unique, (13)

We could approximate as

(forward Over instead)

of backward eller, VK - VK-1 = G-CK-1

In this case we are using a previous value of elt). This adds additional delay and as discussed earlier Can lead to a less stable feedback loop.

 $\frac{V(2)}{E(2)} = \frac{T_s G}{2-1}$ For this case

Sigh pole at Z= | and no zero, Having Me delay is more realizher in that the Sensor data Balways shightly delayed,

Matlab can compite the 2 transter directly from He continuous time transfer transfer transfer transfer transfer transfer transfer usins cade. This is an extremely useful function for desisning discrete time combol systems.

As on example, say we have $H(s) = \frac{1}{s}$

Then the commend, H = tf(1, [1,0]) Ts = 0.1 Hd = c2d(H, Ts)

results in Hd as a discrebe the 2 thmobin of the integrator given as

Hd = 0.1

cad approximates the describer as a formal Guler, and hence assures the dely of the mint, and hence assures the dely of the mint.

This is because C2d assures a default of '20H' on the inputs. Other options are available.

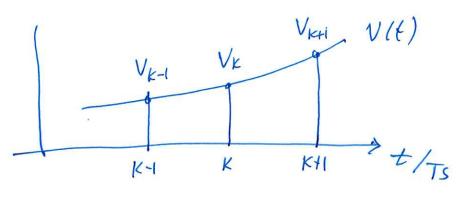
For the present purposes, we will assure the default 170H' optime as being the most applicable, as it is realistic that the imput is delayed by 20H operation. If To is small enough relative to the time constats

of the poles in $H_c(s)$ and $H_p(s)$ then (1) it does not really mether what method is used in

Note also that the converior has the discrete dri recursion regulation for the compensation to the 2 transton equivalent is only recessary for transton equivalent is only recessary for analysis and simulation. What is implemented in analysis and simulation. What is implemented in the embedded processor is the difference equition.

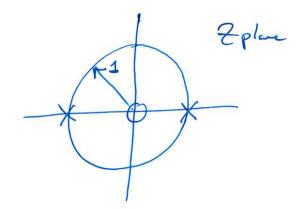
If Ts is larger then we have to be careful how the descritions are mapped into discrete trie. As an example go back to the DEQ equation

Suppose une use a certal d'Îlerice approximation.



We can approximate as

$$\frac{\sqrt{(2)}}{E(2)} = \frac{2T_5 G}{2 - 2^{-1}} = \frac{2T_5 G Z}{Z^2 - 1}$$



This is now a second order 2 transmir hunt will 2 poles and one zero. The additional pole at 2 = -1 does not in fluence the behavior for low heguing signals.

We will leave His part here but just he aware the mapping from continuous to discrete the is approximate and is Heatine not unique.

Example convert to discrete time Confersator and determin the recursion

$$\frac{V(s)}{E(s)} = \frac{s^2 + 4s + 4}{s^2}$$

equation.

$$V(s) s^2 = E(s) s^2 + 4E(s) s + 4 E(s)$$

$$\frac{V_{K+1} - 2V_{K} + V_{K-1}}{T_{s}^{2}} = \frac{e_{K+1} - 2e_{K} + e_{K-1}}{T_{s}^{2}}$$

$$V_{K+1} = 2V_{K} - V_{K-1} + e_{K+1} \left(1 + 4T_{5} \right) + e_{K} \left(-2 - 4T_{5} + 4T_{5}^{2} \right) + e_{K-1}$$

Wholis He regard difference recursive equations
that can be directly implemented.

Note that the central difference was used here him

$$V(s) s^2 \Rightarrow \frac{d^2v(t)}{dt^2}$$

$$\frac{d^{2}V(\ell)}{dt^{2}}\bigg|_{t=kTs} \approx \frac{V_{KH} - 2V_{K} + V_{K-1}}{T_{s}^{L}}$$

For Min example we matched the derivatives of the deflerere equates.

C2d works differently using the approximation of an invarient impolse response with a 20H our the DAC model.

Which model to ve depends on the details of the