

Tutorial 6: LLVM Framework API in C++

LLVM IR stands for low-level virtual machine intermediate representation. LLVM is a Static Single Assignment (SSA) based representation in assembly language form. It provides type safety, low-level operations, flexibility, and the capability of representing 'all' high-level languages cleanly. In assignment 5, use LLVM framework API to generate LLVM IR from Minic AST in C++.

Agenda

- Important LLVM IR Class description
- Typical LLVM Framework API
- Example for implementing in C++
- Example for control flow graph

Some good sources

- LLVM Tutorial: <https://llvm.org/docs/tutorial/MyFirstLanguageFrontend/index.html>
- LLVM Language Reference Manual: <https://llvm.org/docs/LangRef.html>
- llvm Namespace Reference: <https://llvm.org/doxygen/namespacellvm.html>

Name	Description
<code>llvm::Context</code>	Owns a lot of core LLVM data structures such as the type tables. You don't need to understand all of it, but it is one of the arguments for other API calls.
<code>llvm::Module</code>	Owns functions and global variables. It owns the contents of all of the IR.
<code>llvm::Builder</code>	A helper object of <code>llvm::Module</code> to generate LLVM instructions. It has methods to insert or create new instructions.
<code>llvm::Value</code>	A base class. Itself and its inherited classes such as <code>llvm::Constant</code> can represent internal, expr, variable, function argument, etc.
<code>llvm::Type</code>	A base class of type. Itself and its inherited classes such as <code>llvm::FunctionType</code> can represent function prototype, array type, etc.
<code>llvm::BasicBlock</code>	Basic block in control flow graph (CFG), which is mentioned in lecture. Instructions are inserted in the block. <code>llvm::Builder</code> manages CFG.

Table 1: Important LLVM Class Description

LLVM Framework API

Table 2 shows some typical API functions which are used in Assignment 5.

Return type & Methods under namespace llvm	Description
Type* Type::getInt32Ty(*TheContext)	return 32-bit width "integer" type.
ArrayType* ArrayType::get(Type* a, uint64 b)	return b type-a elements' ArrayType type.
FunctionType* FunctionType::get(Type* a, std::vector<Type*>b, false)	a is function return type, b is an array of params types, false means non-variadic function. The methods returns FunctionType
Function* Function::Create(FunctionType* a, Function::ExternalLinkage , std::string b, TheModule.get());	a is function type, b is function name. It returns a function and sets the function into TheModule.
Constant* ConstantInt::get(Type* a, uint64 b, bool isSigned)	return boolean and integer constant. The constant type is a, value is b. It is signed if isSigned is true.
BasicBlock* BasicBlock::Create(* TheContext , std::string a, Function* Parent)	Create a named "a" basic block which is a sequence of instructions. The block belongs to Function Parent.
void IRBuilder::SetInsertPoint(BasicBlock* bb)	This specifies that created instructions should be appended to the end of bb.
BasicBlock* IRBuilder::GetInsertBlock()	Get the inserted block bb.
Value* IRBuilder::CreateStore(Value* val, Value* ptr)	Store instruction to write val to ptr.
Value* IRBuilder::CreateLoad(Value* ptr)	Load instruction to load ptr.
CreateAdd CreateSub CreateMul ...	Add, Sub, Mul instructions ...
Value* IRBuilder::CreateBr(BasicBlock* Dest)	Unconditional 'br label X' instruction.
Value* IRBuilder::CreateCondBr(Value* Cond, BasicBlock* True, BasicBlock* False)	Conditional 'br Cond, TrueDest, FalseDest' instruction.
Value* IRBuilder::CreatePHI(Type* a, unsigned n)	Create a PHI node with n incoming edges.
void PHINode::addincoming(Value* a, BasicBlock* bb)	Add an incoming value a and its corresponding block bb.
Value* IRBuilder::CreateGEP(Value* a, std::vector<Value*>idxlst)	Create getelementptr instruction for variable a. Do some research on it. Refer to A5 handout.

Table 2: Typical LLVM Framework API Description

Examples

(a)

First example is a simplified version from online LLVM tutorial.

After generating AST,

```

1 using namespace llvm;
2 static std::unique_ptr<LLVMContext> TheContext;
3 static std::unique_ptr<Module> TheModule;
4 static std::unique_ptr<IRBuilder<>> Builder;
5 static std::map<std::string, Value*> NamedValues;
6
7 static void InitializeModule() {
8     // Open a new context and module.
9     TheContext = std::make_unique<LLVMContext>();
10    TheModule = std::make_unique<Module>("output.bc", *TheContext);
11
12    // Create a new builder for the module.
13    Builder = std::make_unique<IRBuilder<>>(*TheContext);
14 }
15 //Classes for node: NumberExprAST, BinaryExprAST, CallExprAST ...
16 ...
17 //return value representing numeric literals

```

```

18 Value *NumberExprAST::codegen() {
19     return ConstantFP::get(TheContext, APFloat(Val));
20 }
21 //Look this variable up in the function.
22 Value *VariableExprAST::codegen() {
23     return NamedValues[Name];
24 }
25 //Impl IR for binary expr
26 Value *BinaryExprAST::codegen() {
27     Value *L = LHS->codegen();
28     Value *R = RHS->codegen();
29     if (!L || !R)
30         return nullptr;
31
32     switch (Op) {
33     case '+':
34         return Builder.CreateFAdd(L, R);
35     case '-':
36         return Builder.CreateFSub(L, R);
37     case '*':
38         return Builder.CreateFMul(L, R);
39     case '<':
40         L = Builder.CreateFCmpULT(L, R);
41         // Convert bool 0/1 to double 0.0 or 1.0
42         // You don't need to know it.
43         return Builder.CreateUIToFP(L, Type::getDoubleTy(TheContext));
44     }
45 }
46 //Impl IR for call expr
47 Value *CallExprAST::codegen() {
48     // Look up the name in the global module table.
49     Function *CalleeF = TheModule->getFunction(Callee);
50
51     std::vector<Value *> ArgsV;
52     for (unsigned i = 0, e = Args.size(); i != e; ++i) {
53         ArgsV.push_back(Args[i]->codegen());
54         if (!ArgsV.back())
55             return nullptr;
56
57     return Builder->CreateCall(CalleeF, ArgsV);
58 }
59 //Generate function prototype
60 Function *PrototypeAST::codegen() {
61     // Make the function type: double(double,double, ...) etc.
62     std::vector<Type*> Doubles(Args.size(),
63                                Type::getDoubleTy(TheContext));
64     FunctionType *FT =
65         FunctionType::get(Type::getDoubleTy(TheContext), Doubles, false);
66
67     Function *F =
68         Function::Create(FT, Function::ExternalLinkage, Name, TheModule.get());
69
70     return F;
71 }
72 //Visit Func declaration
73 Function *FunctionAST::codegen() {

```

```

74 // First, check for an existing function from a previous 'extern' declaration.
75 Function *TheFunction = TheModule->getFunction(Proto->getName());
76 // Create a new basic block to start insertion into.
77 BasicBlock *BB = BasicBlock::Create(*TheContext, "entry", TheFunction);
78 Builder->SetInsertPoint(BB);
79 // Record the function arguments in the NamedValues map.
80 // Then allocate them in other functions
81 NamedValues.clear();
82 for (auto &Arg : TheFunction->args())
83     NamedValues[std::string(Arg.getName())] = &Arg;
84
85 Value *RetVal = Body->codegen();
86 // Finish off the function.
87 Builder->CreateRet(RetVal);
88
89 return TheFunction;
90 }

```

Listing 1: Example function implementations

(b)

How to draw "For" statement CFG in general?

'for' '(' e1=exprpt ';' e2=exprpt ';' e3=exprpt ')' stmt

