

Computer Architecture

LAB 12

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BS-CS 4

Submitted to:

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Task 1:

INP

OUT

CLA

OUT

HLT

EXECUTING...

Enter Inputs, the first of which must be an Integer: 9

Output: 9 Output: 0

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

Task 2:

INP

CMA

OUT

HLT

Registers					
Name	Width	Data			
AC	16	1111	1111	1111	0011
AR	12	0000	0000	0001	
DR	16	0000	0000	0000	0000
E	1	0			
I	1	0			
IR	16	1110	0000	0000	0001
PC	12	0000	0000	0101	
S	1	1			

Registers					
Name	Width	Data			
AC	16	1111	1111	1111	1100
AR	12	0000	0000	0001	
DR	16	0000	0000	0000	0000
E	1	0			
I	1	0			
IR	16	1110	0000	0000	0001
PC	12	0000	0000	0101	
S	1	1			

EXECUTING...

Enter Inputs, the first of which must be an Integer: 3

Output: 3 Output: -4

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

Task 3:

INP

INC

OUT

HLT

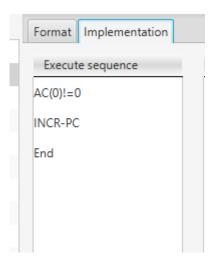
```
EXECUTING...
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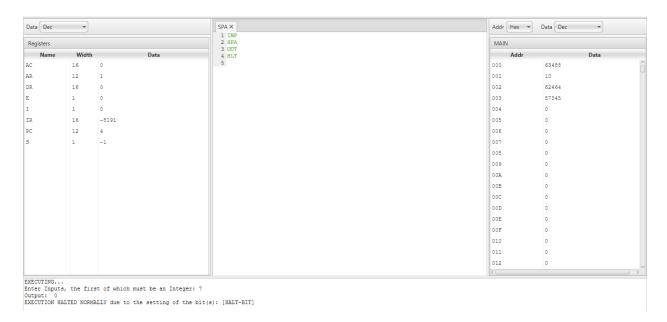
Enter Inputs, the first of which must be an Integer: 5

Output: 8

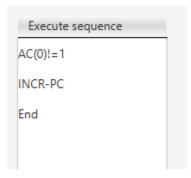
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

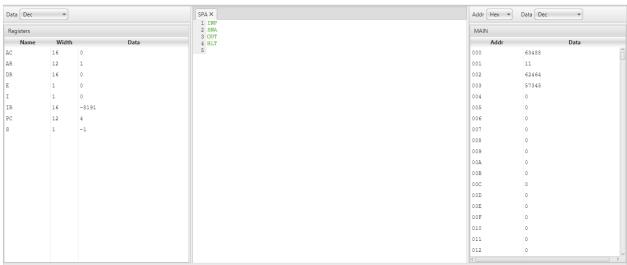
Task 4:





Task 5:





EXECUTING...
Enter Inputs, the first of which must be an Integer: -7
Output: 0
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALI-BIT]