Computer Architecture

LAB 8

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BS-CS 4

Submitted to:

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Task 1:

```
START:
INP
STA NUM
INP
CMA
INC
ADD NUM
STA NUM
INP
CMA
INC
ADD NUM
OUT
HLT

NUM: .data 1 0
```

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 8
Enter Inputs, the first of which must be an Integer: 5
Enter Inputs, the first of which must be an Integer: 1
Output:   2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 2:

START:
ADD NUM2
CMA
INC
ADD NUM1
OUT
HLT
NUM1: .data 1 9
NUM2: .data 1 2

```
EXECUTING...
Output:   7
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 3:

START:
INP
STA NUM
INP
CMA
INC
ADD NUM
STA RESULT
OUT
HLT
NUM: .data 1 0
RESULT: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 9
Enter Inputs, the first of which must be an Integer: 2
Output:  7
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```