



Computer Architecture

LAB 11

Fasiha Tariq-53289

BS-CS 4

Submitted to:

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Date: April 23rd, 2025

Task 1:

INP

ISZ 009

OUT

HLT

Data	Dec	
Registers		
Name	Width	Data
AC	16	0
AR	12	1
DR	16	0
E	1	0
I	1	0
IR	16	-8191
PC	12	4
S	1	-1

Addr	Hex	Data	Dec
MAIN			
Addr			Data
000			63488
001			36873
002			62464
003			57345
004			0
005			0
006			0
007			0
008			0
009			-3
00A			0
00B			0
00C			0
00D			0
00E			0
00F			0
010			0
011			0
012			0

EXECUTING...

Enter Inputs, the first of which must be an Integer: 8

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

Task 2:

INP

ISZ 008

OUT

HLT

Registers		
Name	Width	Data
AC	16	0
AR	12	1
DR	16	0
E	1	0
I	1	0
IR	16	-8191
PC	12	4
S	1	-1

Addr	Hex	Data	Dec
MAIN			
Addr	Data		
000	63488		
001	36872		
002	62464		
003	57345		
004	0		
005	0		
006	0		
007	0		
008	-1		
009	0		
00A	0		
00B	0		
00C	0		
00D	0		
00E	0		
00F	0		
010	0		
011	0		
012	0		

EXECUTING...

Enter Inputs, the first of which must be an Integer: 7

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]