



Computer Architecture

LAB 9

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BS-CS 4

Submitted to:

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OR Task:

START:

INP

STA NUM

INP

OR NUM

OUT

HLT

NUM: .data 1 0

```
EXECUTING...  
Enter Inputs, the first of which must be an Integer: 1  
Enter Inputs, the first of which must be an Integer: 1  
Output: 1  
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

NOT Task :

START:

INP

STA NUM

NOT NUM

OUT

HLT

NUM: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 1
Output:  -2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

NOR Task:

START:

INP

STA NUM

INP

OR NUM

NOT NUM

OUT

HLT

NUM: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 0
Enter Inputs, the first of which must be an Integer: 0
Output:  -1
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

NAND Task:

START:

INP

STA NUM

INP

AND NUM

NOT NUM

OUT

HLT

NUM: .data 1 0

```
EXECUTING...  
Enter Inputs, the first of which must be an Integer: 1  
Enter Inputs, the first of which must be an Integer: 0  
Output: -1  
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

XOR Task:

START:

INP

STA NUM

INP

XOR NUM

OUT

HLT

NUM: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 1
Enter Inputs, the first of which must be an Integer: 0
Output: 1
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 1
Enter Inputs, the first of which must be an Integer: 1
Output: 0
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```