Schematic

Group: http://www.hqyj.com/

Project:FS_MP157_MARS

Revision	Description	Date	Drawn
Ver 1.0	Releas version	2019.07.19	szl
Ver 2.0	RGB/HDMI signal correction	2020.04.08	szl

Notes:

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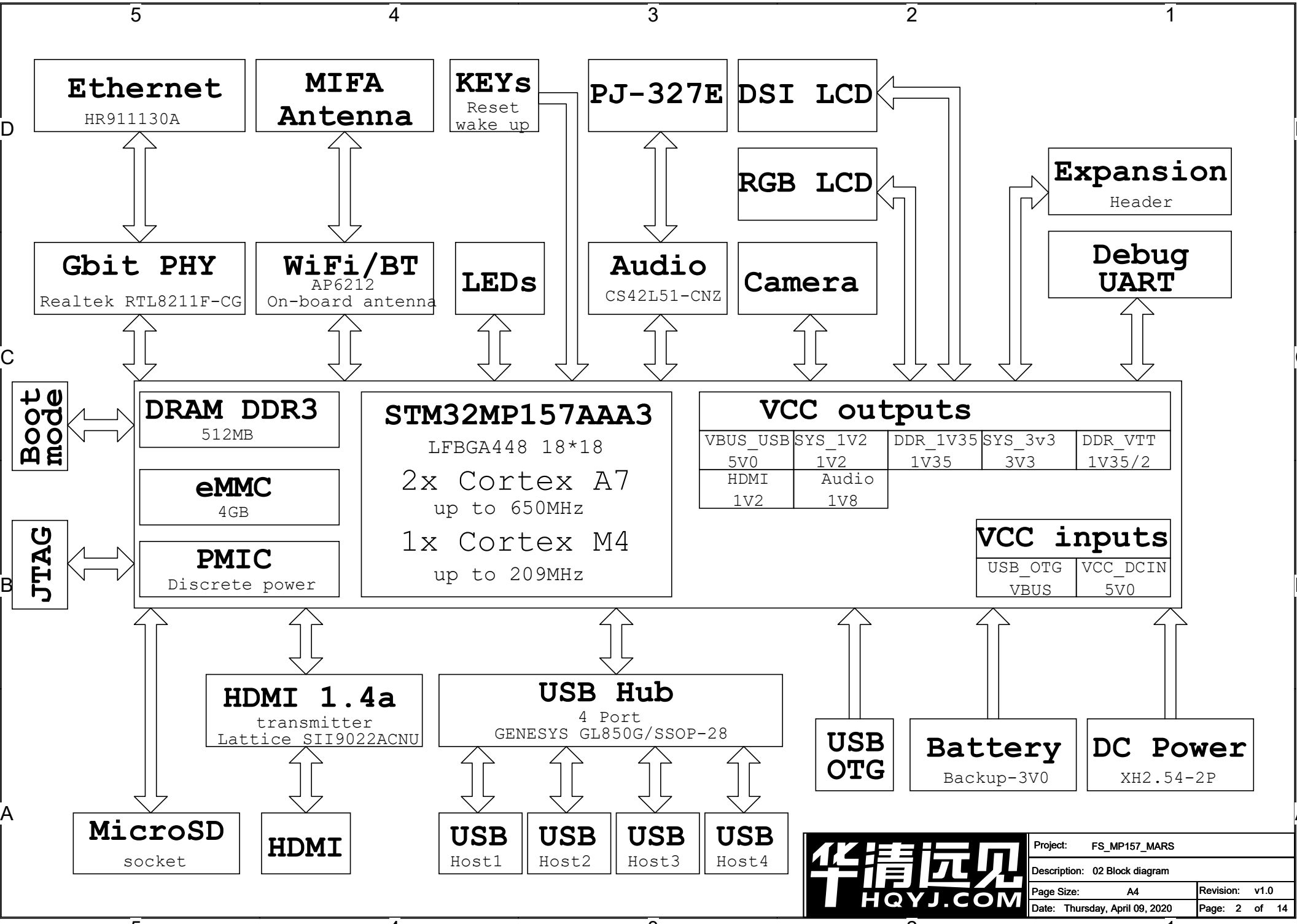

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Description: 02 Block diagram		
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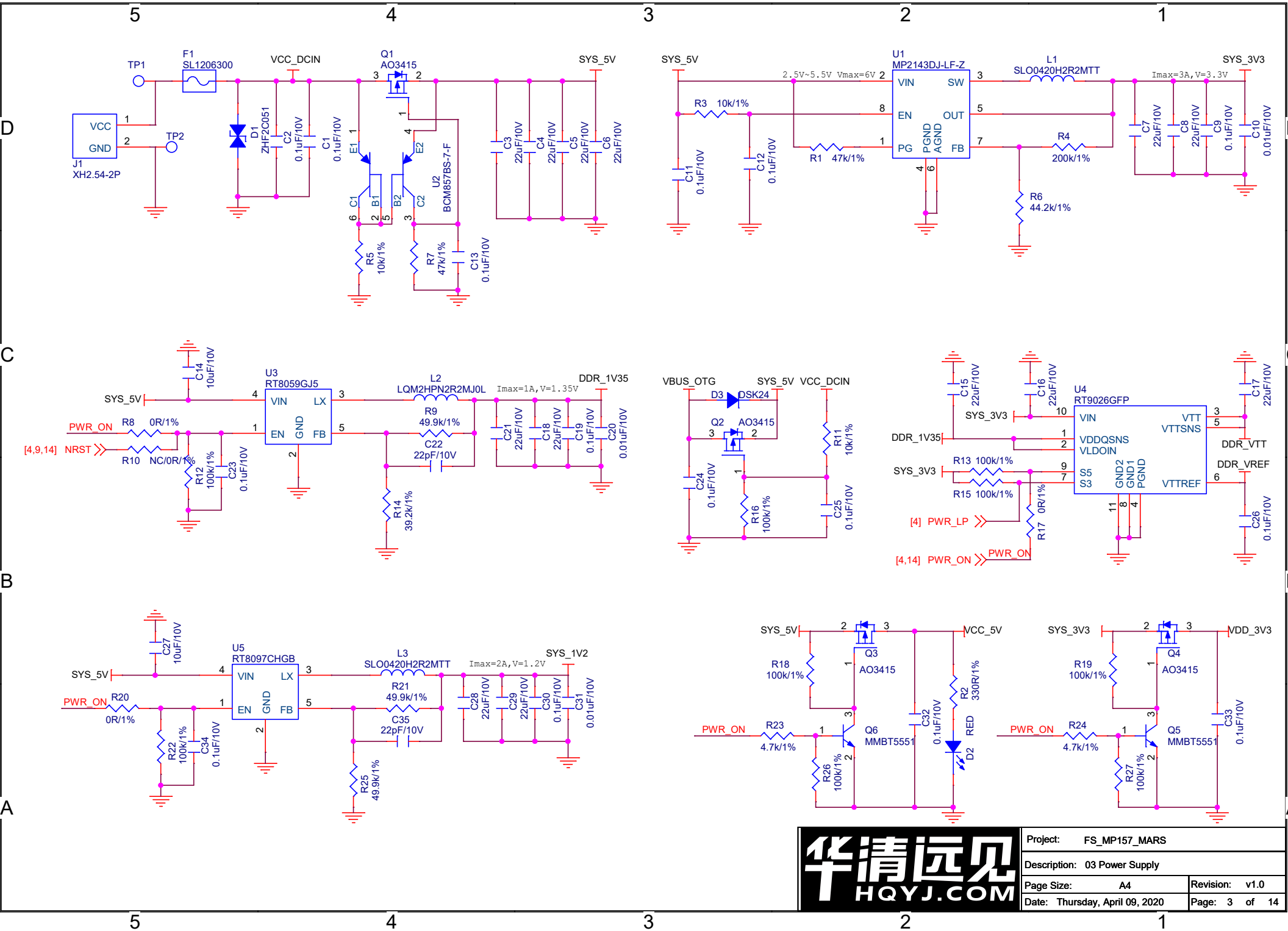
TP1
F1
SL1206300
VCC_DCIN
Q1
AO3415
SYS_5V
VCC
GND
J1
XH2.54-2P
TP2
D1
ZHF2C051
C2
0.1uF/10V
C1
0.1uF/10V
E1
E2
U2
BCM857BS-7-F
C1
B1
B2
C2
R5
10k/1%
R7
47k/1%
C13
0.1uF/10V
C3
22uF/10V
C4
22uF/10V
C5
22uF/10V
C6
22uF/10V
SYS_5V
2.5V~5.5V Vmax=6V
U1
MP2143DJ-LF-Z
VIN
SW
EN
OUT
PG
PGND
AGND
FB
L1
SLO0420H2R2MTT
SYS_3V3
Imax=3A,V=3.3V
R3
10k/1%
R1
47k/1%
C11
0.1uF/10V
C12
0.1uF/10V
R4
200k/1%
R6
44.2k/1%
C7
22uF/10V
C8
22uF/10V
C9
0.1uF/10V
C10
0.01uF/10V
C14
10uF/10V
U3
RT8059GJ5
SYS_5V
VIN
LX
EN
GND
FB
L2
LQM2HPN2R2MJ0L
Imax=1A,V=1.35V
DDR_1V35
PWR_ON
R8
0R/1%
[4,9,14]
NRST
R10
NC/0R/1%
R12
100k/1%
C23
0.1uF/10V
R9
49.9k/1%
C22
22pF/10V
R14
39.2k/1%
C21
22uF/10V
C18
22uF/10V
C19
0.1uF/10V
C20
0.01uF/10V
VBUS_OTG
D3
DSK24
SYS_5V
VCC_DCIN
Q2
AO3415
R11
10k/1%
C24
0.1uF/10V
R16
100k/1%
C25
0.1uF/10V
C15
22uF/10V
C16
22uF/10V
U4
RT9026GFP
SYS_3V3
VIN
VTT
VTTSNS
VDDQSNS
VLDOIN
S5
S3
GND2
GND1
PGND
VTTREF
DDR_1V35
DDR_VTT
DDR_VREF
R13
100k/1%
R15
100k/1%
R17
0R/1%
[4]
PWR_LP
[4,14]
PWR_ON
C17
22uF/10V
C26
0.1uF/10V
C27
10uF/10V
U5
RT8097CHGB
SYS_5V
VIN
LX
EN
GND
FB
L3
SLO0420H2R2MTT
Imax=2A,V=1.2V
SYS_1V2
PWR_ON
R20
0R/1%
R22
100k/1%
C34
0.1uF/10V
R21
49.9k/1%
C35
22pF/10V
R25
49.9k/1%
C28
22uF/10V
C29
22uF/10V
C30
0.1uF/10V
C31
0.01uF/10V
SYS_5V
VCC_5V
Q3
AO3415
R18
100k/1%
R2
330R/1%
PWR_ON
R23
4.7k/1%
Q6
MMBT5551
C32
0.1uF/10V
RED
D2
R26
100k/1%
SYS_3V3
VDD_3V3
Q4
AO3415
R19
100k/1%
PWR_ON
R24
4.7k/1%
Q5
MMBT5551
C33
0.1uF/10V
R27
100k/1%
华清远见
HQYJ.COM
Project: FS_MP157_MARS
Description: 03 Power Supply
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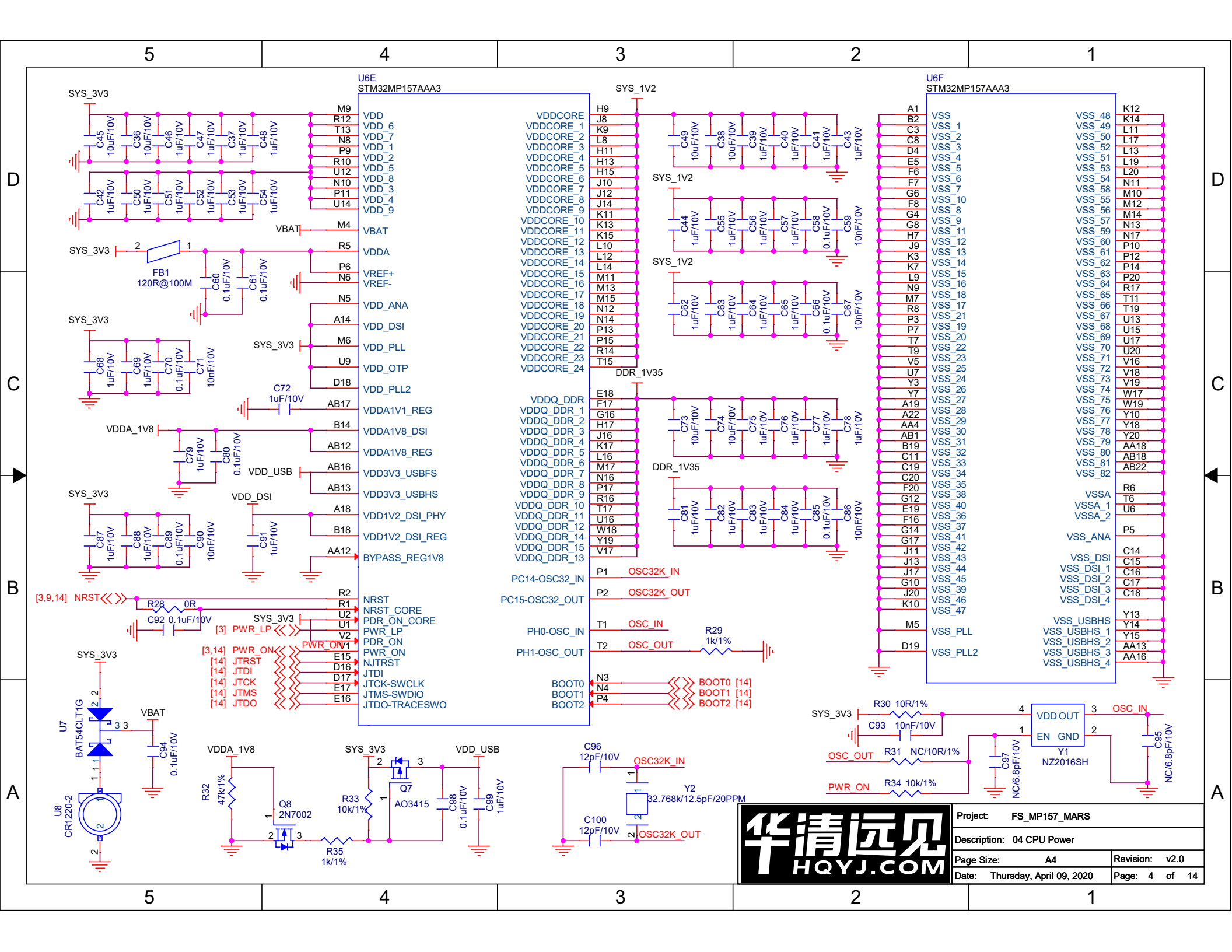
U6E
STM32MP157AAA3
U6F
STM32MP157AAA3
SYS_3V3
SYS_1V2
DDR_1V35
VBAT
VDDA_1V8
VDD_USB
VDD_DSI
FB1
120R@100M
[3,9,14] NRST
[3] PWR_LP
[3,14] PWR_ON
[14] JTRST
[14] JTDI
[14] JTCK
[14] JTMS
[14] JTDO
BOOT0 [14]
BOOT1 [14]
BOOT2 [14]
OSC32K_IN
OSC32K_OUT
OSC_IN
OSC_OUT
U7
BAT54CLT1G
U8
CR1220-2
Q7
AO3415
Q8
2N7002
Y1
NZ2016SH
Y2
32.768k/12.5pF/20PPM
华清远见
HQYJ.COM
Project: FS_MP157_MARS
Description: 04 CPU Power
Page Size: A4
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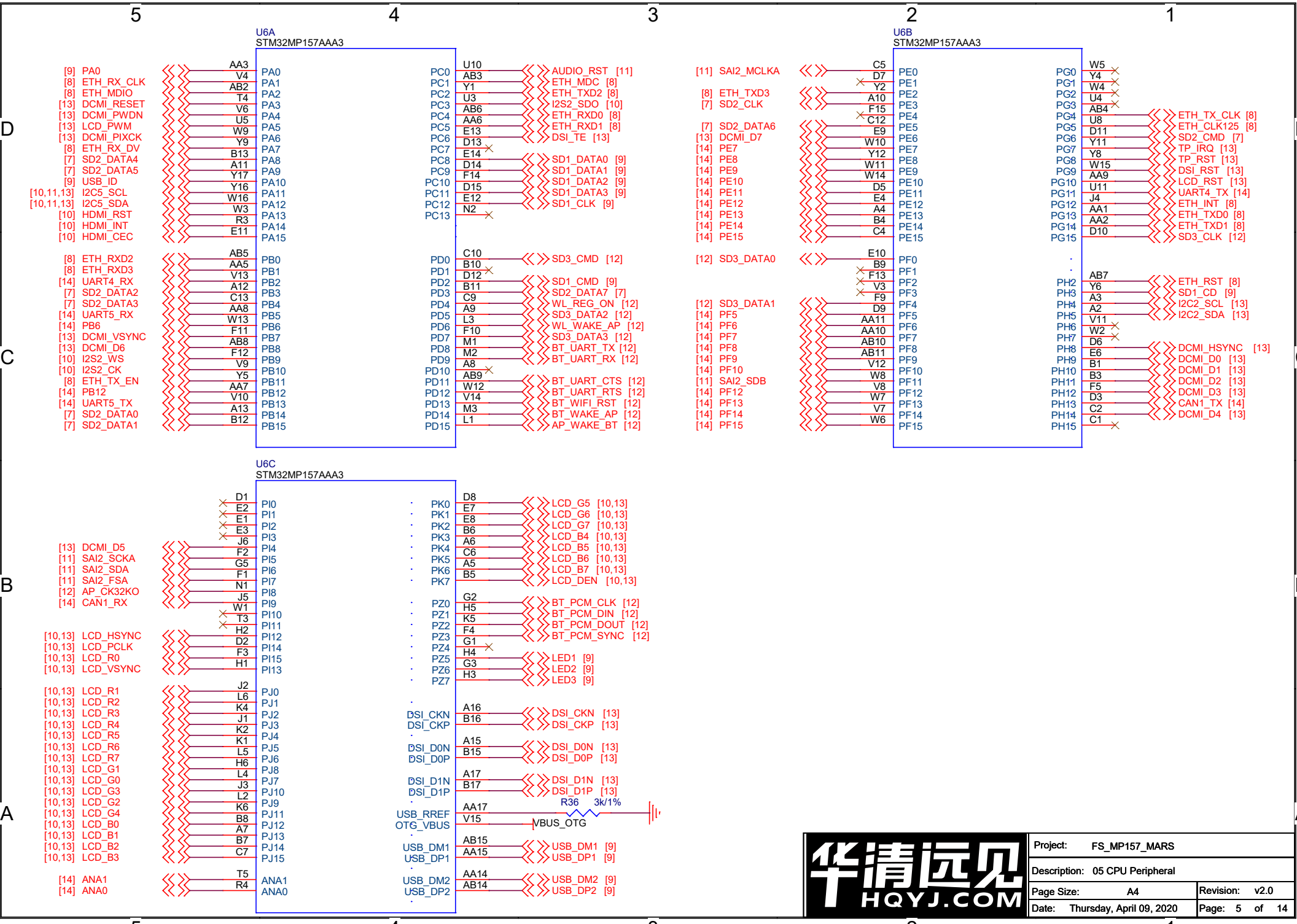
U6A
STM32MP157AAA3
[9] PA0
[8] ETH_RX_CLK
[8] ETH_MDIO
[13] DCMI_RESET
[13] DCMI_PWDN
[13] LCD_PWM
[13] DCMI_PIXCK
[8] ETH_RX_DV
[7] SD2_DATA4
[7] SD2_DATA5
[9] USB_ID
[10,11,13] I2C5_SCL
[10,11,13] I2C5_SDA
[10] HDMI_RST
[10] HDMI_INT
[10] HDMI_CEC
[8] ETH_RXD2
[8] ETH_RXD3
[14] UART4_RX
[7] SD2_DATA2
[7] SD2_DATA3
[14] UART5_RX
[14] PB6
[13] DCMI_VSYNC
[13] DCMI_D6
[10] I2S2_WS
[10] I2S2_CK
[8] ETH_TX_EN
[14] PB12
[14] UART5_TX
[7] SD2_DATA0
[7] SD2_DATA1
AA3 V4 AB2 T4 V6 U5 W9 Y9 B13 A11 Y17 Y16 W16 W3 R3 E11
AB5 AA5 V13 A12 C13 AA8 W13 F11 AB8 F12 V9 Y5 AA7 V10 A13 B12
PA0 PA1 PA2 PA3 PA4 PA5 PA6 PA7 PA8 PA9 PA10 PA11 PA12 PA13 PA14 PA15
PB0 PB1 PB2 PB3 PB4 PB5 PB6 PB7 PB8 PB9 PB10 PB11 PB12 PB13 PB14 PB15
PC0 PC1 PC2 PC3 PC4 PC5 PC6 PC7 PC8 PC9 PC10 PC11 PC12 PC13
PD0 PD1 PD2 PD3 PD4 PD5 PD6 PD7 PD8 PD9 PD10 PD11 PD12 PD13 PD14 PD15
U10 AB3 Y1 U3 AB6 AA6 E13 D13 E14 D14 F14 D15 E12 N2
C10 B10 D12 B11 C9 A9 L3 F10 M1 M2 A8 AB9 W12 V14 M3 L1
AUDIO_RST [11]
ETH_MDC [8]
ETH_TXD2 [8]
I2S2_SDO [10]
ETH_RXD0 [8]
ETH_RXD1 [8]
DSI_TE [13]
SD1_DATA0 [9]
SD1_DATA1 [9]
SD1_DATA2 [9]
SD1_DATA3 [9]
SD1_CLK [9]
SD3_CMD [12]
SD1_CMD [9]
SD2_DATA7 [7]
WL_REG_ON [12]
SD3_DATA2 [12]
WL_WAKE_AP [12]
SD3_DATA3 [12]
BT_UART_TX [12]
BT_UART_RX [12]
BT_UART_CTS [12]
BT_UART_RTS [12]
BT_WIFI_RST [12]
BT_WAKE_AP [12]
AP_WAKE_BT [12]
U6B
STM32MP157AAA3
[11] SAI2_MCLKA
[8] ETH_TXD3
[7] SD2_CLK
[7] SD2_DATA6
[13] DCMI_D7
[14] PE7
[14] PE8
[14] PE9
[14] PE10
[14] PE11
[14] PE12
[14] PE13
[14] PE14
[14] PE15
[12] SD3_DATA0
[12] SD3_DATA1
[14] PF5
[14] PF6
[14] PF7
[14] PF8
[14] PF9
[14] PF10
[11] SAI2_SDB
[14] PF12
[14] PF13
[14] PF14
[14] PF15
C5 D7 Y2 A10 F15 C12 E9 W10 Y12 W11 W14 D5 E4 A4 B4 C4
E10 B9 F13 V3 F9 D9 AA11 AA10 AB10 AB11 V12 W8 V8 W7 V7 W6
PE0 PE1 PE2 PE3 PE4 PE5 PE6 PE7 PE8 PE9 PE10 PE11 PE12 PE13 PE14 PE15
PF0 PF1 PF2 PF3 PF4 PF5 PF6 PF7 PF8 PF9 PF10 PF11 PF12 PF13 PF14 PF15
PG0 PG1 PG2 PG3 PG4 PG5 PG6 PG7 PG8 PG9 PG10 PG11 PG12 PG13 PG14 PG15
PH2 PH3 PH4 PH5 PH6 PH7 PH8 PH9 PH10 PH11 PH12 PH13 PH14 PH15
W5 Y4 W4 U4 AB4 U8 D11 Y11 Y8 W15 AA9 U11 J4 AA1 AA2 D10
AB7 Y6 A3 A2 V11 W2 D6 E6 B1 B3 F5 D3 C2 C1
ETH_TX_CLK [8]
ETH_CLK125 [8]
SD2_CMD [7]
TP_IRQ [13]
TP_RST [13]
DSI_RST [13]
LCD_RST [13]
UART4_TX [14]
ETH_INT [8]
ETH_TXD0 [8]
ETH_TXD1 [8]
SD3_CLK [12]
ETH_RST [8]
SD1_CD [9]
I2C2_SCL [13]
I2C2_SDA [13]
DCMI_HSYNC [13]
DCMI_D0 [13]
DCMI_D1 [13]
DCMI_D2 [13]
DCMI_D3 [13]
CAN1_TX [14]
DCMI_D4 [13]
U6C
STM32MP157AAA3
[13] DCMI_D5
[11] SAI2_SCKA
[11] SAI2_SDA
[11] SAI2_FSA
[12] AP_CK32KO
[14] CAN1_RX
[10,13] LCD_HSYNC
[10,13] LCD_PCLK
[10,13] LCD_R0
[10,13] LCD_VSYNC
[10,13] LCD_R1
[10,13] LCD_R2
[10,13] LCD_R3
[10,13] LCD_R4
[10,13] LCD_R5
[10,13] LCD_R6
[10,13] LCD_R7
[10,13] LCD_G1
[10,13] LCD_G0
[10,13] LCD_G3
[10,13] LCD_G2
[10,13] LCD_G4
[10,13] LCD_B0
[10,13] LCD_B1
[10,13] LCD_B2
[10,13] LCD_B3
[14] ANA1
[14] ANA0
D1 E2 E1 E3 J6 F2 G5 F1 N1 J5 W1 T3 H2 D2 F3 H1 J2 L6 K4 J1 K2 K1 L5 H6 L4 J3 L2 K6 B8 A7 B7 C7 T5 R4
PI0 PI1 PI2 PI3 PI4 PI5 PI6 PI7 PI8 PI9 PI10 PI11 PI12 PI14 PI15 PI13 PJ0 PJ1 PJ2 PJ3 PJ4 PJ5 PJ6 PJ8 PJ7 PJ10 PJ9 PJ11 PJ12 PJ13 PJ14 PJ15 ANA1 ANA0
PK0 PK1 PK2 PK3 PK4 PK5 PK6 PK7 PZ0 PZ1 PZ2 PZ3 PZ4 PZ5 PZ6 PZ7
DSI_CKN DSI_CKP DSI_D0N DSI_D0P DSI_D1N DSI_D1P USB_RREF OTG_VBUS USB_DM1 USB_DP1 USB_DM2 USB_DP2
D8 E7 E8 B6 A6 C6 A5 B5 G2 H5 K5 F4 G1 H4 G3 H3 A16 B16 A15 B15 A17 B17 AA17 V15 AB15 AA15 AA14 AB14
LCD_G5 [10,13]
LCD_G6 [10,13]
LCD_G7 [10,13]
LCD_B4 [10,13]
LCD_B5 [10,13]
LCD_B6 [10,13]
LCD_B7 [10,13]
LCD_DEN [10,13]
BT_PCM_CLK [12]
BT_PCM_DIN [12]
BT_PCM_DOUT [12]
BT_PCM_SYNC [12]
LED1 [9]
LED2 [9]
LED3 [9]
DSI_CKN [13]
DSI_CKP [13]
DSI_D0N [13]
DSI_D0P [13]
DSI_D1N [13]
DSI_D1P [13]
R36 3k/1%
VBUS_OTG
USB_DM1 [9]
USB_DP1 [9]
USB_DM2 [9]
USB_DP2 [9]
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Project: FS_MP157_MARS
Description: 05 CPU Peripheral
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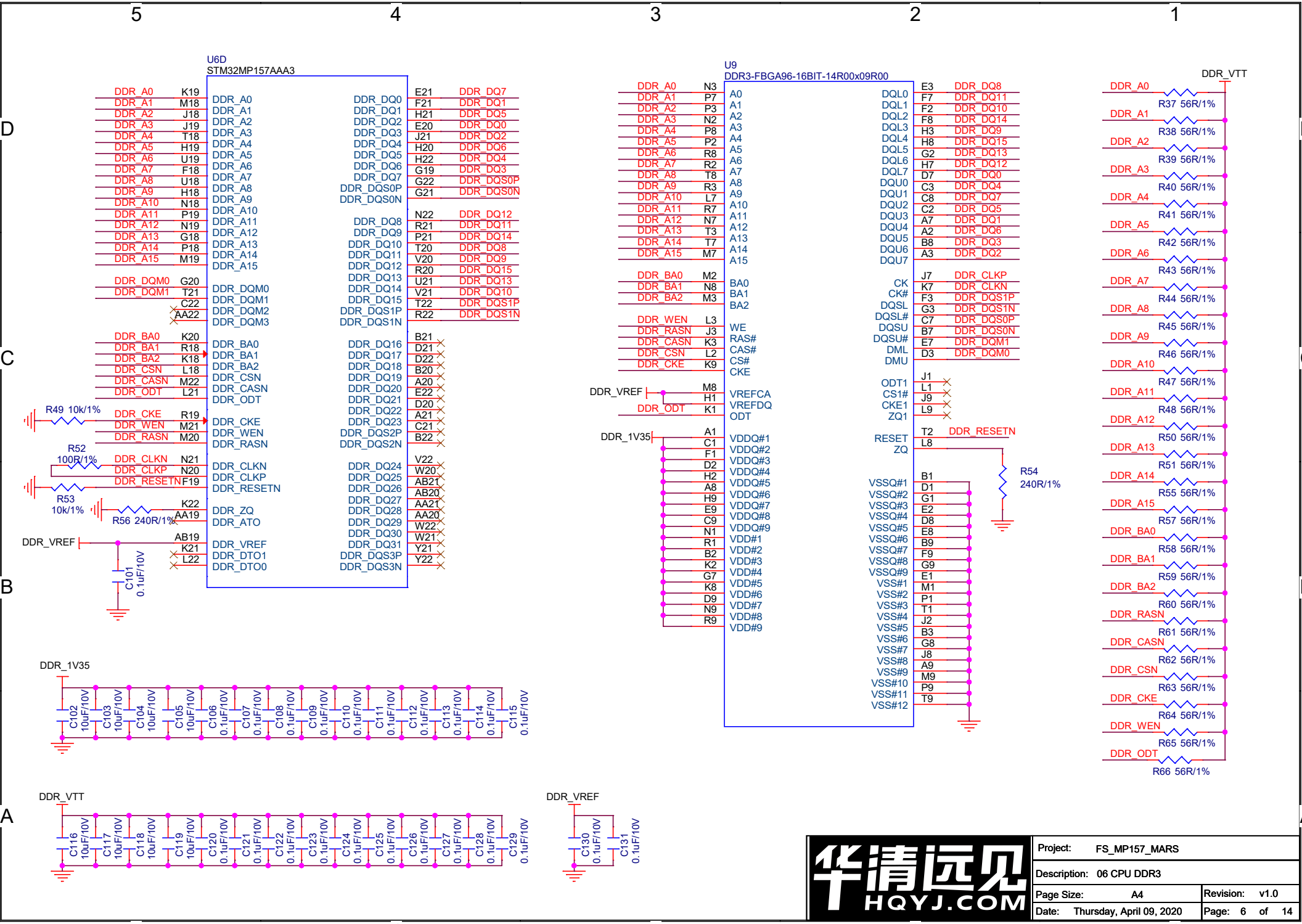
U6D
STM32MP157AAA3
DDR_A0 K19
DDR_A1 M18
DDR_A2 J18
DDR_A3 J19
DDR_A4 T18
DDR_A5 H19
DDR_A6 U19
DDR_A7 F18
DDR_A8 U18
DDR_A9 H18
DDR_A10 N18
DDR_A11 P19
DDR_A12 N19
DDR_A13 G18
DDR_A14 P18
DDR_A15 M19
DDR_DQM0 G20
DDR_DQM1 T21
C22
AA22
DDR_BA0 K20
DDR_BA1 R18
DDR_BA2 K18
DDR_CSN L18
DDR_CASN M22
DDR_ODT L21
R49 10k/1%
DDR_CKE R19
DDR_WEN M21
DDR_RASN M20
R52 100R/1%
DDR_CLKN N21
DDR_CLKP N20
DDR_RESETN F19
R53 10k/1%
K22
R56 240R/1%
AA19
AB19
K21
L22
DDR_VREF
C101 0.1uF/10V
DDR_A0 DDR_A1 DDR_A2 DDR_A3 DDR_A4 DDR_A5 DDR_A6 DDR_A7 DDR_A8 DDR_A9 DDR_A10 DDR_A11 DDR_A12 DDR_A13 DDR_A14 DDR_A15
DDR_DQM0 DDR_DQM1 DDR_DQM2 DDR_DQM3
DDR_BA0 DDR_BA1 DDR_BA2
DDR_CSN DDR_CASN DDR_ODT
DDR_CKE DDR_WEN DDR_RASN
DDR_CLKN DDR_CLKP DDR_RESETN
DDR_ZQ DDR_ATO
DDR_VREF DDR_DTO1 DDR_DTO0
DDR_DQ0 E21 DDR_DQ7
DDR_DQ1 F21 DDR_DQ1
DDR_DQ2 H21 DDR_DQ5
DDR_DQ3 E20 DDR_DQ0
DDR_DQ4 J21 DDR_DQ2
DDR_DQ5 H20 DDR_DQ6
DDR_DQ6 H22 DDR_DQ4
DDR_DQ7 G19 DDR_DQ3
DDR_DQS0P G22 DDR_DQS0P
DDR_DQS0N G21 DDR_DQS0N
DDR_DQ8 N22 DDR_DQ12
DDR_DQ9 R21 DDR_DQ11
DDR_DQ10 P21 DDR_DQ14
DDR_DQ11 T20 DDR_DQ8
DDR_DQ12 V20 DDR_DQ9
DDR_DQ13 R20 DDR_DQ15
DDR_DQ14 U21 DDR_DQ13
DDR_DQ15 V21 DDR_DQ10
DDR_DQS1P T22 DDR_DQS1P
DDR_DQS1N R22 DDR_DQS1N
DDR_DQ16 B21
DDR_DQ17 D21
DDR_DQ18 D22
DDR_DQ19 B20
DDR_DQ20 A20
DDR_DQ21 E22
DDR_DQ22 D20
DDR_DQ23 A21
DDR_DQS2P C21
DDR_DQS2N B22
DDR_DQ24 V22
DDR_DQ25 W20
DDR_DQ26 AB21
DDR_DQ27 AB20
DDR_DQ28 AA21
DDR_DQ29 AA20
DDR_DQ30 W22
DDR_DQ31 W21
DDR_DQS3P Y21
DDR_DQS3N Y22
U9
DDR3-FBGA96-16BIT-14R00x09R00
DDR_A0 N3
DDR_A1 P7
DDR_A2 P3
DDR_A3 N2
DDR_A4 P8
DDR_A5 P2
DDR_A6 R8
DDR_A7 R2
DDR_A8 T8
DDR_A9 R3
DDR_A10 L7
DDR_A11 R7
DDR_A12 N7
DDR_A13 T3
DDR_A14 T7
DDR_A15 M7
DDR_BA0 M2
DDR_BA1 N8
DDR_BA2 M3
DDR_WEN L3
DDR_RASN J3
DDR_CASN K3
DDR_CSN L2
DDR_CKE K9
DDR_VREF M8
H1
DDR_ODT K1
DDR_1V35 A1
C1 F1 D2 H2 A8 H9 E9 C9 N1 R1 B2 K2 G7 K8 D9 N9 R9
A0 A1 A2 A3 A4 A5 A6 A7 A8 A9 A10 A11 A12 A13 A14 A15
BA0 BA1 BA2
WE RAS# CAS# CS# CKE
VREFCA VREFDQ ODT
VDDQ#1 VDDQ#2 VDDQ#3 VDDQ#4 VDDQ#5 VDDQ#6 VDDQ#7 VDDQ#8 VDDQ#9
VDD#1 VDD#2 VDD#3 VDD#4 VDD#5 VDD#6 VDD#7 VDD#8 VDD#9
DQL0 E3 DDR_DQ8
DQL1 F7 DDR_DQ11
DQL2 F2 DDR_DQ10
DQL3 F8 DDR_DQ14
DQL4 H3 DDR_DQ9
DQL5 H8 DDR_DQ15
DQL6 G2 DDR_DQ13
DQL7 H7 DDR_DQ12
DQU0 D7 DDR_DQ0
DQU1 C3 DDR_DQ4
DQU2 C8 DDR_DQ7
DQU3 C2 DDR_DQ5
DQU4 A7 DDR_DQ1
DQU5 A2 DDR_DQ6
DQU6 B8 DDR_DQ3
DQU7 A3 DDR_DQ2
CK J7 DDR_CLKP
CK# K7 DDR_CLKN
DQSL F3 DDR_DQS1P
DQSL# G3 DDR_DQS1N
DQSU C7 DDR_DQS0P
DQSU# B7 DDR_DQS0N
DML E7 DDR_DQM1
DMU D3 DDR_DQM0
ODT1 J1
CS1# L1
CKE1 J9
ZQ1 L9
RESET T2 DDR_RESETN
ZQ L8
R54 240R/1%
VSSQ#1 B1
VSSQ#2 D1
VSSQ#3 G1
VSSQ#4 E2
VSSQ#5 D8
VSSQ#6 E8
VSSQ#7 B9
VSSQ#8 F9
VSSQ#9 G9
VSS#1 E1
VSS#2 M1
VSS#3 P1
VSS#4 T1
VSS#5 J2
VSS#6 B3
VSS#7 G8
VSS#8 J8
VSS#9 A9
VSS#10 M9
VSS#11 P9
VSS#12 T9
DDR_VTT
DDR_A0 R37 56R/1%
DDR_A1 R38 56R/1%
DDR_A2 R39 56R/1%
DDR_A3 R40 56R/1%
DDR_A4 R41 56R/1%
DDR_A5 R42 56R/1%
DDR_A6 R43 56R/1%
DDR_A7 R44 56R/1%
DDR_A8 R45 56R/1%
DDR_A9 R46 56R/1%
DDR_A10 R47 56R/1%
DDR_A11 R48 56R/1%
DDR_A12 R50 56R/1%
DDR_A13 R51 56R/1%
DDR_A14 R55 56R/1%
DDR_A15 R57 56R/1%
DDR_BA0 R58 56R/1%
DDR_BA1 R59 56R/1%
DDR_BA2 R60 56R/1%
DDR_RASN R61 56R/1%
DDR_CASN R62 56R/1%
DDR_CSN R63 56R/1%
DDR_CKE R64 56R/1%
DDR_WEN R65 56R/1%
DDR_ODT R66 56R/1%
DDR_1V35
C102 10uF/10V
C103 10uF/10V
C104 10uF/10V
C105 10uF/10V
C106 0.1uF/10V
C107 0.1uF/10V
C108 0.1uF/10V
C109 0.1uF/10V
C110 0.1uF/10V
C111 0.1uF/10V
C112 0.1uF/10V
C113 0.1uF/10V
C114 0.1uF/10V
C115 0.1uF/10V
DDR_VTT
C116 10uF/10V
C117 10uF/10V
C118 10uF/10V
C119 10uF/10V
C120 0.1uF/10V
C121 0.1uF/10V
C122 0.1uF/10V
C123 0.1uF/10V
C124 0.1uF/10V
C125 0.1uF/10V
C126 0.1uF/10V
C127 0.1uF/10V
C128 0.1uF/10V
C129 0.1uF/10V
DDR_VREF
C130 0.1uF/10V
C131 0.1uF/10V
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Project: FS_MP157_MARS
Description: 06 CPU DDR3
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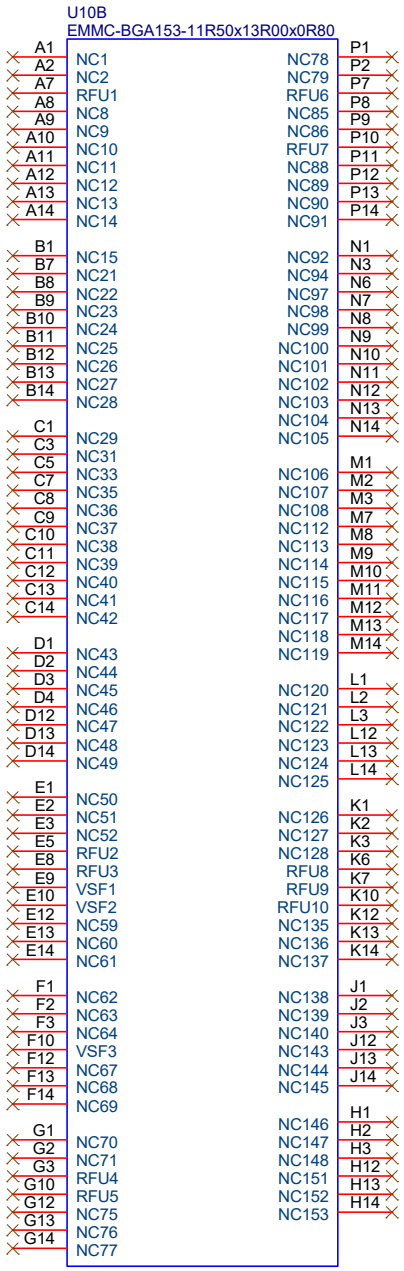

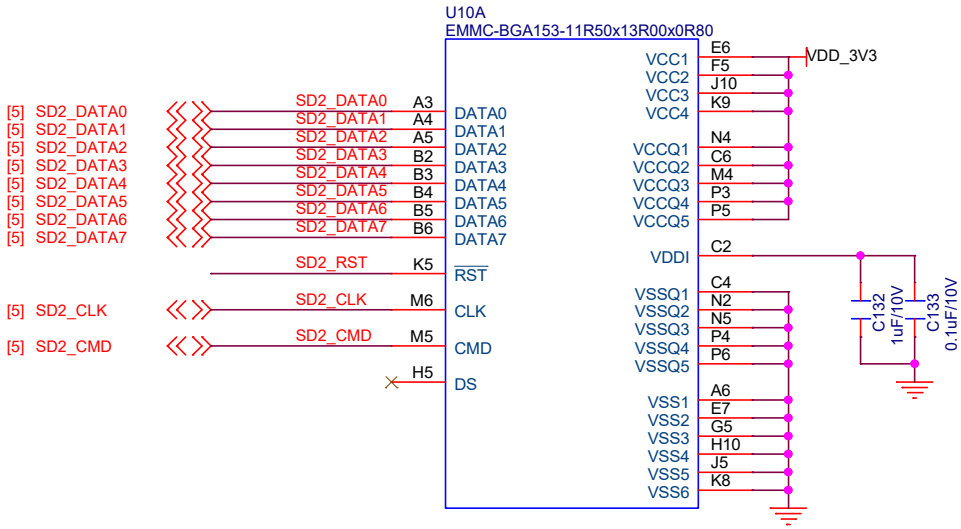

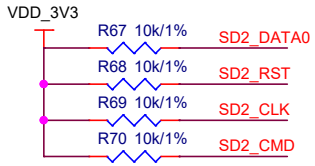

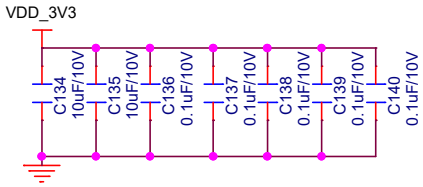

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Project: FS_MP157_MARS	
Description: 07 Flash Memory	
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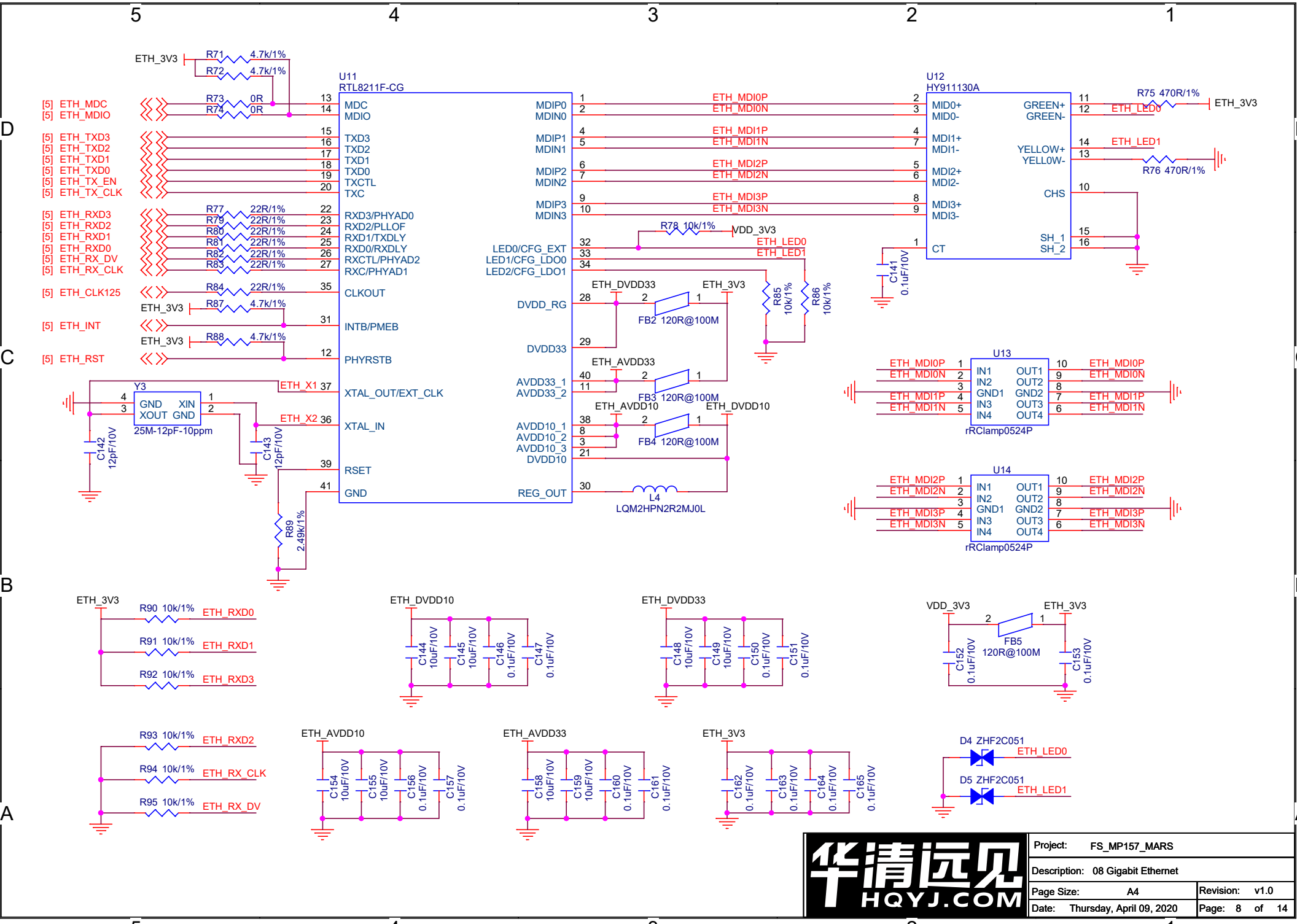

Project:	FS_MP157_MARS
Description:	08 Gigabit Ethernet
Page Size:	A4
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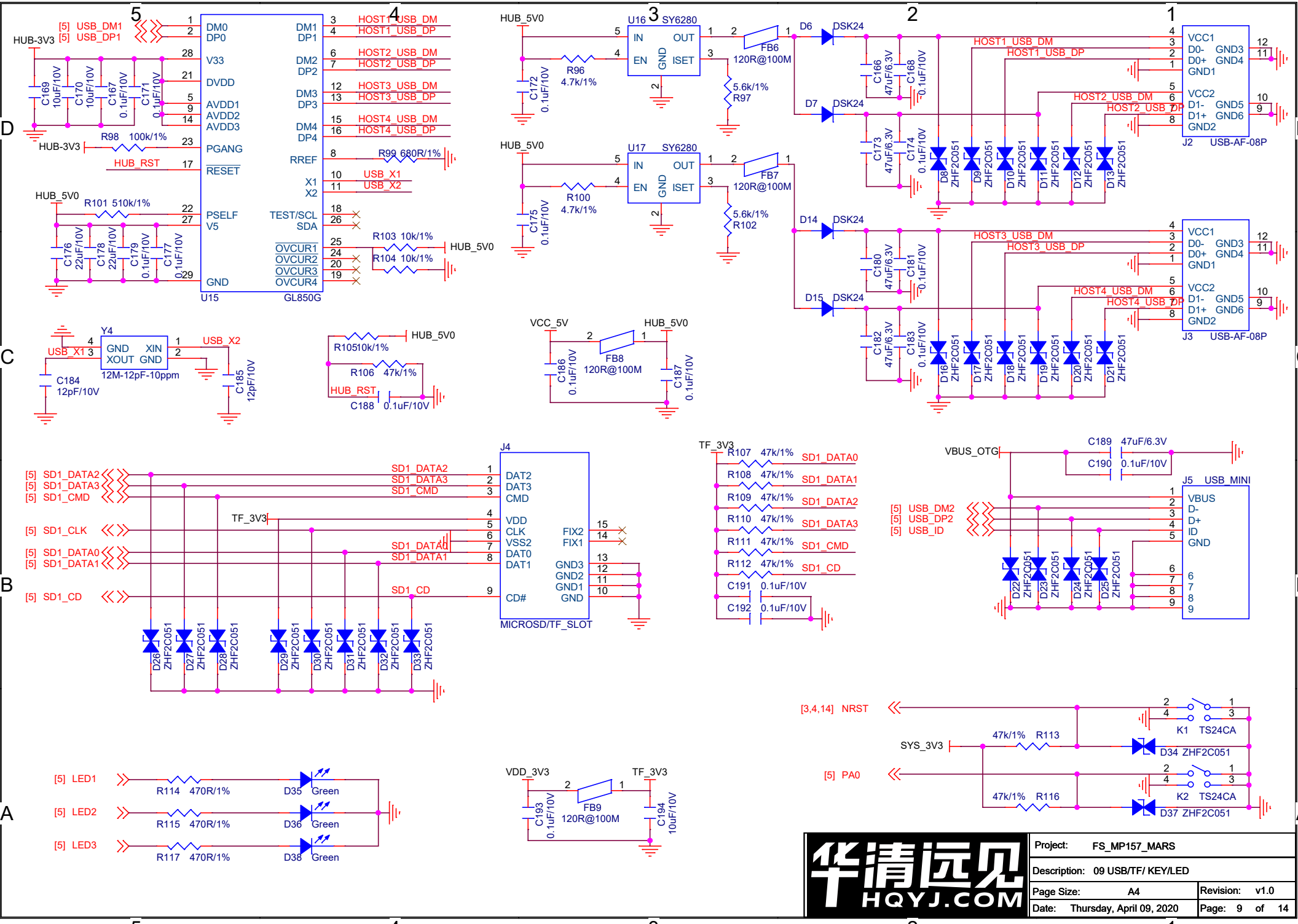

Project:	FS_MP157_MARS
Description:	09 USB/TF/ KEY/LED
Page Size:	A4
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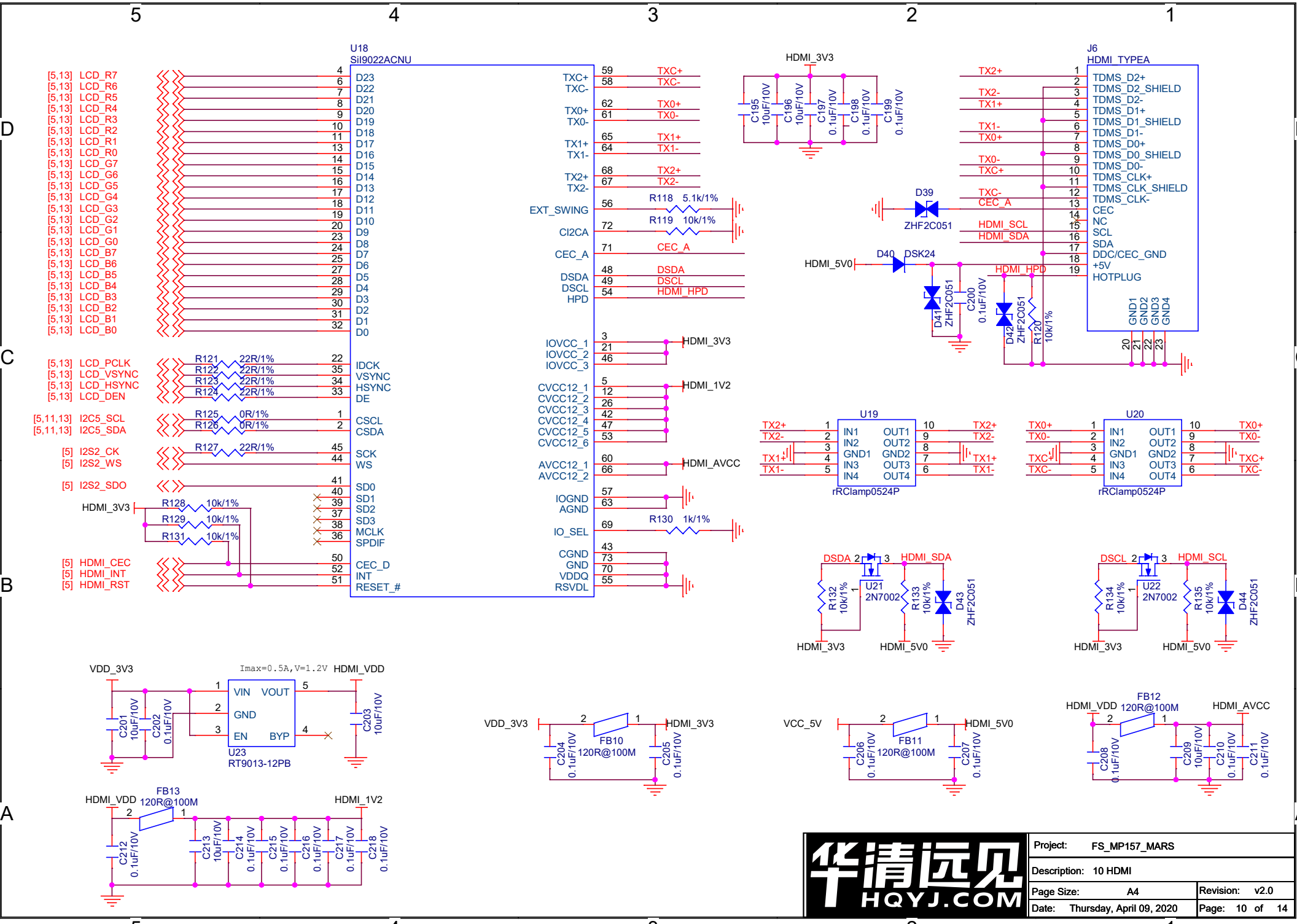

Project:	FS_MP157_MARS
Description:	10 HDMI
Page Size:	A4
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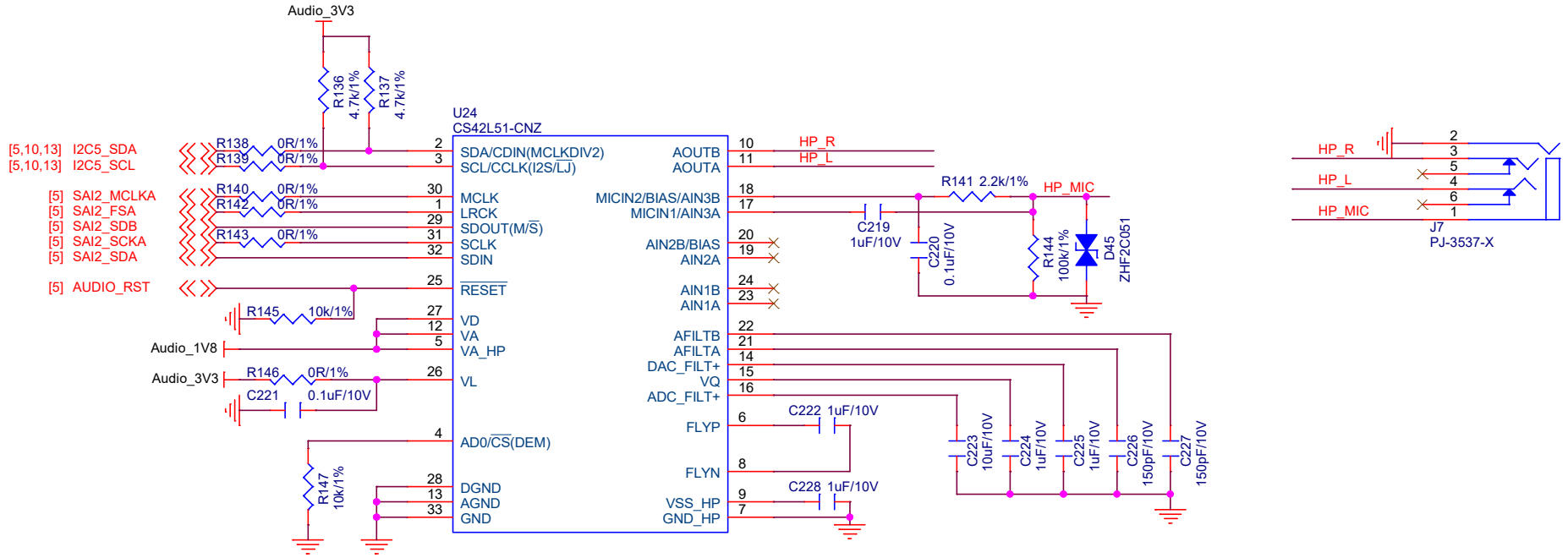

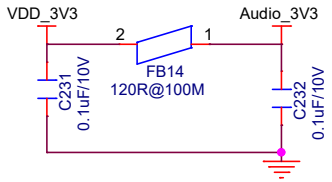

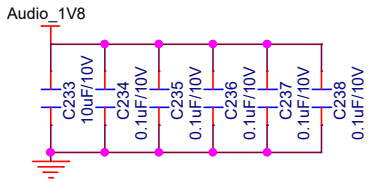

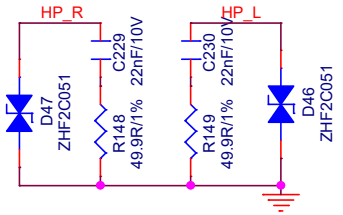

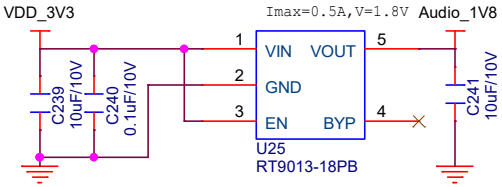

Project:	FS_MP157_MARS		
Description:	11 Audio		
Page Size:	A4	Revision:	v1.0
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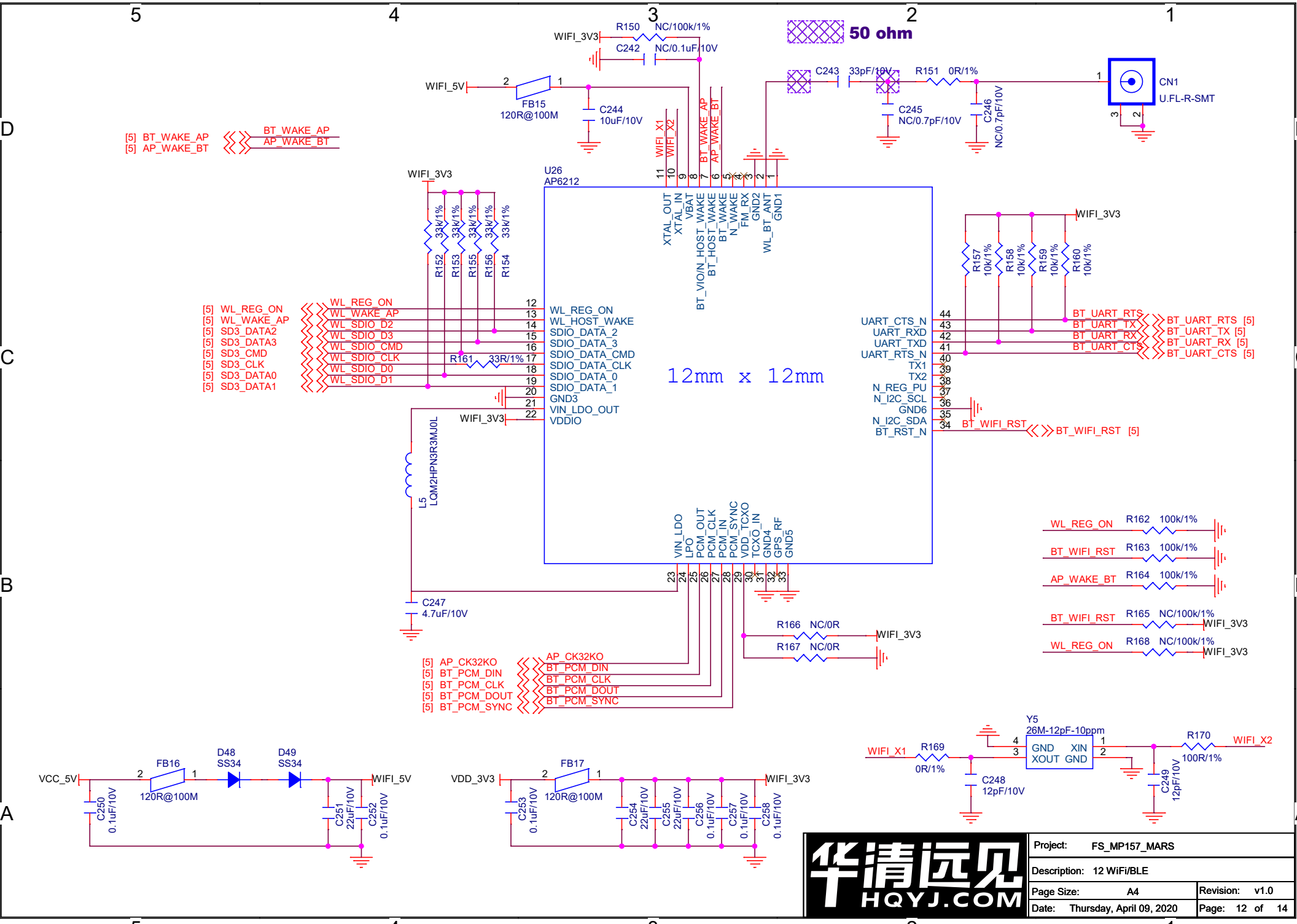
50 ohm
12mm x 12mm
U26
AP6212
WIFI_3V3
WIFI_5V
VDD_3V3
VCC_5V
R150 NC/100k/1%
C242 NC/0.1uF/10V
FB15 120R@100M
C244 10uF/10V
C243 33pF/10V
R151 0R/1%
C245 NC/0.7pF/10V
C246 NC/0.7pF/10V
CN1 U.FL-R-SMT
[5] BT_WAKE_AP
[5] AP_WAKE_BT
BT_WAKE_AP
AP_WAKE_BT
WIFI_X1
WIFI_X2
XTAL_OUT
XTAL_IN
VBAT
BT_VIO/N_HOST_WAKE
BT_HOST_WAKE
BT_WAKE
N_WAKE
FM_RX
GND2
WL_BT_ANT
GND1
R152 33k/1%
R153 33k/1%
R155 33k/1%
R156 33k/1%
R154 33k/1%
[5] WL_REG_ON
[5] WL_WAKE_AP
[5] SD3_DATA2
[5] SD3_DATA3
[5] SD3_CMD
[5] SD3_CLK
[5] SD3_DATA0
[5] SD3_DATA1
WL_REG_ON
WL_WAKE_AP
WL_SDIO_D2
WL_SDIO_D3
WL_SDIO_CMD
WL_SDIO_CLK
WL_SDIO_D0
WL_SDIO_D1
R161 33R/1%
WL_REG_ON
WL_HOST_WAKE
SDIO_DATA_2
SDIO_DATA_3
SDIO_DATA_CMD
SDIO_DATA_CLK
SDIO_DATA_0
SDIO_DATA_1
GND3
VIN_LDO_OUT
VDDIO
L5 LQM2HPN3R3MJ0L
C247 4.7uF/10V
VIN_LDO
LPO
PCM_OUT
PCM_CLK
PCM_IN
PCM_SYNC
VDD_TCXO
TCXO_IN
GND4
GPS_RF
GND5
R157 10k/1%
R158 10k/1%
R159 10k/1%
R160 10k/1%
UART_CTS_N
UART_RXD
UART_TXD
UART_RTS_N
TX1
TX2
N_REG_PU
N_I2C_SCL
GND6
N_I2C_SDA
BT_RST_N
BT_UART_RTS
BT_UART_TX
BT_UART_RX
BT_UART_CTS
BT_WIFI_RST
BT_UART_RTS [5]
BT_UART_TX [5]
BT_UART_RX [5]
BT_UART_CTS [5]
BT_WIFI_RST [5]
R166 NC/0R
R167 NC/0R
[5] AP_CK32KO
[5] BT_PCM_DIN
[5] BT_PCM_CLK
[5] BT_PCM_DOUT
[5] BT_PCM_SYNC
AP_CK32KO
BT_PCM_DIN
BT_PCM_CLK
BT_PCM_DOUT
BT_PCM_SYNC
WL_REG_ON R162 100k/1%
BT_WIFI_RST R163 100k/1%
AP_WAKE_BT R164 100k/1%
BT_WIFI_RST R165 NC/100k/1%
WL_REG_ON R168 NC/100k/1%
Y5 26M-12pF-10ppm
GND XIN
XOUT GND
R169 0R/1%
R170 100R/1%
C248 12pF/10V
C249 12pF/10V
FB16 120R@100M
D48 SS34
D49 SS34
C250 0.1uF/10V
C251 22uF/10V
C252 0.1uF/10V
FB17 120R@100M
C253 0.1uF/10V
C254 22uF/10V
C255 22uF/10V
C256 0.1uF/10V
C257 0.1uF/10V
C258 0.1uF/10V
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Project: FS_MP157_MARS
Description: 12 WiFi/BLE
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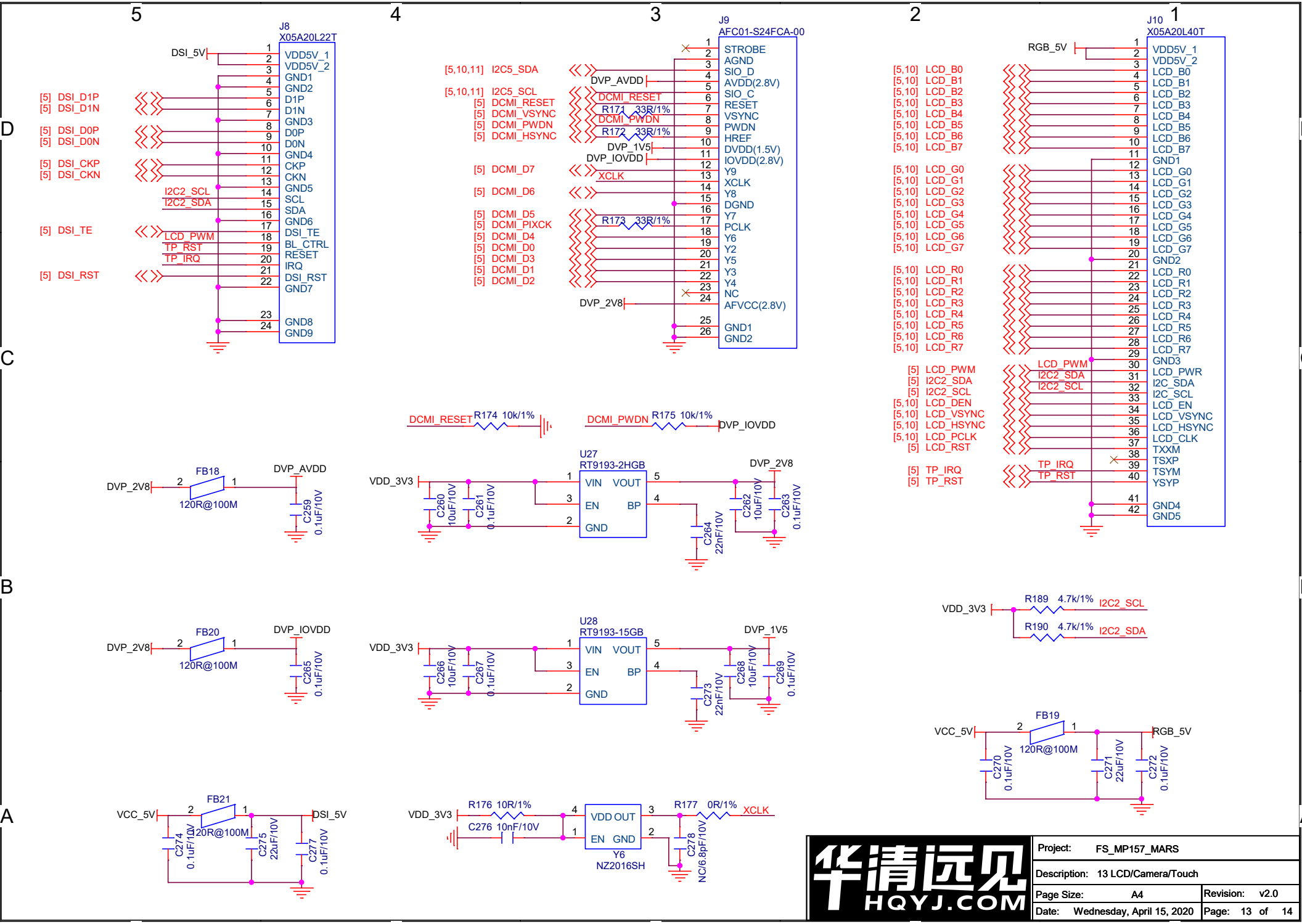

Project:	FS_MP157_MARS
Description:	13 LCD/Camera/Touch
Page Size:	A4
Revision:	v2.0
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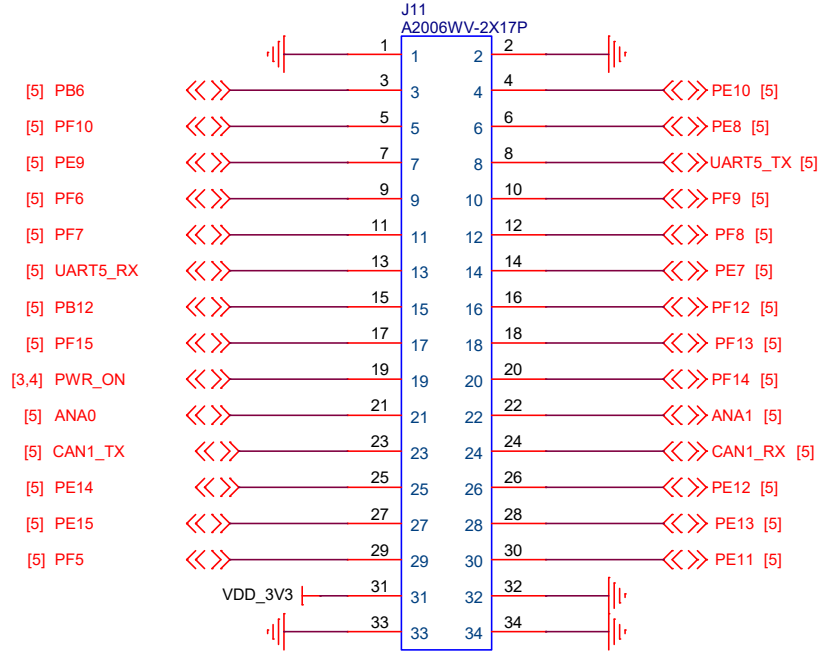
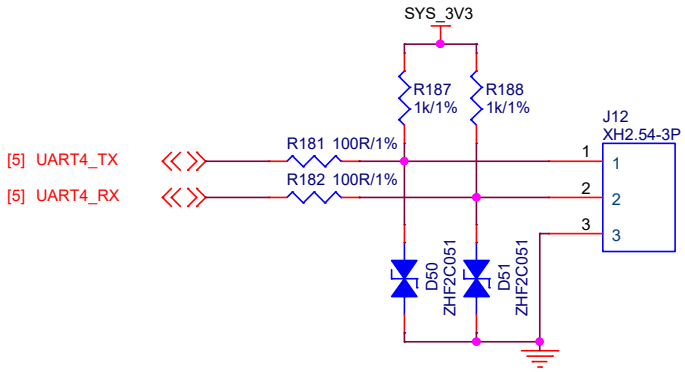
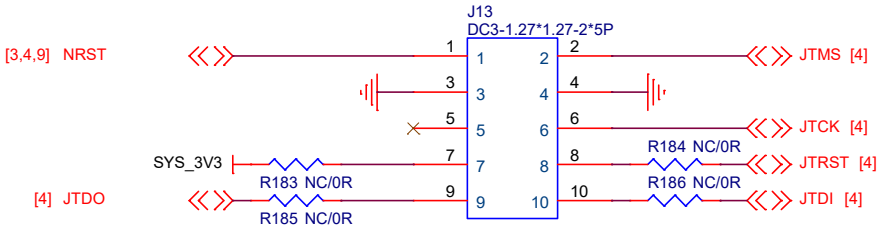
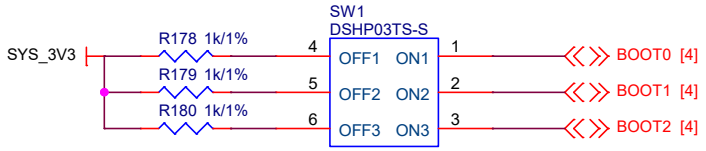


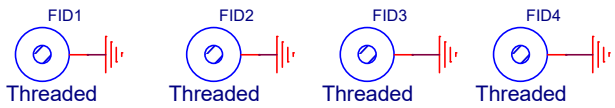






BOOT Mode	BOOT2	BOOT1	BOOT0
Reserver	1	0	0
Serial NOR-Flash on QUADSPI	0	0	1
Serial NAND-Flash on QUADSPI	1	1	1
NAND-Flash	0	1	1
eMMC	0	1	0
SD-Card	1	0	1
UART and USB	1	1	0
	0	0	0

"0" is floating signal or grounded
"1" is signal with pullup or tied to VDD



Project:	FS_MP157_MARS
Description:	14 GPIO/UART/BOOT/JTAG
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