

### Direct Mapped Caches 50x50

Block Size	4		8		16		32		64	
Cache Size	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256	%24	698	%13	379	%8	228	%5	160	%5	145
512	%23	672	%12	351	%7	195	%4	128	%4	114
1024	%22	660	%11	339	%6	172	%4	104	%3	77
2048	%22	655	%11	332	%6	172	%3	95	%2	60
4096	%22	648	%11	330	%6	172	%3	89	%2	52

### Direct Mapped Caches 50x50

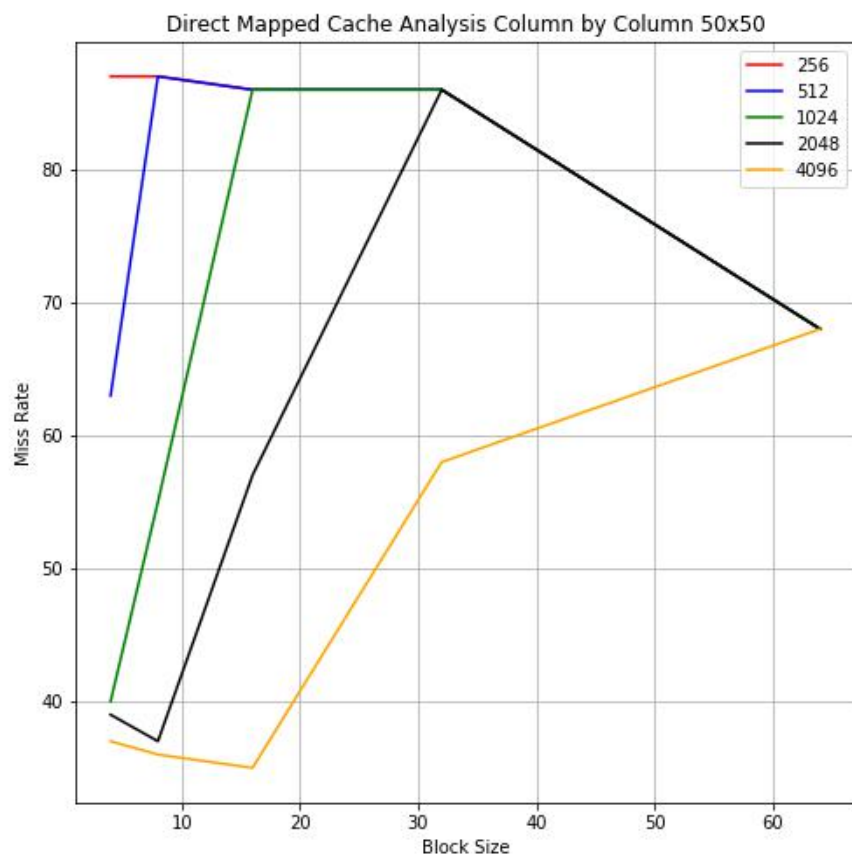
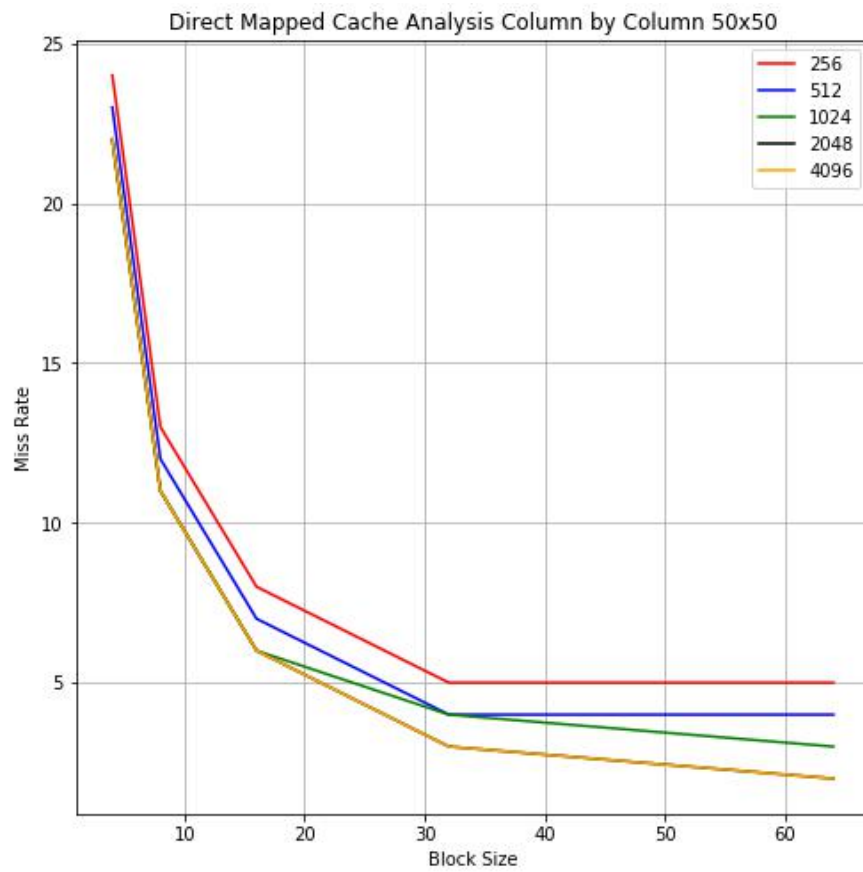
Block Size	4		8		16		32		64	
Cache Size	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256	%87	2580	%87	2567	%86	2560	%86	2558	%68	2019
512	%63	1862	%87	2564	%86	2558	%86	2555	%68	2016
1024	%40	1176	%55	1617	%86	2558	%86	2555	%68	2015
2048	%39	1147	%37	1084	%57	1698	%86	2555	%68	2015
4096	%37	1100	%36	1054	%35	1032	%58	1705	%68	2015

### Fully Associative Caches 50x50

Block Size	4						8						64					
Cache Size	Direct Mapped		Fully Ass. LRU		Fully Ass. Random		Direct Mapped		Fully Ass. LRU		Fully Ass. Random		Direct Mapped		Fully Ass. LRU		Fully Ass. Random	
	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC
256	%24	698	%22	656	%23	683	%13	379	%11	331	%12	356	%5	145	%5	145	%5	145
512	%23	672	%22	651	%23	670	%12	351	%11	328	%12	344	%4	114	%3	81	%3	68
1024	%22	660	%22	651	%22	658	%11	339	%11	328	%11	339	%3	77	%1	44	%2	54

### N-Way set associative caches for 50x50

Set Size (Blocks)	1		2		4		8		16	
Cache Size   Block Size (Word)	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256   4	%24	698	%23	683	%22	665	%22	654	%22	656
512   8	%12	351	%11	328	%11	328	%11	328	%11	328
2048   32	%3	95	%3	85	%3	85	%3	85	%3	85



### Direct Mapped Caches 200x200

Block Size	4		8		16		32		64	
Cache Size	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256	%25	10230	%13	5243	%7	2761	%4	1559	%3	1032
512	%25	10225	%13	5227	%7	2734	%4	1506	%2	929
1024	%25	10181	%13	5183	%7	2691	%4	1462	%2	879
2048	%25	10103	%13	5099	%6	2599	%3	1358	%2	755
4096	%25	10042	%12	5058	%6	2554	%3	1307	%2	694

### Direct Mapped Caches 200x200

Block Size	4		8		16		32		64	
Cache Size	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256	%98	40131	%99	40217	%99	40210	%99	40208	%99	40207
512	%98	40126	%99	40214	%99	40208	%99	40205	%99	40204
1024	%98	40126	%99	40214	%99	40208	%99	40205	%99	40203
2048	%98	40126	%99	40214	%99	40208	%99	40205	%99	40203
4096	%98	40126	%75	30547	%99	40208	%99	40205	%99	40203

### Fully Associative Caches 200x200

Block Size	4						8						64					
Cache Size	Direct Mapped		Fully Ass. LRU		Fully Ass. Random		Direct Mapped		Fully Ass. LRU		Fully Ass. Random		Direct Mapped		Fully Ass. LRU		Fully Ass. Random	
	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC	MR	MC
256	%25	10230	%25	10230	%25	10222	%13	5243	%13	5218	%13	5239	%3	1032	%3	1032	%3	1032
512	%25	10225	%25	10225	%25	10189	%13	5227	%13	5215	%13	5186	%2	929	%2	829	%2	905
1024	%25	10181	%25	10026	%25	10141	%13	5183	%13	5016	%13	5139	%2	879	%2	829	771	54

### N-Way set associative caches for 200x200

Set Size (Blocks)	1		2		4		8		16	
Cache Size   Block Size (Word)	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count	Miss Rate	Miss Count
256   4	%25	10230	%25	10226	%25	10227	%25	10228	%25	10230
512   8	%13	5227	%13	5215	%13	5215	%13	5215	%13	5215
2048   32	%3	1358	%3	1257	%3	1257	%3	1257	%3	1257

