Faculty of Computing

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Subject: Computer Architecture

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Task 1:

START:

INP

STA NUM

 INP

CMA

INC

ADD NUM

STA NUM

INP

CMA

INC

ADD NUM

 OUT

 HLT

NUM: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 3
Enter Inputs, the first of which must be an Integer: 6
Enter Inputs, the first of which must be an Integer: 3
Output:  -6
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 2:

START:

   ADD NUM1

   CMA

   INC

   ADD NUM2

   OUT

HLT

NUM2: .data 1 10

NUM1: .data 1 12

```
EXECUTING...
Output: -2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

Task 3:

START:

INP

STA NUM

 INP

CMA

INC

ADD NUM

STA NUM

INP

CMA

INC

ADD NUM

STA NUM ; for storing the answer

 OUT

 HLT

NUM: .data 1 0

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 3
Enter Inputs, the first of which must be an Integer: 5
Enter Inputs, the first of which must be an Integer: 1
Output:   -3
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```