

IRMP on STM32 - Construction manual

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Foreword

This manual is for two ST-Links. You could use a developer board and a USB-serial-TTL adapter instead. The ST-Links in USB stick cases are wired like red ST-Links.

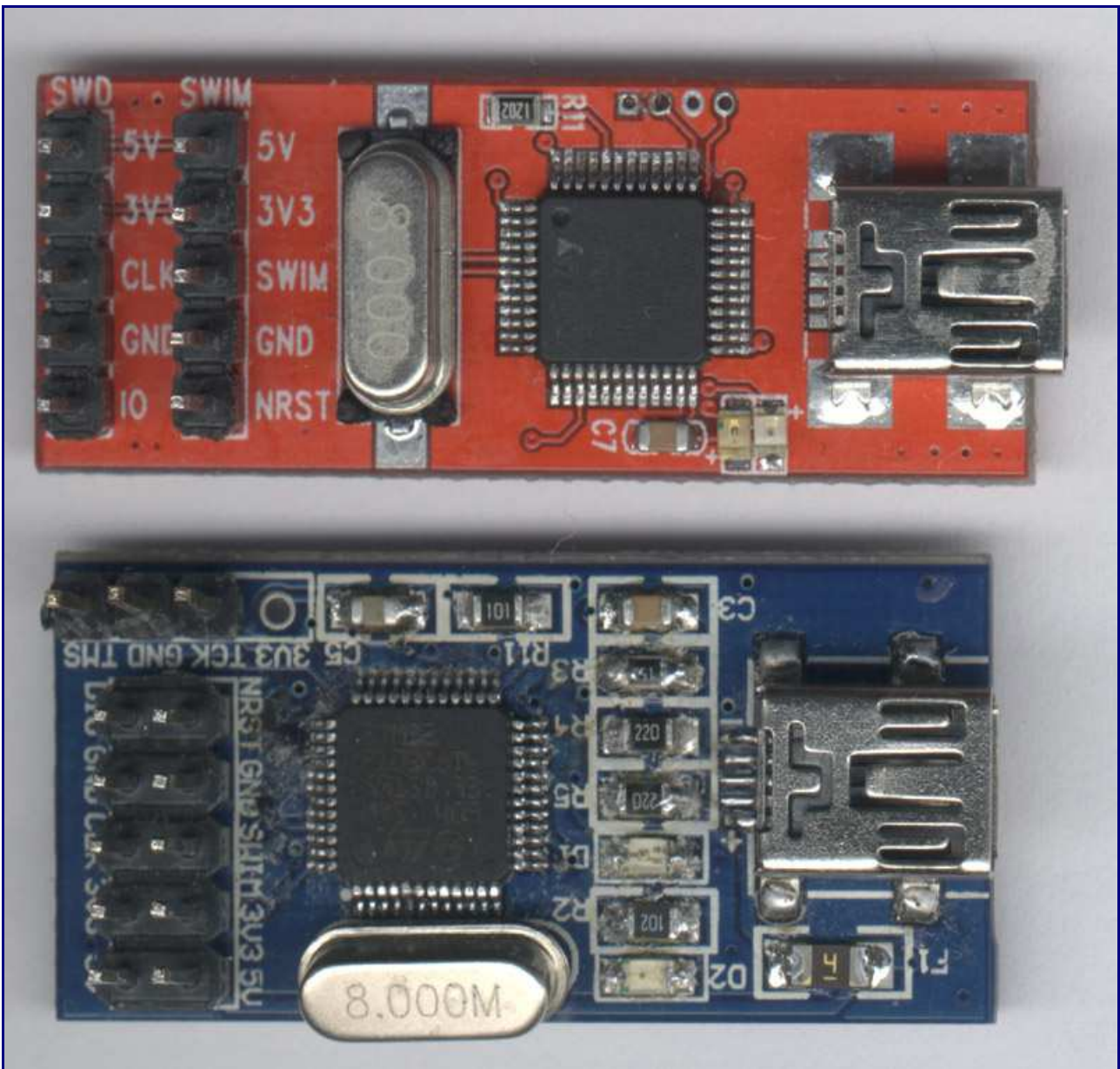
Buy

You need two ST-Link V2 emulators.

An ebay search for "ST-Link V2 emulator" shows, what is available.

I bought the red one from seller e_goto for 4.99 \$, so ca. 4 €.

The blue ones you get at <http://www.aliexpress.com/item/STLINK-V2-STM8-STM32-emulator-programmer-mini-STLINK-downloader/1446941850.html>



There are three pins soldered into the blue one for TCK and TMS
 On the blue one CLK and TCK and also DIO and TMS are exchanged.

Firmware Compilation (Linux)

gcc-arm-none-eabi is needed for compilation. If it is not already available through your distribution, you can get it from <https://launchpad.net/gcc-arm-embedded/+download> .

```
git clone git://github.com/j1rie/IRMP\_STM32.git IRMP_STM32
```

```
cd IRMP_STM32
```

```
cd F103
```

```
./make
```

Firmware Compilation (Windows)

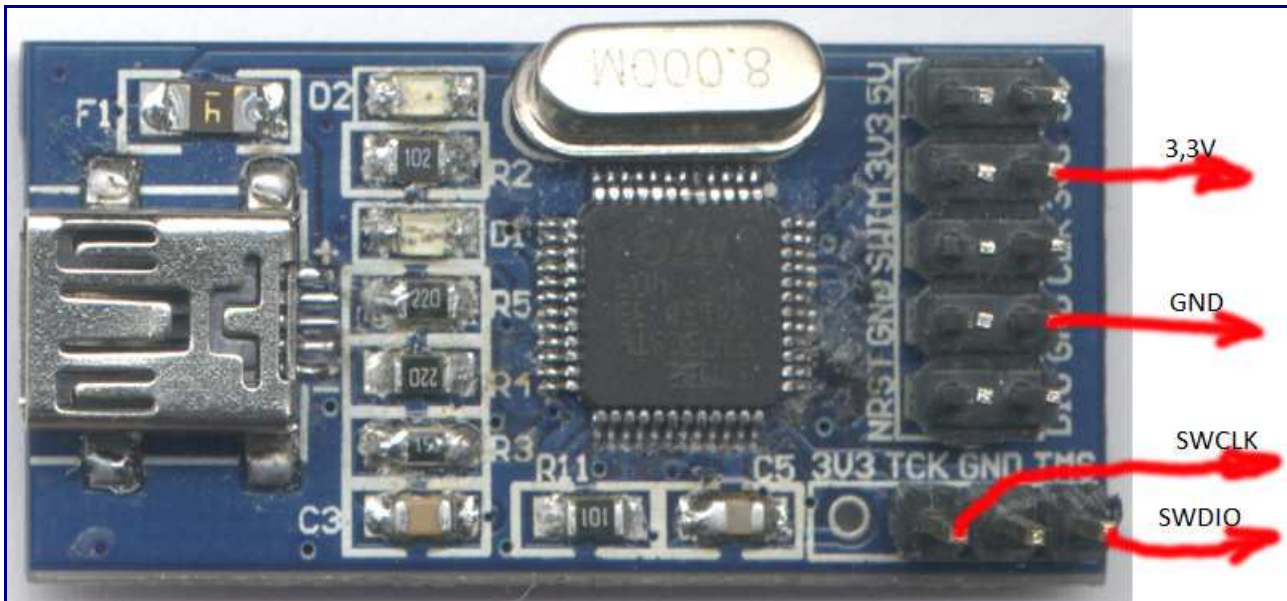
https://github.com/j1rie/IRMP_STM32

download and extract IRMP_STM32-master.zip

run prepare.bat in scripts

open IR.coproj in CoCoX and compile

Programmer

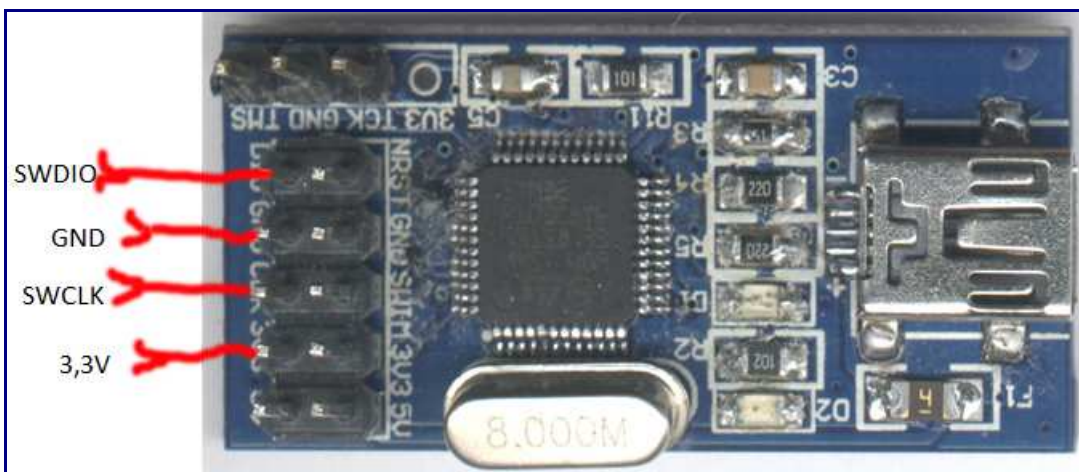


The blue as programmer.

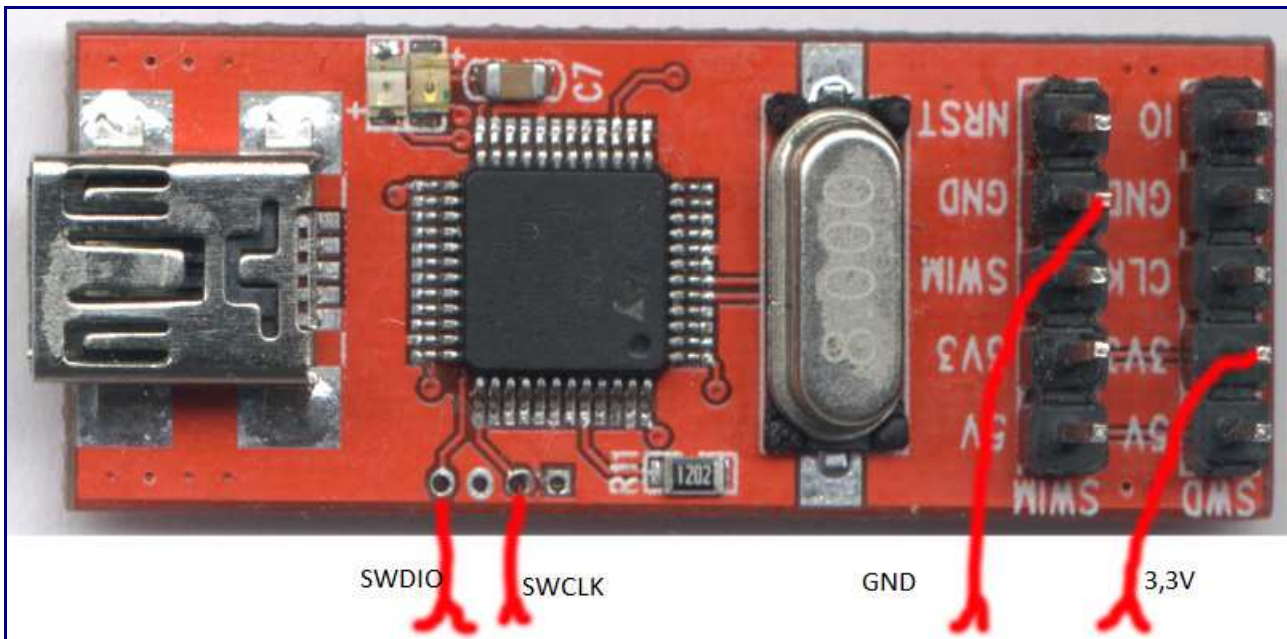


The red as programmer.

Target



The blue as target.



The red as target.

For the TSOP to work on SWIM, it might be necessary to remove the pullup on SWIM (image below). I have a (very old) TSOP, which does not work with the pullup. But a TSOP 34136 works even with the resistor.

With a red as programmer and a blue as target, you don't need to solder (if you go without the bootloader). Or you could simply press two contact pins against SWDIO und SWCLK during flashing.

Flashing (Linux)

Connect programmer and target, SWCLK - SWCLK, SWDIO - SWDIO, 3,3V - 3,3V, Gnd -Gnd.

You need to unlock the controller before the first flash with an openocd command:

```
$ openocd -f interface/stlink-v2.cfg -f target/stm32f1x_stlink.cfg -c "init" -c "halt" -c "stm32f1x
unlock 0" -c "shutdown"
```

Then flash the firmware:

```
git clone git://github.com/texane/stlink.git
cd stlink
```

```
./autogen.sh
```

```
./configure
```

```
./make
```

```
./st-flash erase
```

```
./st-flash --reset write /IRMP_STM32/F103C8/IR.bin 0x8000000
```

In some cases it is necessary, to reset the ST-Link at the beginning of flashing. You need to pull the marked area in the image to ground and release it shortly after the flash command.

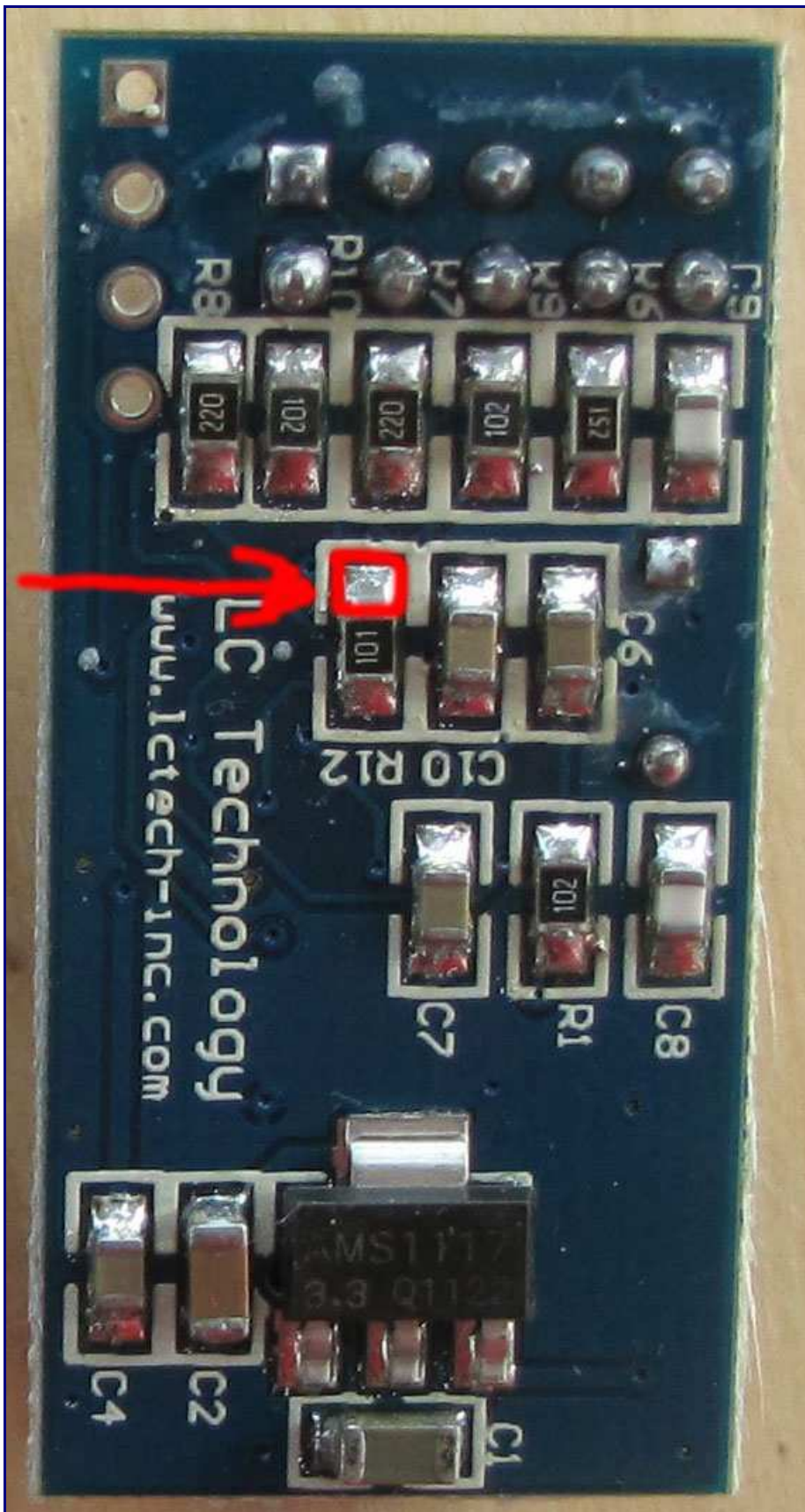
Flashing (Windows)

With the STM32 ST-LINK utility.

Connection and reset as in linux section.

Unlocking: -> Target -> Option Bytes, put Read Out Protection to Disabled.

Flashen: -> Target -> Program & Verify

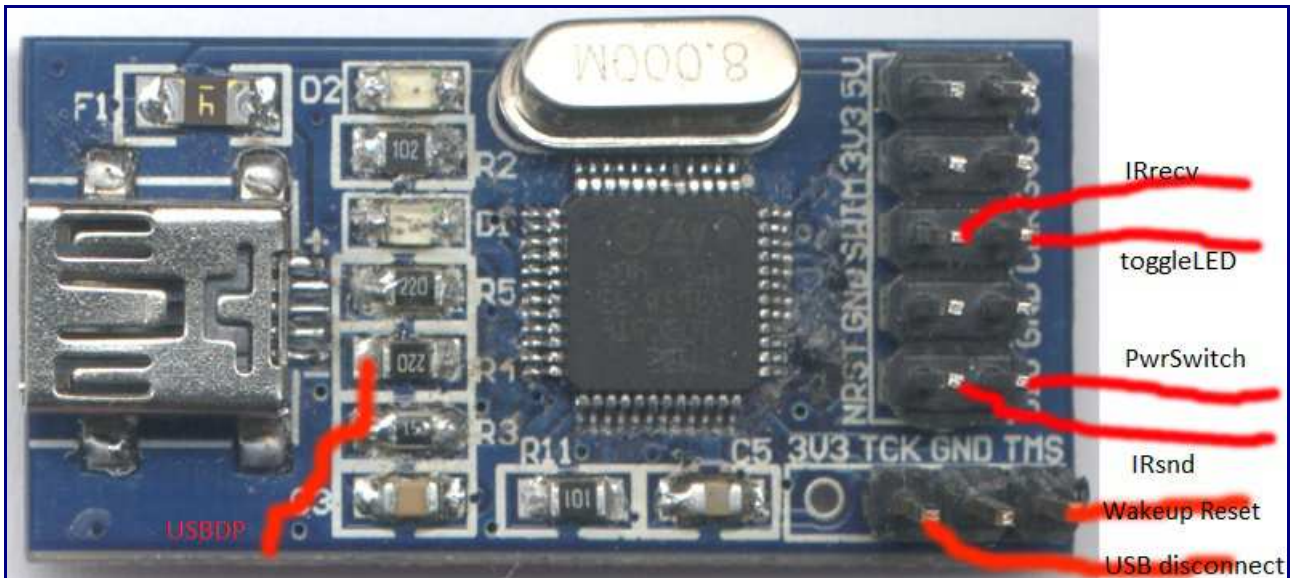


For reset pull the red marked to ground.

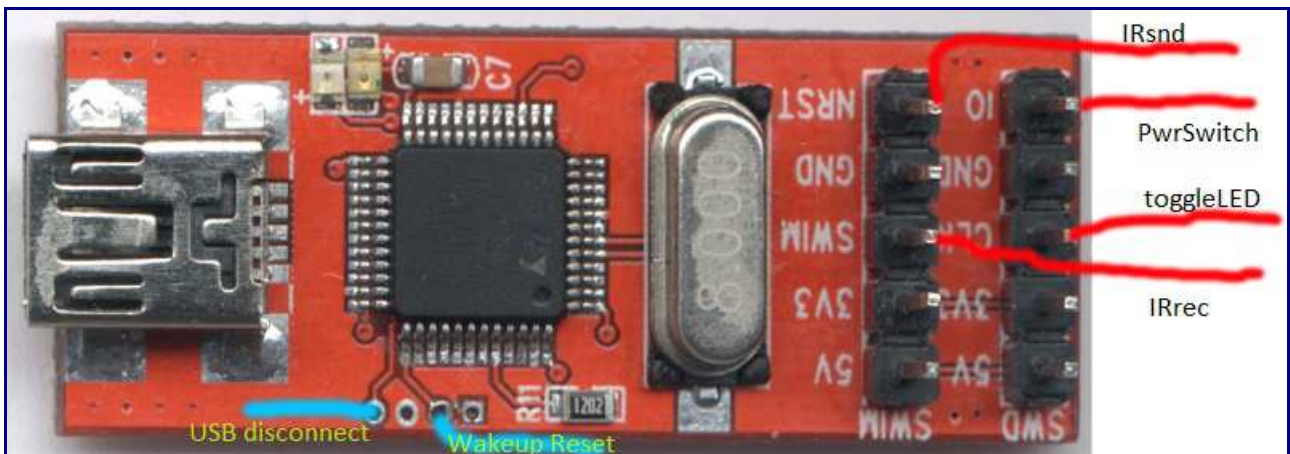


For reset pull the yellow marked to ground. Green marked the pullup on SWIM.

Connections



Connect the blue.



Connect the red.

Learning Wakeup

If the first wakeup is empty, the first received IR data will be stored into the first wakeup.

To renew learning pull the wakeup reset pin shortly to ground and push within the next 5 seconds the button, which shall be learned.

You could enter the new wakeup IR data as well manually or via remote control with the configuration program.

Testing (Linux)

With stm32IRconfig_gui: connect the ST-Link, start stm32IRconfig_gui, press "receive mode" and test.

With stm32IRconfig: connect the ST-Link, start stm32IRconfig, enter m for monitor mode and test.

With irmplired: connect the ST-Link, start irmplired and irw and test.

Testing (Windows)

With stm32IRconfig_gui: connect the ST-Link, start stm32IRconfig_gui, press "receive mode" and test.

With stm32IRconfig: connect the ST-Link, start stm32IRconfig, enter m for monitor mode and test.

With SimpleHIDWrite: connect the ST-Link and select it in SimpleHIDWrite and test.

<http://janaxelson.com/hidpage.htm#tools>

Expansion Board

There is a expansion board. With the connector on one side, it fits onto the blue, and with the connector on the other side it fits onto the red.

[Circuit](#)

<http://www.vdr-portal.de/board73-marktplatz/board75-verkaufen/p1220193-usb-ir-receiver-inkl-einschalter-open-source-sw-hw/#post1220193>

Bootloader (Linux)

```
git clone -b unify_platforms git://github.com/j1rie/maple-bootloader.git
```

```
cd maple-bootloader
```

```
make Platform=Dev (Developer board) or
```

```
make Platform=Red (Red) or
```

```
make PPlatform=Blue (Blue)
```

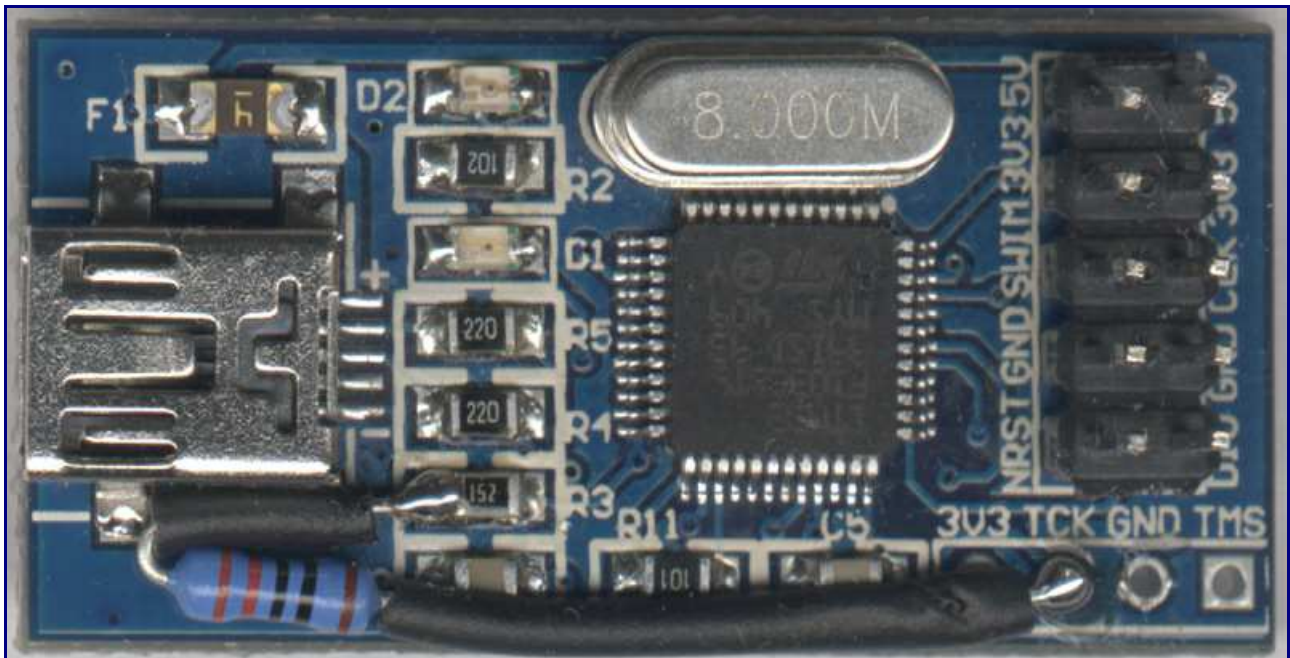
```
Then flash ./build/maple_boot.bin.
```

```
After that you flash like this:
```

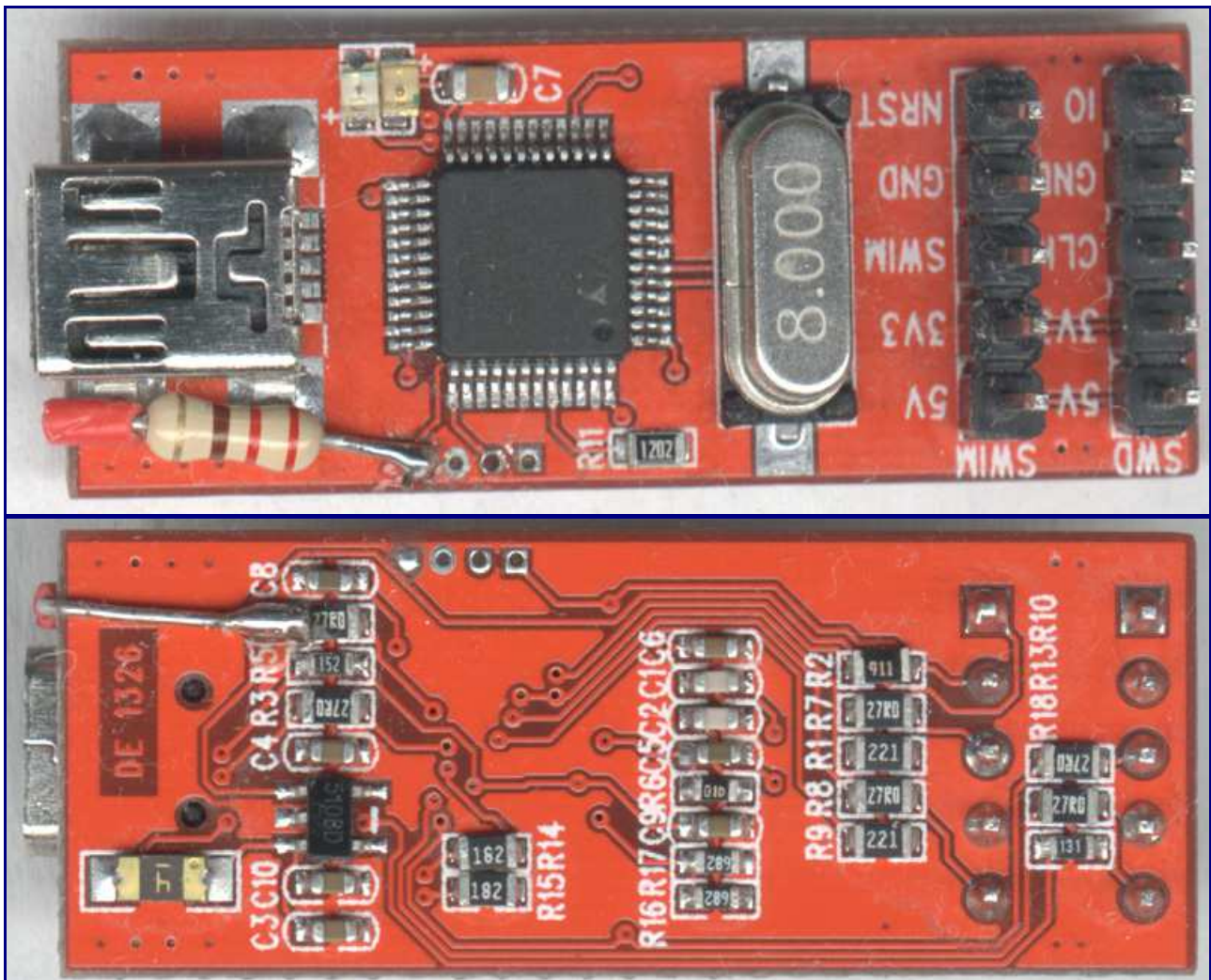
```
First run /path/FlashDFUSe.sh /path/newFirmware.bin*,
```

```
then connect the ST-Link.
```

(*) The firmware needs to be compiled with Bootloader=1.



The blue with pulldown resistor between USBDP and USB disconnect pin.



The red with pulldown resistor between USB DP and USB disconnect pin.

Automatic Start/Stop of `irmplircd(*)` on Re/Disconnect with `systemd`

Copy `80-irmp.rules` from `IRMP_STM32/irmplircd/` to `/etc/udev/rules.d/`.

Copy `irmplircd.service` from `IRMP_STM32/irmplircd/` to `/etc/systemd/system/`.

Activate service by '`systemctl enable irmplircd.service`'.

Adapt paths to `irmplircd` and `irmp_stm32.map` in `irmplircd.service` if needed.

The translation table `irmp_stm32.map` can be created and adapted with `stm32IRconfig_gui`.

(*) <https://github.com/realglotzi/irmplircd>

Minimalistic assembly for experienced users

Only for IR reception and turning the PC on. The TSOP is directly connected to the corresponding pins and the mainboard's active power switch pin is pulled down via the resistor (220 ohms)

(SimpleCircuit = 1). Those who know, what they're doing, could omit the resistor as well. Please be careful and don't damage your motherboard!

