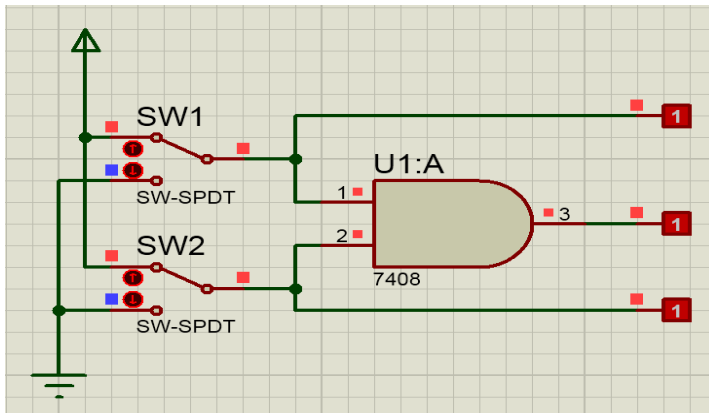


Nama :Fawwaz Haidar A.K

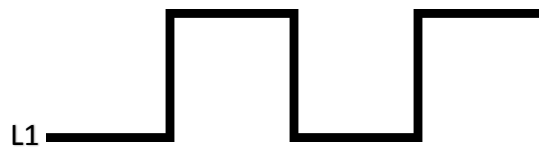
NIM : L200183143

Kelas : G

1. Gerbang AND

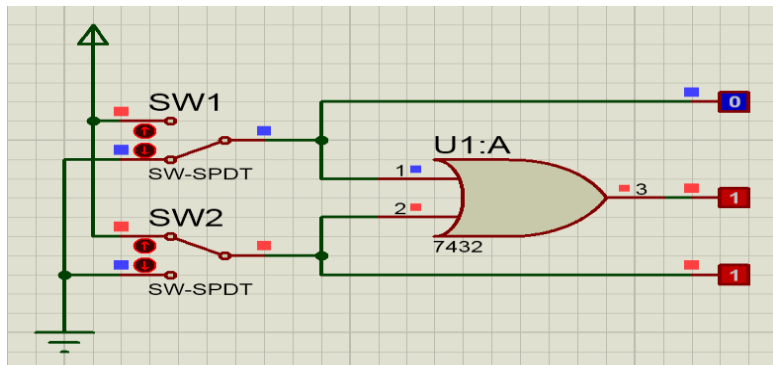


SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	0
0	1	0	1	0
1	1	1	1	1





2. Gerbang OR



L1



L2

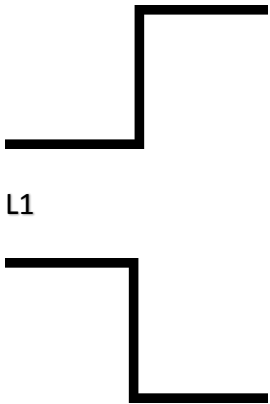


SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	1

The diagram illustrates a NOT gate circuit. It features a 5V supply and a ground. A switch (SW1) is connected to the 5V supply. A switch-mode pushdown toggle (SW-SPDT) is connected to the output of SW1. The output of SW-SPDT is connected to the input of a 7404 hex inverter (U1:A). The output of the inverter is labeled '1' (high) and '0' (low).

SW 1	L2	L1
0	0	1
1	1	0

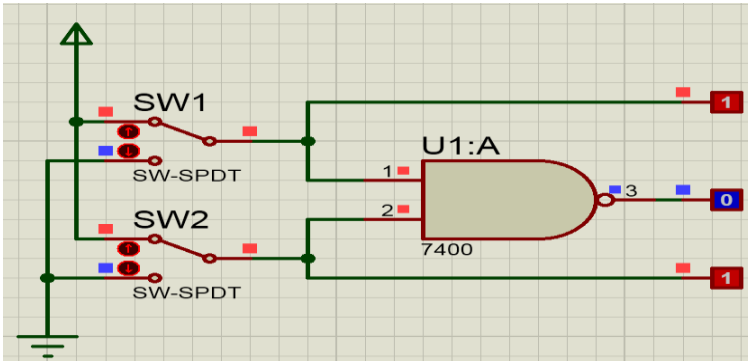
L2



L1

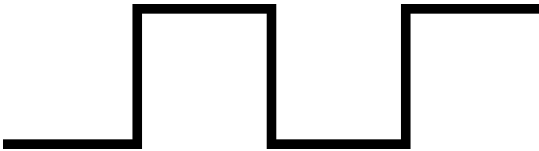
Percobaan 4. NAND

SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	1



0	1	0	1	1
1	1	1	1	0

L1



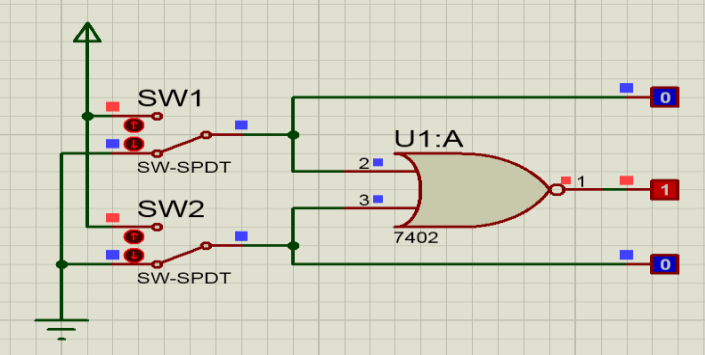
L2



L3



Percobaan 5. NOR



L1



L2

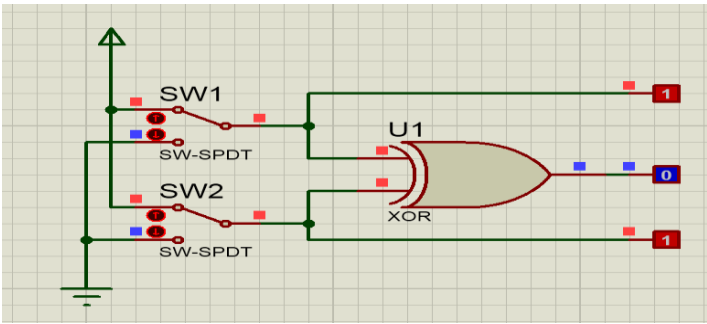


L3

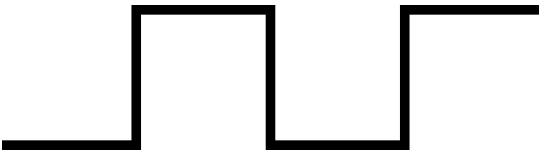


SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	0
0	1	0	1	0
1	1	1	1	0

Percobaan 6. XOR



L1



L2

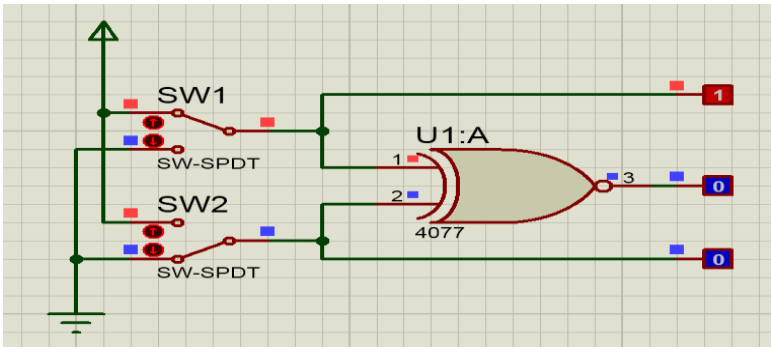


L3

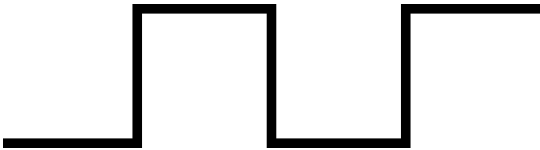
SW 1	SW 2	L1	L2	L3
0	0	0	0	0
1	0	1	0	1
0	1	0	1	1
1	1	1	1	0



Percobaan 7. XNOR



L1



L2



SW 1	SW 2	L1	L2	L3
0	0	0	0	1
1	0	1	0	0
0	1	0	1	0
1	1	1	1	1

L3

