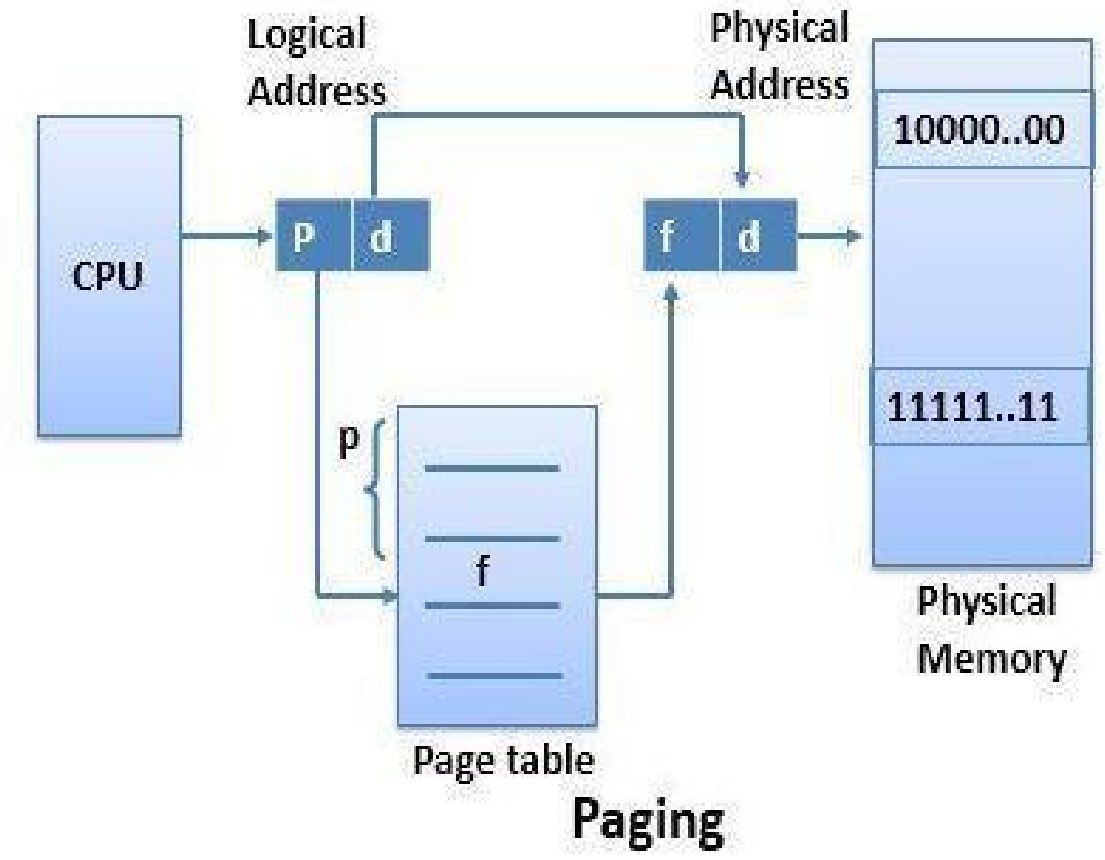

Page table base register =40000 +1= 40001

Logical address 32bits

2^{32}

Page size =1024bytes =2^{10}

bits needed for page number= $2^{32}/2^{10}=2^{22}$ 22bits

32-22=10 bits page offset

Logical address=700

00000000 0000001---0 10111100

Page number page offset

p d

24 bits logical address

$2^{24}/2^{9}=2^{15}$ 15bits

16bits physical address

$2^{16}/2^{9}=2^{7}$ =7 bits page frame

Page size 512bytes=2^{9}

Page # 1 is loaded in frame # 3

Physical address: 0000011 0 10111100

f d