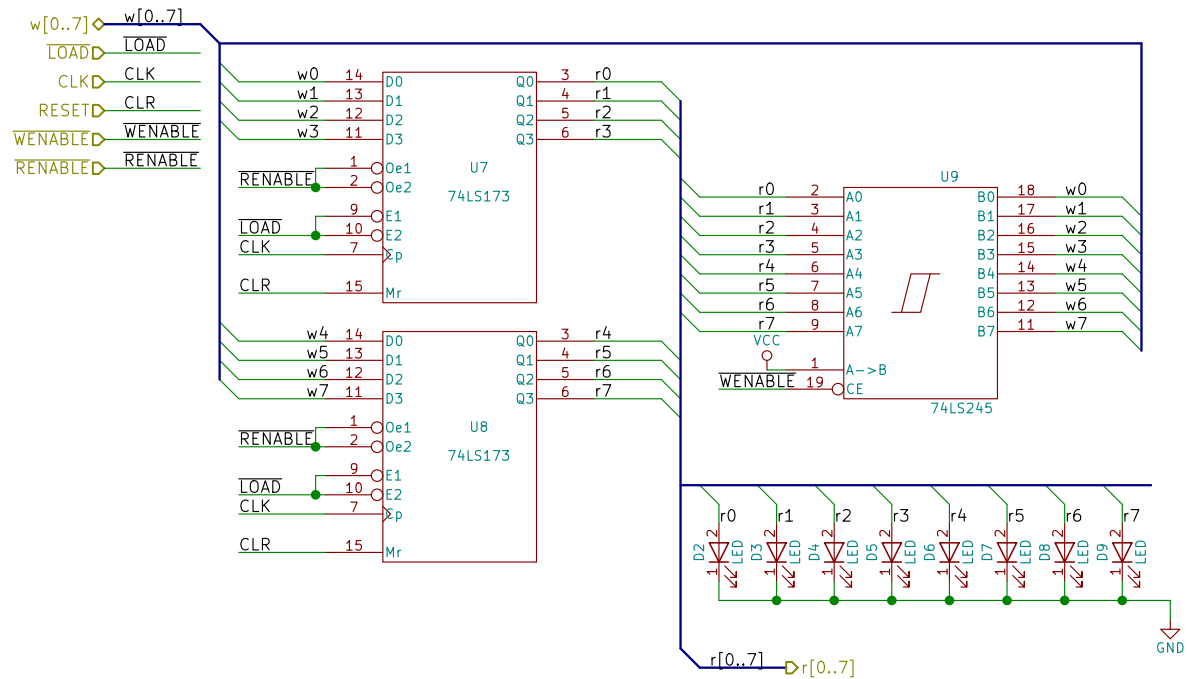


Rev: 1
Id: 3/12



Schematic capture by Vincent Sanders

Schematic released as CC BY-SA

Sheet: /RegisterB/

File: register.sch

Title: Simple As Possible computer by Ben Eater

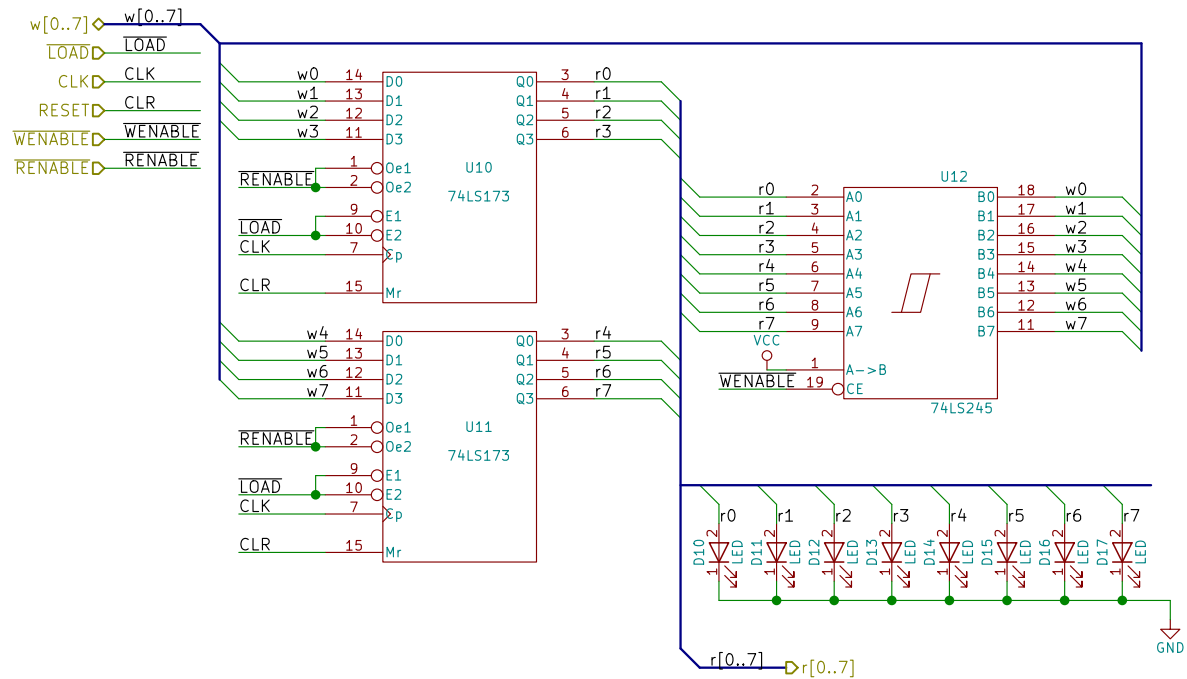
Size: A4

Date: 2017-05-10

Rev: 1

KiCad E.D.A. eeschema (2014-03-19 BZR 4756)-product

Id: 4/12



Schematic capture by Vincent Sanders

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Sheet: /Register/

File: register.sch

Title: Simple As Possible computer by Ben Eater

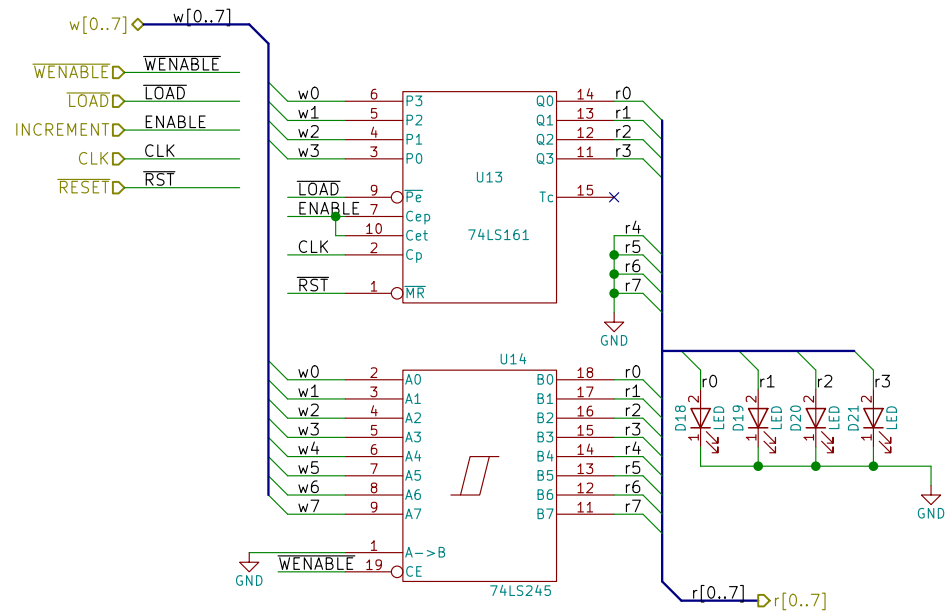
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Date: 2017-05-10

Rev: 1

KiCad E.D.A. eeschema (2014-03-19 BZR 4756)-product

Id: 5/12

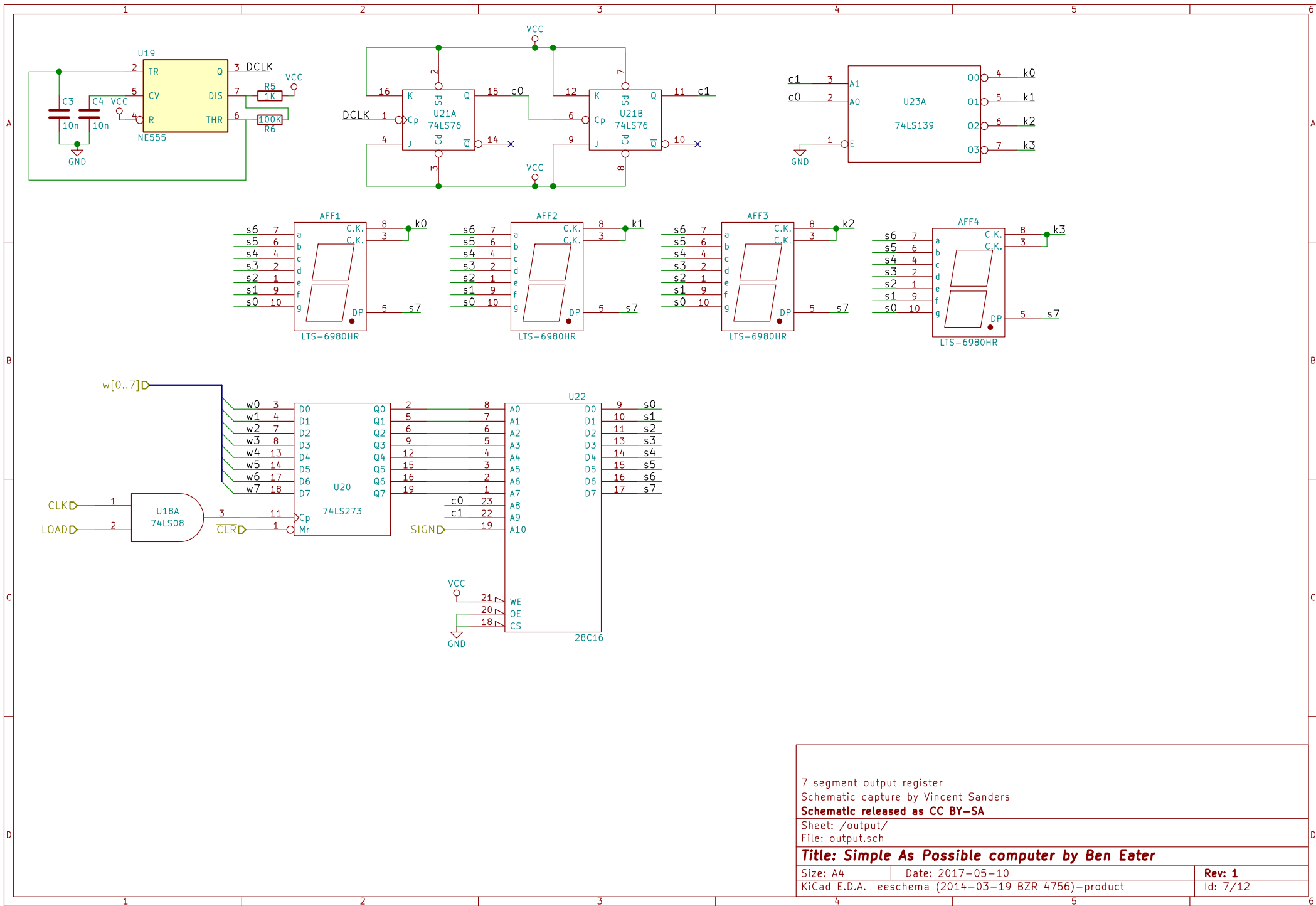


Program counter
Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

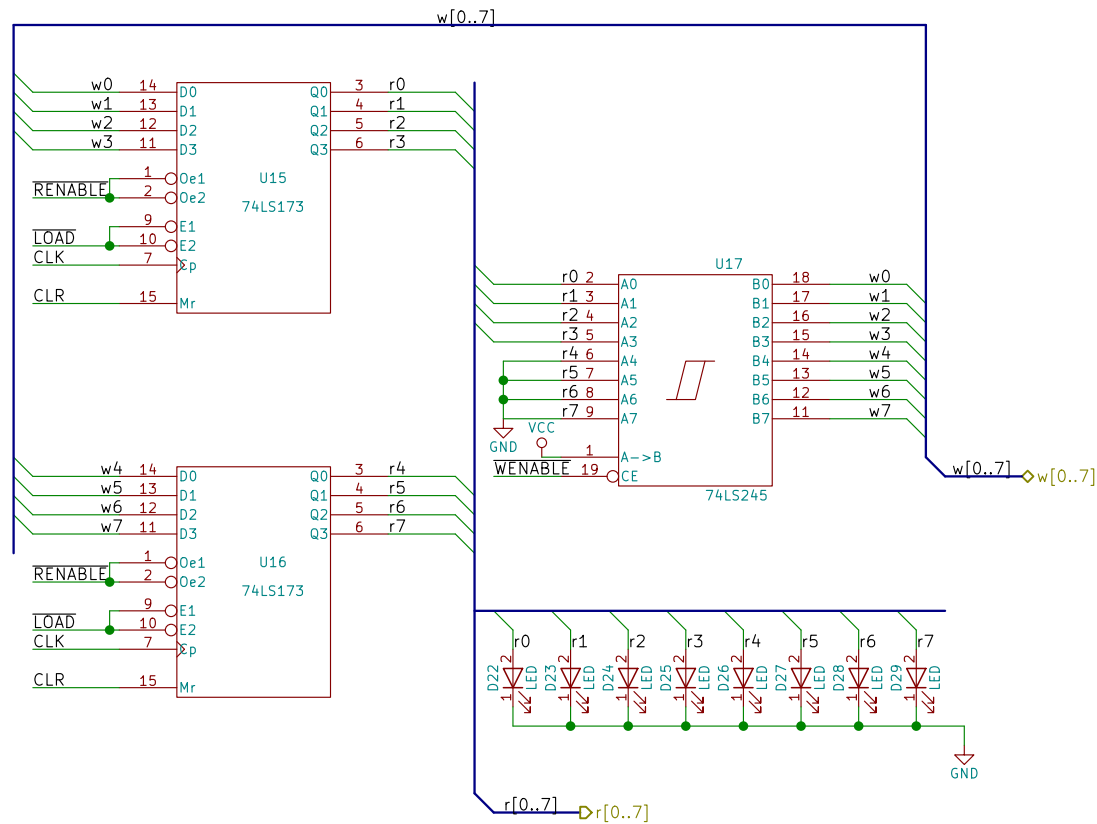
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File: programcounter.sch

Title: Simple As Possible computer by Ben Eater

Size: A4	Date: 2017-05-10	Rev: 1
KiCad E.D.A. eeschema (2014-03-19 BZR 4756)-product		Id: 6/12



LOAD \triangleright $\overline{\text{LOAD}}$
 CLK \triangleright CLK
 RESET \triangleright CLR
 WENABLE \triangleright $\overline{\text{WENABLE}}$
 RENABLE \triangleright $\overline{\text{REENABLE}}$



Instruction register
 Schematic capture by Vincent Sanders
Schematic released as CC BY-SA

Sheet: /IR/
 File: instructionregister.sch

Title: Simple As Possible computer by Ben Eater

Size: A4	Date: 2017-05-10	Rev: 1
KiCad E.D.A. eeschema (2014-03-19 BZR 4756)-product		Id: 10/12

