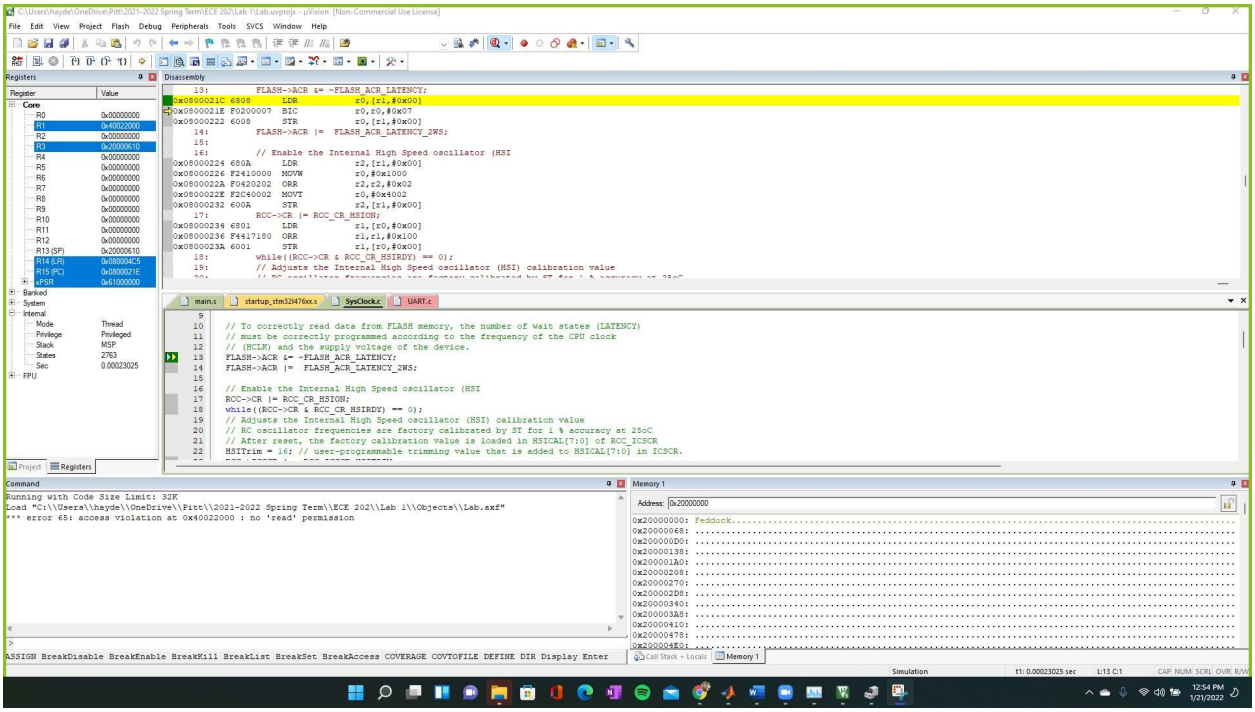
C:\Users\hayde\OneDrive\Pitt\2021-2022 Spring Term\ECE 202\Lab 1\Lab.uvprojx - µVision [Non-Commercial Use License]
File Edit View Project Flash Debug Peripherals Tools SVCS Window Help
Registers
Register Value
Core
R0 0x00000000
R1 0x40022000
R2 0x00000000
R3 0x20000510
R4 0x00000000
R5 0x00000000
R6 0x00000000
R7 0x00000000
R8 0x00000000
R9 0x00000000
R10 0x00000000
R11 0x00000000
R12 0x00000000
R13 (SP) 0x20000610
R14 (LR) 0x080004C5
R15 (PC) 0x080002E6
xPSR 0x61000000
Banked
System
Internal
Mode Thread
Privilege Privileged
Stack MSP
States 2763
Sec 0.00023025
FPU
Project Registers
Disassembly
13: FLASH->ACR &= ~FLASH_ACR_LATENCY;
0x0800021C 6808 LDR r0,[r1,#0x00]
0x0800021E F0200007 BIC r0,r0,#0x07
0x08000222 6008 STR r0,[r1,#0x00]
14: FLASH->ACR |= FLASH_ACR_LATENCY_2WS;
15:
16: // Enable the Internal High Speed oscillator (HSI
0x08000224 680A LDR r2,[r1,#0x00]
0x08000226 F2410000 MOVW r0,#0x1000
0x0800022A F0420202 ORR r2,r2,#0x02
0x0800022E F2C40002 MOVT r0,#0x4002
0x08000232 600A STR r2,[r1,#0x00]
17: RCC->CR |= RCC_CR_HSION;
0x08000234 6801 LDR r1,[r0,#0x00]
0x08000236 F4417180 ORR r1,r1,#0x100
0x0800023A 6001 STR r1,[r0,#0x00]
18: while((RCC->CR & RCC_CR_HSIRDY) == 0);
19: // Adjusts the Internal High Speed oscillator (HSI) calibration value
main.s startup_stm32l476xx.s SysClock.c UART.c
9
10 // To correctly read data from FLASH memory, the number of wait states (LATENCY)
11 // must be correctly programmed according to the frequency of the CPU clock
12 // (HCLK) and the supply voltage of the device.
13 FLASH->ACR &= ~FLASH_ACR_LATENCY;
14 FLASH->ACR |= FLASH_ACR_LATENCY_2WS;
15
16 // Enable the Internal High Speed oscillator (HSI
17 RCC->CR |= RCC_CR_HSION;
18 while((RCC->CR & RCC_CR_HSIRDY) == 0);
19 // Adjusts the Internal High Speed oscillator (HSI) calibration value
20 // RC oscillator frequencies are factory calibrated by ST for 1 % accuracy at 25oC
21 // After reset, the factory calibration value is loaded in HSICAL[7:0] of RCC_ICSCR
22 HSITrim = 16; // user-programmable trimming value that is added to HSICAL[7:0] in ICSCR.
Command
Running with Code Size Limit: 32K
Load "C:\\Users\\hayde\\OneDrive\\Pitt\\2021-2022 Spring Term\\ECE 202\\Lab 1\\Objects\\Lab.axf"
*** error 65: access violation at 0x40022000 : no 'read' permission
ASSIGN BreakDisable BreakEnable BreakKill BreakList BreakSet BreakAccess COVERAGE COVTOFILE DEFINE DIR Display Enter
Memory 1
Address: 0x20000000
0x20000000: Feddock
0x20000068:
0x200000D0:
0x20000138:
0x200001A0:
0x20000208:
0x20000270:
0x200002D8:
0x20000340:
0x200003A8:
0x20000410:
0x20000478:
0x200004E0:
Call Stack + Locals Memory 1
Simulation t1: 0.00023025 sec L:13 C:1 CAP NUM SCRL OVR R/W
12:54 PM
1/21/2022