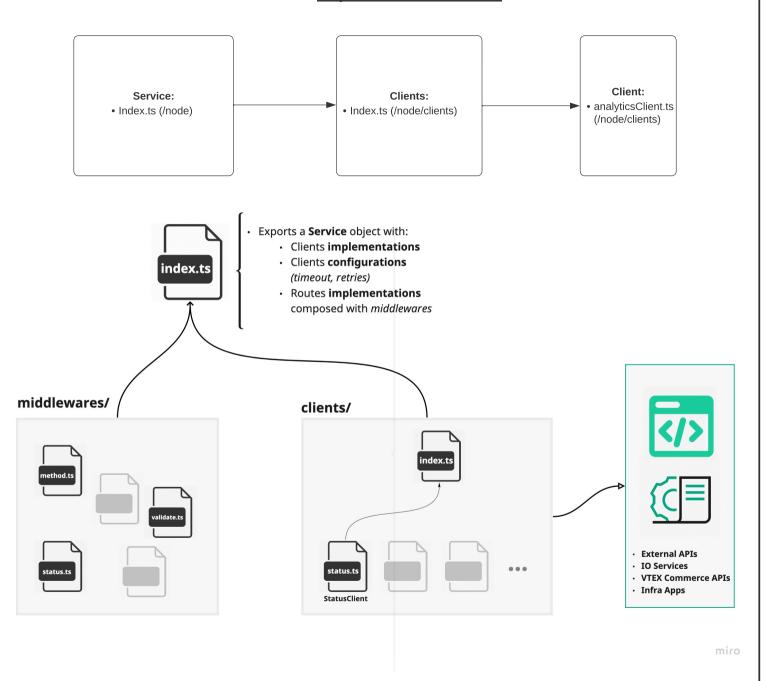
## **Project VTEX IO Architecture**



## **Architecture Flow**

