Quality of design) Quality 000 Qualit D&[LSL, USL] -> pim is NOT comprising De (rsr ' asr) feature / + systems (Verencity in the 4 V's) design vs. -> pin is confraint - 1 It is NOT DEFECTIVE 27/02/2023

PIN TO DE のスナ KACHINED madel the variable to madel the variable, operator Process PRODUCT! process becomed , vector > 0 = [D] 0=10,02,03 ... eshalibous machina d = distur t = proper poronulus romes 0

D~N(4,62) DIN(HIGE) - Process LSL. USL = destign

the bades lit P(D=LSL 9 1 DNN(4,52) defective i be because of LSL 1 Pen USL

0 N(0,1) J25 184-4

CV

10 = P(D>USL | D~N(µ, 62)) = 1-P/D=USL 1 T(1)>USL) P(DELL) (15) = f(0 > 02L)* 1) Dearchwood =1-0/ Variable 11 (2)

6 SIGMA PROCESS 80 1=/1 0

(5)

Yesacity of my data -CHECK MEASUREMENT SYSTEP (of THE OUTPUT) 72AG - FUTUCE ACTIONS

Assume you want to "measur" STOPWATCH (ARWOM,

9,98 10,52 No, No T=10 18.0.8. lg. 86.6 D A10,52 ō. 6 BIANCA

0 0 1100 3/02 John's y. John's y's

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