

● = equal a zero!

Logica negativa!

x_1	x_2	y
0	0	0
0	1	1
1	0	1
1	1	1

Diagram illustrating a 2-to-4 decoder circuit with two inputs, X_0 and X_1 , and four outputs.

The decoder block is labeled "Decoder". It has two inputs, X_0 and X_1 , and four outputs. The outputs are connected to four OR gates.

The OR gates are labeled with green text: 0, 1, y' , and y'' .

The outputs of the OR gates are connected to two multiplexers (Mux).

The first multiplexer (Mux) has two inputs, y' and y'' , and one output, y_0 . It is controlled by input X_2 .

The second multiplexer (Mux) has two inputs, y' and y'' , and one output, y_1 . It is controlled by input X_2 .

Handwritten notes:

- 1 check, 2 input $\leftarrow X_2$
- nello esempio ogni 0=1

	x_0	x_1	x_2
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
4	1	0	0
5	1	0	1
6	1	1	0
7	1	1	1

↓
2³

90	91
0	1
1	1
0	1
1	1
0	1
1	0
0	0
1	0