

TRADUZIONE ASSEMBLY A LINGUAGGIO MACCHINA



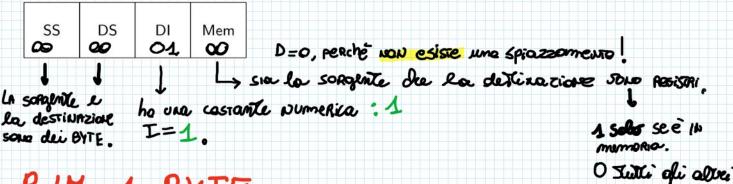
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LA ADD É CLASSE 2 1 0010

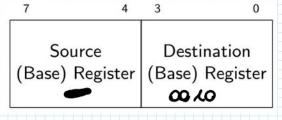
OPCODE 1 BYTE.



MODE 1 BYTE



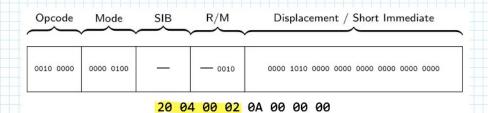
RIM 1 BYTE



dl = RDX

→ ∞10 ed è il registro desicuazione IL REGISTED SOMOLITE & don't CARE: -

4 bit registro 4 bit reustro Destinazione SORGENTE



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