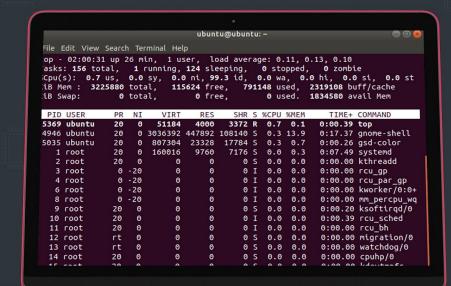
PROCESOS



DigitalHouse>

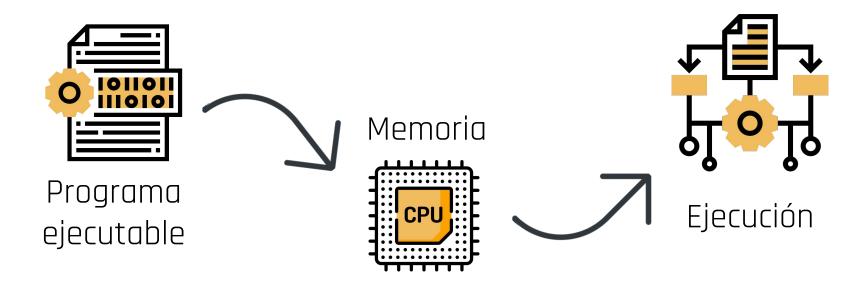


Contenido

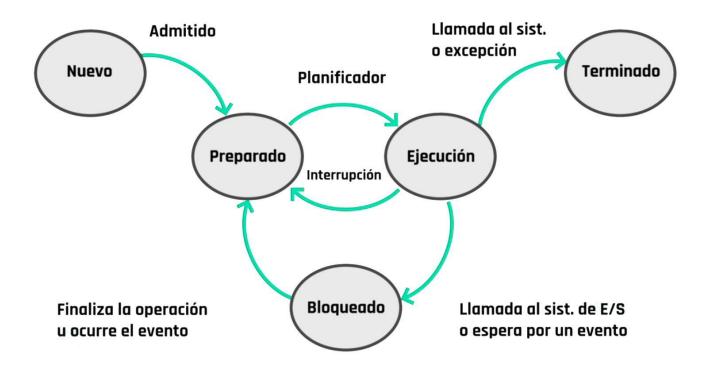
- > Procesos
- > Comunicación
- > Hilos
- > Planificación



> Definición



Estados





Comunicación de Procesos

PROCESOS INDEPENDIENTES



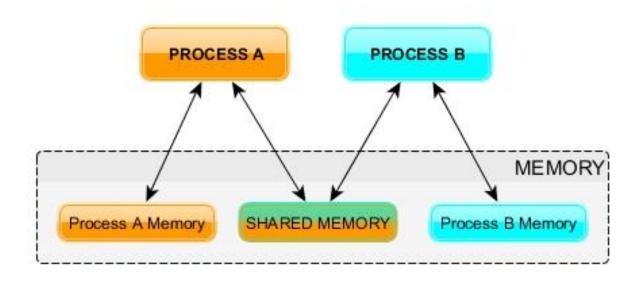
PROCESOS COOPERATIVOS



1 2 3 4 5

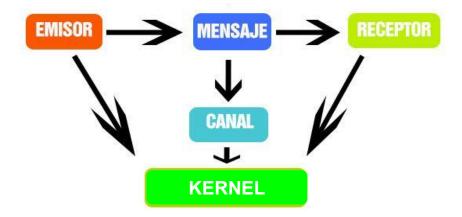


Memoria compartida



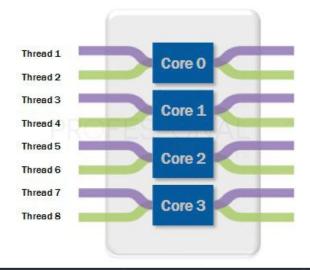


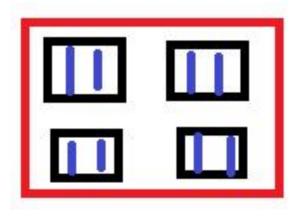
Paso de mensajes





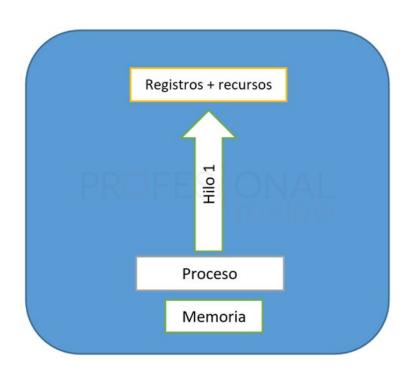
Son una secuencia de instrucciones que se ejecutan simultáneamente y en paralelo





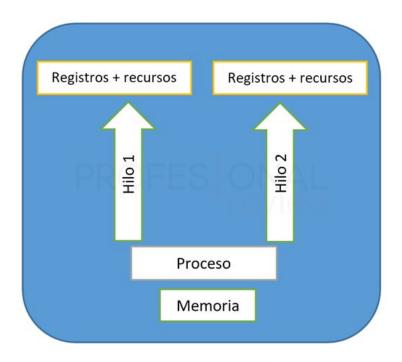


Procesadores Monolíticos





Procesadores Multihilos





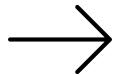
¿Como ver la cantidad de hilos?

CPU I	Caches	Mainboa	rd Mem	ory S	PD	Graphic	s Bench	About
Process	or —							
Name		AMD Ryzen 5 1600X						
Code Name		Summit Ridge Max TDP 95.0 W					_0	
Package		Socket AM4 (1331)						
Technology		14 nm	Core Voltage		1	1.236 V		5
Specific	ation	AM	D Ryzen !	5 1600X	Six-0	Core Proc	essor	
Family		F	M	odel	1	5	tepping	1
Ext. Family		17	Ext. M	lodel	1	Revision		ZP-B1
Instruc		MMX(+), S 86-64, AM						SSE4A,
Clocks (Core #	0)		Cach	e —			
Core Speed		2052.25 MHz		L1 Data		6 x 32 KBytes		8-way
Multiplier		x 20.57		L1 Inst.		6 x 64 KBytes		4-way
Bus Speed 99.76		99.761	MHz Lev		12	6 x 512 KBytes		8-way
	Rated FSB			Level 3		2 x 8 MBytes		16-way
Rated				17.11				



Conjunto de técnicas y algoritmos

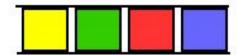
Objetivo



Optimizar el rendimiento del sistema

FIFO - First In, First Out

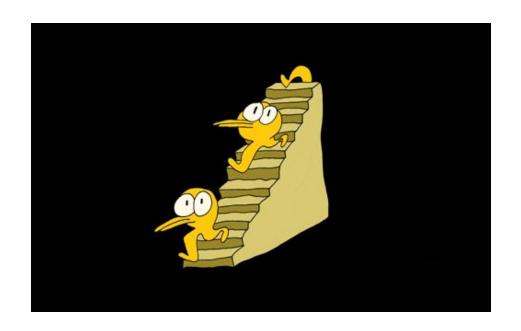
N



OUT



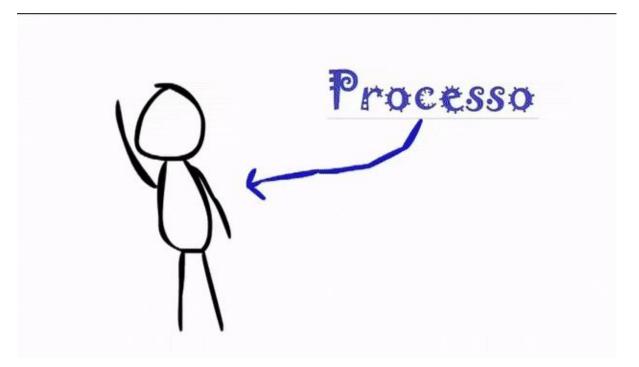
RR - Round Robin





SJF, Shortest-Job-First

El más corto primero





SRTF, Shortest-Remaining Time-First

El de tiempo restante más corto









GRACIAS!

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