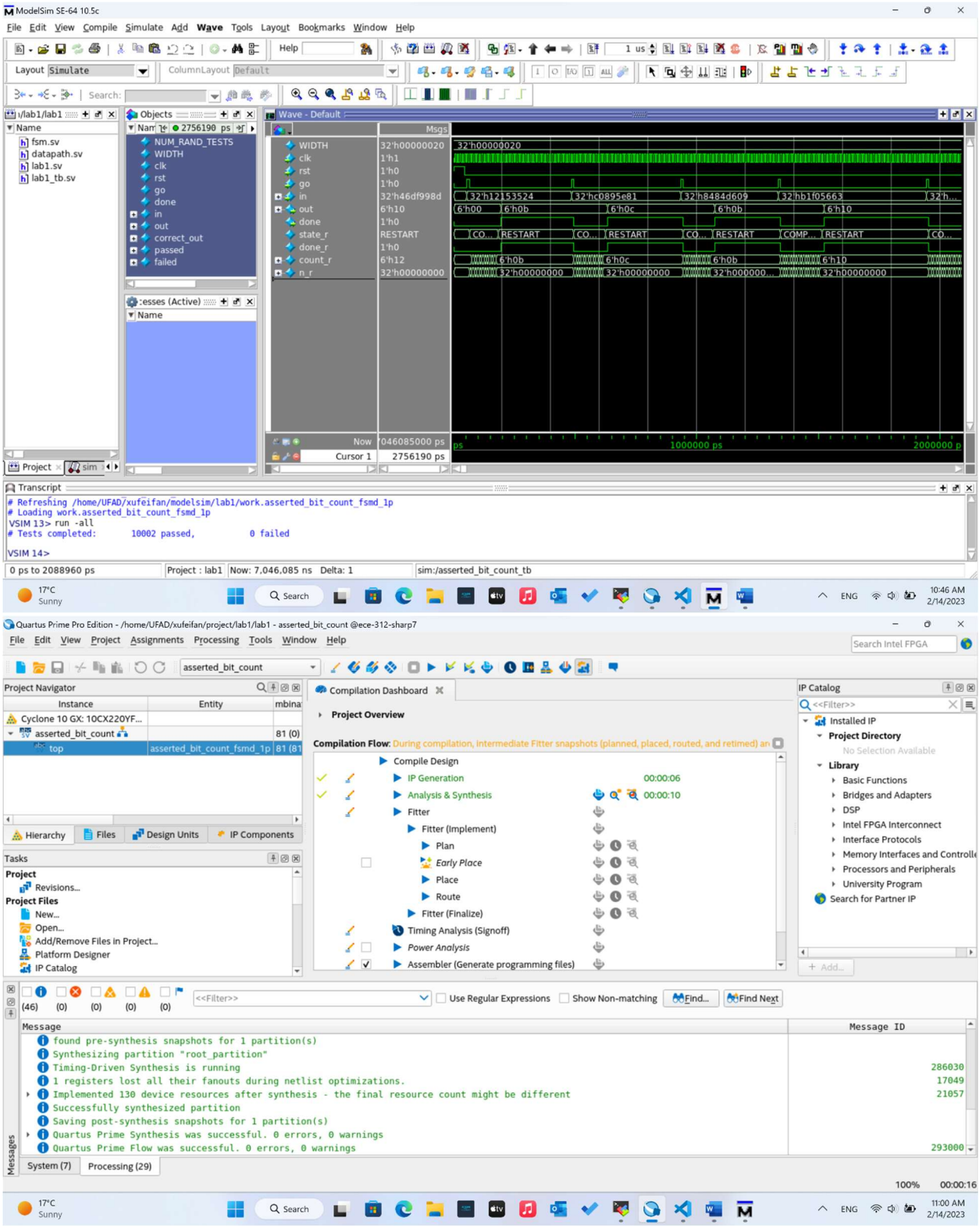
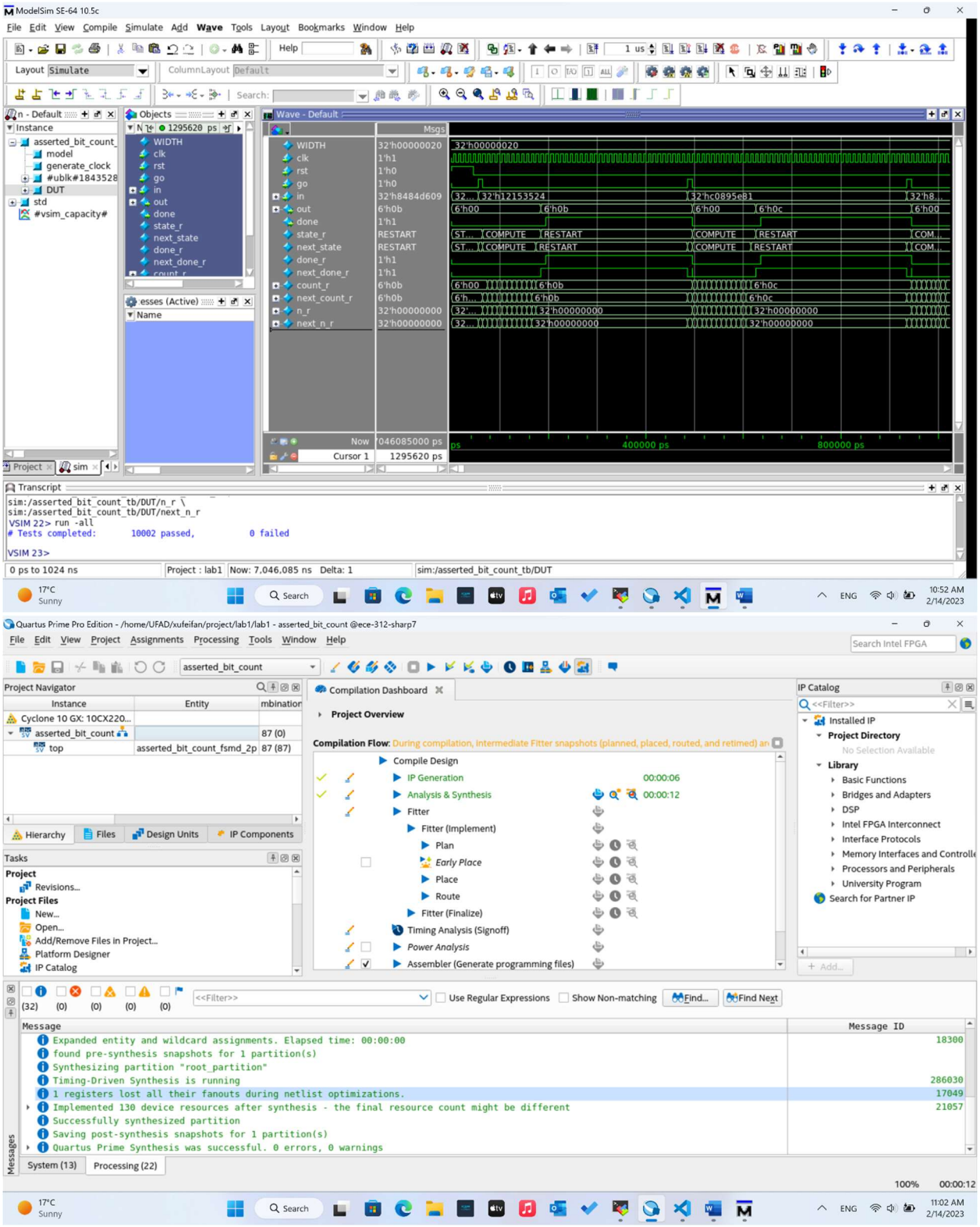
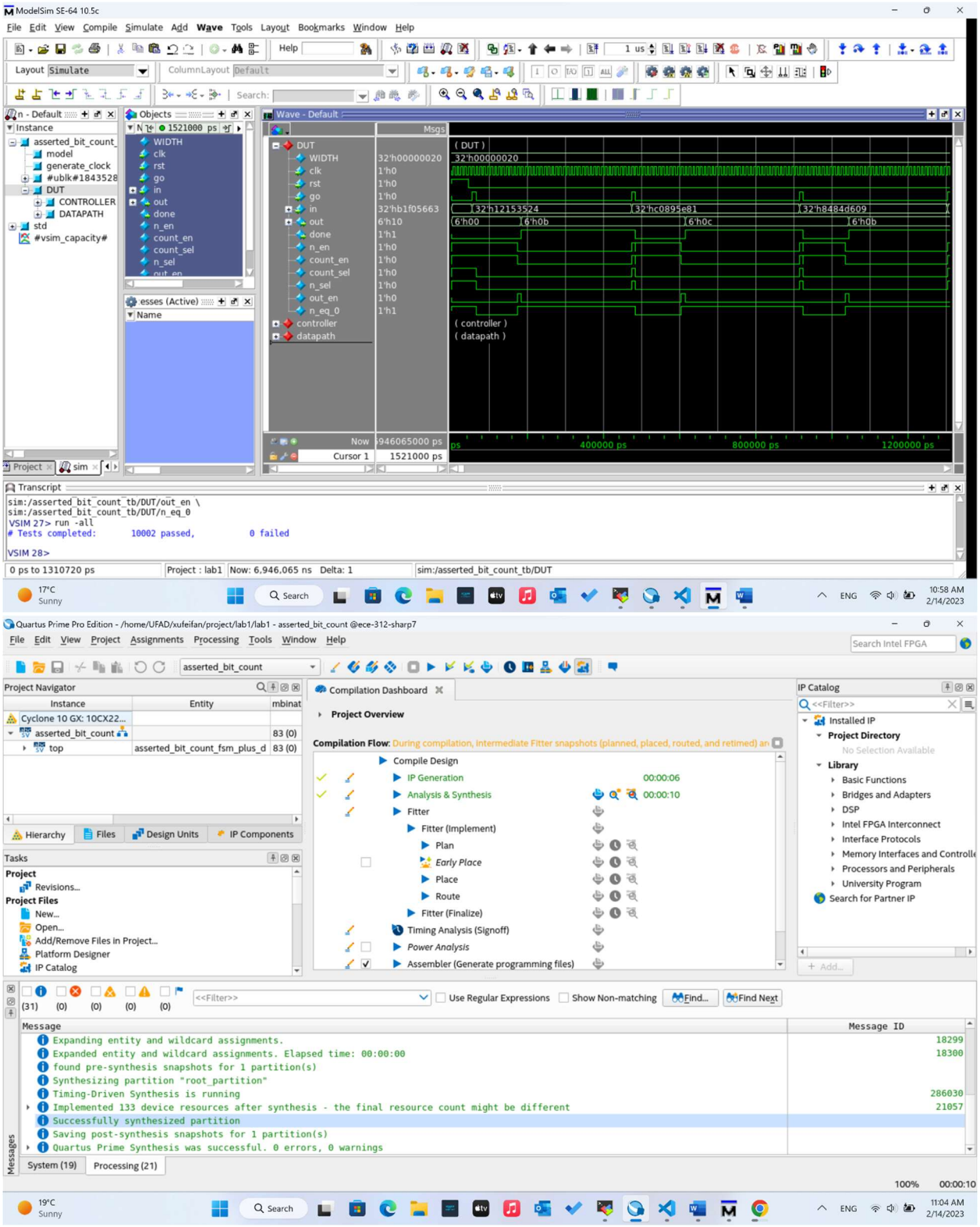
1. 1-process fsmd



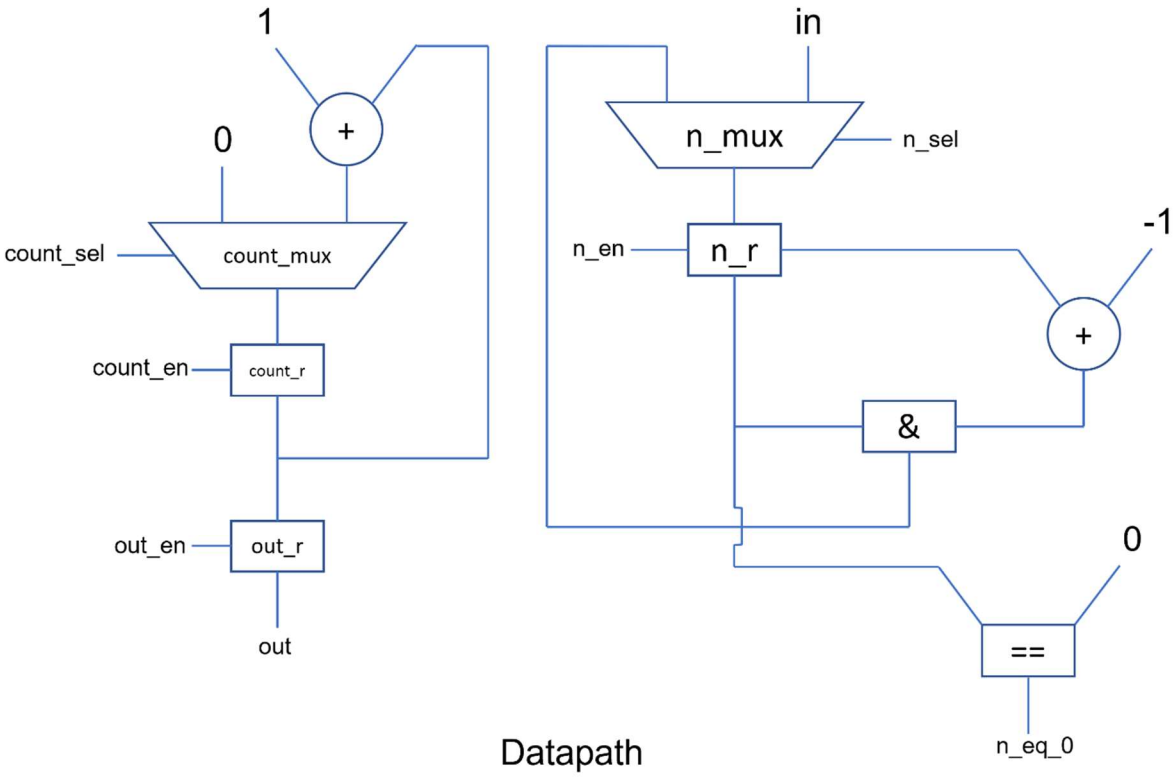
2. 2-process fsmd



3. fsm_plus_d


ModelSim SE-64 10.5c
Wave - Default
Transcript
sim:/asserted_bit_count_tb/DUT/out_en \
sim:/asserted_bit_count_tb/DUT/n_eq_0
VSIM 27> run -all
Tests completed: 10002 passed, 0 failed
VSIM 28>
Quartus Prime Pro Edition - /home/UFAD/xufeifan/project/lab1/lab1 - asserted_bit_count @ece-312-sharp7
Project Navigator
Compilation Dashboard
IP Catalog
Tasks
Expanding entity and wildcard assignments.
Expanded entity and wildcard assignments. Elapsed time: 00:00:00
found pre-synthesis snapshots for 1 partition(s)
Synthesizing partition "root_partition"
Timing-Driven Synthesis is running
Implemented 133 device resources after synthesis - the final resource count might be different
Successfully synthesized partition
Saving post-synthesis snapshots for 1 partition(s)
Quartus Prime Synthesis was successful. 0 errors, 0 warnings

4. datapath schematic



Datapath