

## **Exercício Prático 6:**

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### **Parte 1:**

#### **1. O que é um arquivo fonte?**

- A. um arquivo de texto que contém instruções de linguagem de programação.
- B. um subdiretório que contém os programas.
- C. um arquivo que contém dados para um programa.
- D. um documento que contém os requisitos para um projeto.

#### **2. O que é um registrador?**

- A. parte do sistema de computador que mantém o controle dos parâmetros do sistema.
- B. uma parte do processador que possui um padrão de bits.
- C. parte do processador que contém o seu número de série único.
- D. parte do bus de sistema que contém dados.

#### **3. Qual o caracter que, na linguagem assembly do SPIM, inicia um comentário?**

- A. #
- B. \$
- C. //
- D. \*

#### **4. Quantos bits há em cada instrução de máquina MIPS?**

- A. 8
- B. 16
- C. 32
- D. instruções diferentes possuem diferentes comprimentos.

**5. O que é o contador de programa?**

- A. um registrador que mantém a conta do número de erros durante a execução de um programa.
- B. uma parte do processador que contém o endereço da primeira palavra de dados.
- C. uma variável na montadora que os números das linhas do arquivo de origem.
- D. parte do processador que contém o endereço da próxima instrução de máquina para ser obtida.

**6. Ao executarmos uma instrução, quanto será adicionado ao contador de programa?**

- A. 1
- B. 2
- C. 4
- D. 8

**7. O que é uma diretiva, tal como a diretiva .text?**

- A. uma instrução em linguagem assembly que resulta em uma instrução em linguagem de máquina.
- B. uma das opções de menu do sistema SPIM.
- C. uma instrução em linguagem de máquina que faz com que uma operação sobre os dados ocorra.
- D. uma declaração que diz o montador algo sobre o que o programador quer, mas não corresponde diretamente a uma instrução de máquina.

**8. O que é um endereço simbólico?**

- A. um local de memória que contém dados simbólicos.
- B. um byte na memória que contém o endereço de dados.
- C. símbolo dado como argumento para uma directiva.
- D. um nome usado no código-fonte em linguagem assembly para um local na memória.

**9. Em qual endereço o simulador SPIM coloca a primeira instrução de máquina quando ele está sendo executado?**

- A. 0x00000000
- B. 0x00400000
- C. 0x10000000
- D. 0xFFFFFFFF

**10. Algumas instruções de máquina possuem uma constante como um dos operandos. Como é chamado tal operando?**

- A. operando imediato
- B. operando embutido
- C. operando binário
- D. operando de máquina

**11. Como é chamada uma operação lógica executada entre bits de cada coluna dos operandos para produzir um bit de resultado para cada coluna?**

- A. operação lógica
- B. operação bitwise
- C. operação binária
- D. operação coluna

**12. Quando uma operação é de fato executada, como estão os operandos na ALU?**

- A. Pelo menos um operando deve ser de 32 bit.
- B. Cada operando pode ser de qualquer tamanho.
- C. Ambos operandos devem vir de registros.
- D. Cada um dos registradores deve possuir 32 bit.

**13. Dezesseis bits de dados de uma instrução de ori são usados como um operando imediato. Durante execução, o que deve ser feito primeiro?**

- A. Os dados são estendidos em zero à direita por 16 bits.
- B. Os dados são estendidos em zero à esquerda por 16 bits.
- C. Nada precisa ser feito.
- D. Apenas 16 bits são usados pelo outro operando.

**14. Qual das instruções seguintes armazenam no registrador \$5 um padrão de bits que representa positivo 48?**

- A. ori \$5,\$0,0x48
- B. ori \$5,\$5,0x48
- C. ori \$5,\$0,48
- D. ori \$0,\$5,0x48

**15. A instrução de ori pode armazenar o complemento de dois de um número em um registrador?**

- A. Não.
- B. Sim.

**16. Qual das instruções seguintes limpa todos os bits no registrador \$8 com exceção do byte de baixa ordem que fica inalterado?**

- A. ori \$8,\$8,0xFF
- B. ori \$8,\$0,0x00FF
- C. xori \$8,\$8,0xFF
- D. andi \$8,\$8,0xFF

**17. Qual é o resultado de um ou exclusivo de padrão sobre ele mesmo?**

- A. Todos os bits em zero.
- B. Todos os bits em um.
- C. O padrão original utilizado.
- D. O resultado é o contrário do original.

**18. Todas as instruções de máquina têm os mesmos campos?**

- A. Não. Diferentes de instruções de máquina possuem campos diferentes.
- B. Não. Cada instrução de máquina é completamente diferente de qualquer outra.
- C. Sim. Todas as instruções de máquina têm os mesmos campos na mesma ordem.
- D. Sim. Todas as instruções de máquina têm os mesmos campos, mas eles podem estar em ordens diferentes

## Parte 2:

### Programa 1:

```
mips1.asm
1  #a = 2
2  #b = 3
3  #c = 4
4  #d = 5
5  #x = (a+b) - (c+d)
6  #y = a - b + x
7  #b = x - y
8
9 .text
10 main:
11    ori $s0, $zero, 2 # a = 2
12    ori $s1, $zero, 3 # b = 3
13    ori $s2, $zero, 4 # c = 4
14    ori $s3, $zero, 5 # d = 5
15    add $t0, $s0, $s1 # t0 = a + b
16    add $t1, $s2, $s3 # t1 = c + d
17    sub $t0, $t0, $t1 # t0 = t0 - t1
18    sub $t1, $s0, $s1 # t1 = a - b
19    add $t1, $t1, $t0 # t1 = t1 - t0
20    sub $s1, $t0, $t1 # b = t0 - t1
```

The screenshot shows a MIPS assembly debugger interface with several windows:

- Text Segment:** Displays the assembly code from mips1.asm.
- Registers:** Shows the state of all 32 general-purpose registers (\$zero to \$t1). \$s1 is highlighted in green.
- Data Segment:** Shows the memory dump starting at address 0x10010000, with columns for Address, Value (+0), Value (+4), Value (+8), Value (+c), Value (+10), Value (+14), Value (+18), and Value (+1c).
- Memory Dump:** A detailed view of memory starting at address 0x10010000, showing bytes and their hex values.

## Programa 2:

mips1.asm

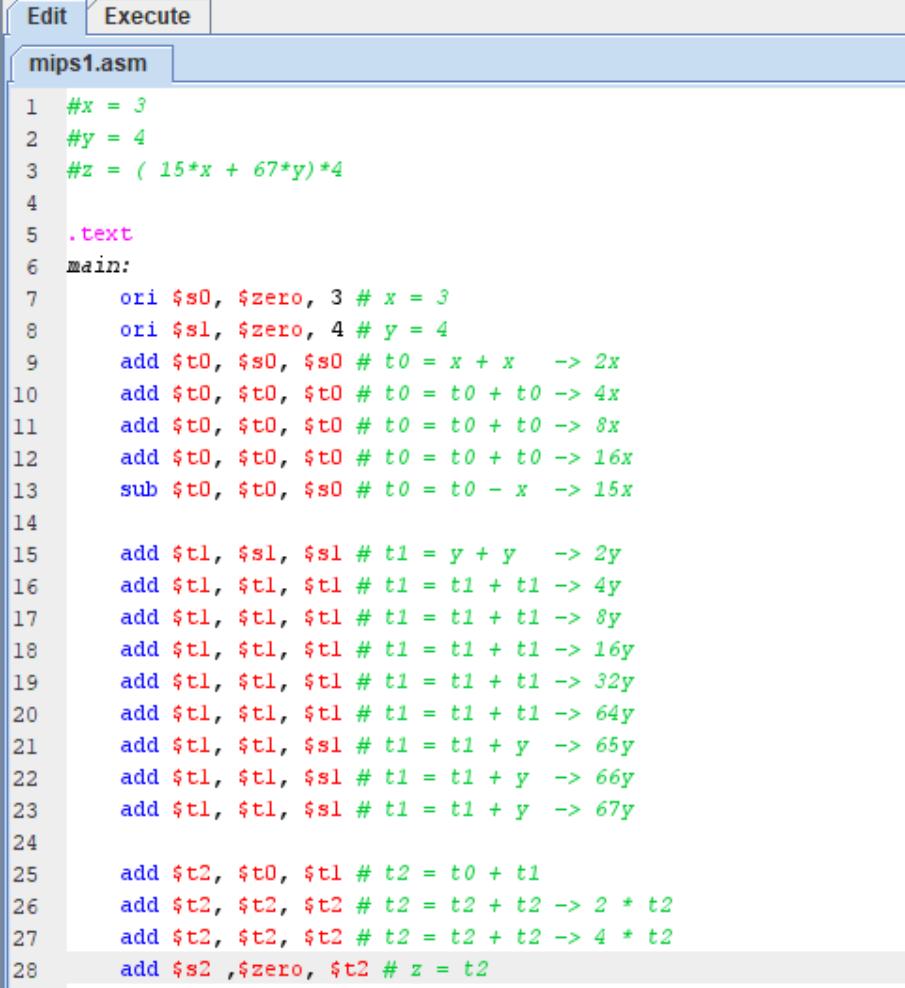
```

1  #x = 1
2  #y = 5*x + 15
3
4  ori $s0, $zero, 1 # x = 1
5  add $t0, $s0, $s0 # t0 = x + x -> 2x
6  add $t0, $t0, $t0 # t0 = 2x + 2x -> 4x
7  add $t0, $t0, $s0 # t0 = 4x + x -> 5x
8  addi $s1, $t0, 15 # y = t0 + 15

```

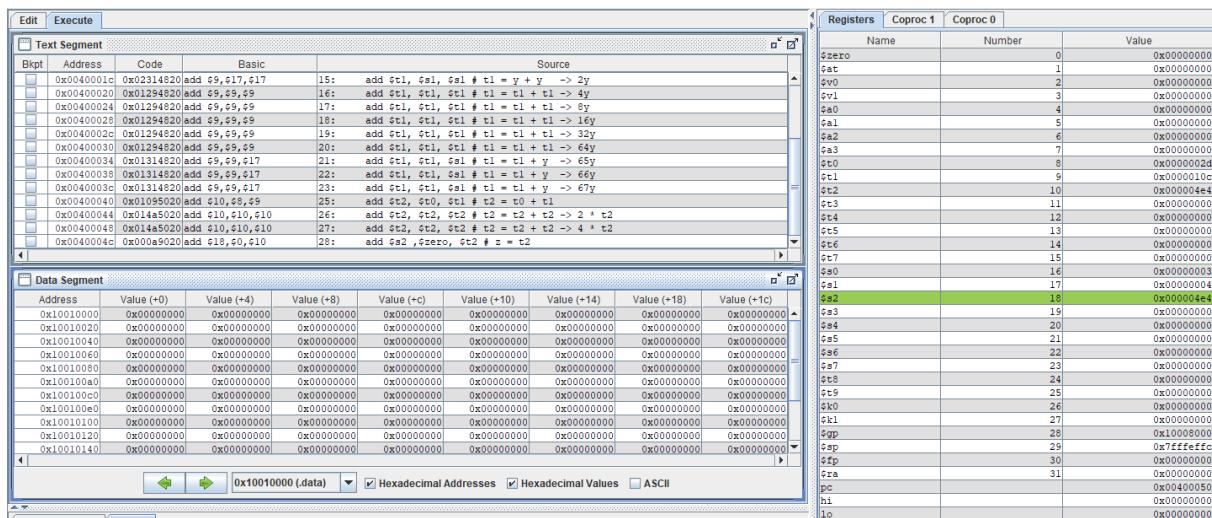
Registers	Coproc 1	Coproc 0
\$zero	0	0
\$at	1	0
\$v0	2	0
\$v1	3	0
\$a0	4	0
\$a1	5	0
\$a2	6	0
\$a3	7	0
\$t0	8	5
\$t1	9	0
\$t2	10	0
\$t3	11	0
\$t4	12	0
\$t5	13	0
\$t6	14	0
\$t7	15	0
\$s0	16	1
\$s1	17	20
\$s2	18	0
\$s3	19	0
\$s4	20	0
\$s5	21	0
\$s6	22	0
\$s7	23	0
\$t8	24	0
\$t9	25	0
\$k0	26	0
\$k1	27	0
\$gp	28	268469224
\$sp	29	2147479543
\$fp	30	0
\$ra	31	0
\$pc		4194324
\$hi		0
\$lo		0

### Programa 3:



The screenshot shows a window titled "mips1.asm" with two tabs: "Edit" and "Execute". The "Edit" tab is selected. The code is numbered from 1 to 28. It defines constants #x = 3 and #y = 4, and calculates #z = (15\*x + 67\*y)\*4. The assembly code uses registers \$s0, \$s1, \$t0, \$t1, and \$t2 to perform the calculations.

```
1  #x = 3
2  #y = 4
3  #z = ( 15*x + 67*y)*4
4
5 .text
6 main:
7     ori $s0, $zero, 3 # x = 3
8     ori $s1, $zero, 4 # y = 4
9     add $t0, $s0, $s0 # t0 = x + x    -> 2x
10    add $t0, $t0, $t0 # t0 = t0 + t0 -> 4x
11    add $t0, $t0, $t0 # t0 = t0 + t0 -> 8x
12    add $t0, $t0, $t0 # t0 = t0 + t0 -> 16x
13    sub $t0, $t0, $s0 # t0 = t0 - x -> 15x
14
15    add $t1, $s1, $s1 # t1 = y + y    -> 2y
16    add $t1, $t1, $t1 # t1 = t1 + t1 -> 4y
17    add $t1, $t1, $t1 # t1 = t1 + t1 -> 8y
18    add $t1, $t1, $t1 # t1 = t1 + t1 -> 16y
19    add $t1, $t1, $t1 # t1 = t1 + t1 -> 32y
20    add $t1, $t1, $t1 # t1 = t1 + t1 -> 64y
21    add $t1, $t1, $s1 # t1 = t1 + y -> 65y
22    add $t1, $t1, $s1 # t1 = t1 + y -> 66y
23    add $t1, $t1, $s1 # t1 = t1 + y -> 67y
24
25    add $t2, $t0, $t1 # t2 = t0 + t1
26    add $t2, $t2, $t2 # t2 = t2 + t2 -> 2 * t2
27    add $t2, $t2, $t2 # t2 = t2 + t2 -> 4 * t2
28    add $s2, $zero, $t2 # z = t2
```



## Programa 4:

The screenshot shows the QEMU debugger interface with the assembly code for `mips1.asm`.

```

1  #x = 3
2  #y = 4
3  #z = ( 15*x + 67*y)*4

4

5 .text
6 main:
7     ori $s0, $zero, 3 # x = 3
8     ori $s1, $zero, 4 # y = 4
9     sll $t0, $s0, 4 # t0 = x << 4 -> 16x
10    sub $t0, $t0, $s0 # t0 = t0 - x -> 15x
11
12    sll $t1, $s1, 6 # t1 = y << 6 -> 64y
13    add $t1, $t1, $s1 # t1 = t1 + y -> 65y
14    add $t1, $t1, $s1 # t1 = t1 + y -> 66y
15    add $t1, $t1, $s1 # t1 = t1 + y -> 67y
16
17    add $t2, $t0, $t1 # t2 = t0 + t1
18    sll $t2, $t2, 2 # t2 = t2 << 2 -> 4 * t2
19    add $s2 ,$zero, $t2 # z = t2
20

```

**Text Segment**

Bkpt	Address	Code	Basic	Source
	0x00400000	0x34100003	ori \$t0, \$zero, 3	# x = 3
	0x00400004	0x34110004	ori \$t1, \$zero, 4	# y = 4
	0x00400008	0x00104100	sll \$s0, \$t0, 4	# t0 = x << 4 -> 16x
	0x0040000c	0x11104022	sub \$s1, \$s0, 1	# t1 = t0 - x -> 15x
	0x00400010	0x00114980	sll \$s1, \$t1, 6	# t1 = y << 6 -> 64y
	0x00400014	0x01311482	add \$s2, \$s0, \$t1	# t1 = t1 + y -> 65y
	0x00400018	0x01311482	add \$s1, \$t1, \$s1 # t1 = t1 + y -> 66y	
	0x0040001c	0x01311482	add \$s0, \$s1, \$t1	# t1 = t1 + y -> 67y
	0x00400020	0x01095020	add \$t0, \$s0, \$s0	# t0 = t0 + t1
	0x00400024	0x000a5080	sll \$t2, \$t0, 2	# t2 = t2 << 2 -> 4 * t2
	0x00400028	0x000aa9020	add \$s2, \$s0, \$t2 # s2 = t2	

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

**Registers**

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x0000002d
\$t1	9	0x00000010c
\$t2	10	0x0000004e4
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000003
\$s1	17	0x00000004
<b>\$s2</b>	18	0x0000004e4
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$s8	24	0x00000000
\$s9	25	0x00000000
\$s0	26	0x00000000
\$s1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0xffffffff
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040002c
hi		0x00000000
lo		0x00000000

## Programa 5:

**Edit      Execute**

mips1.asm

```

1  # x = 100000
2  # y = 200000
3  # z = x + y
4
5  .text
6  main:
7      ori $t0, $zero, 0x186A # t0 = 0x186A
8      sll $s0, $t0, 4        # s0 = t0 << 4
9      ori $t1, $zero, 0x30D4 # t1 = 0x30D4
10     sll $s1, $t1, 4        # s1 = t1 << 4
11     add $s2, $s0, $s1      # s2 = s0 + s1

```

**Text Segment**

Bkpt	Address	Code	Basic	Source
	0x00400000	0x3408186a	ori \$t0, \$zero, 0x186a	# t0 = 0x186a
	0x00400004	0x000a0010	sll \$s0, \$t0, 4	# s0 = t0 << 4
	0x00400008	0x340930d4	ori \$t1, \$zero, 0x30d4	# t1 = 0x30d4
	0x0040000c	0x00098900	sll \$s1, \$t1, 4	# s1 = t1 << 4
	0x00400010	0x02119020	add \$s2, \$s0, \$s1	# s2 = s0 + s1

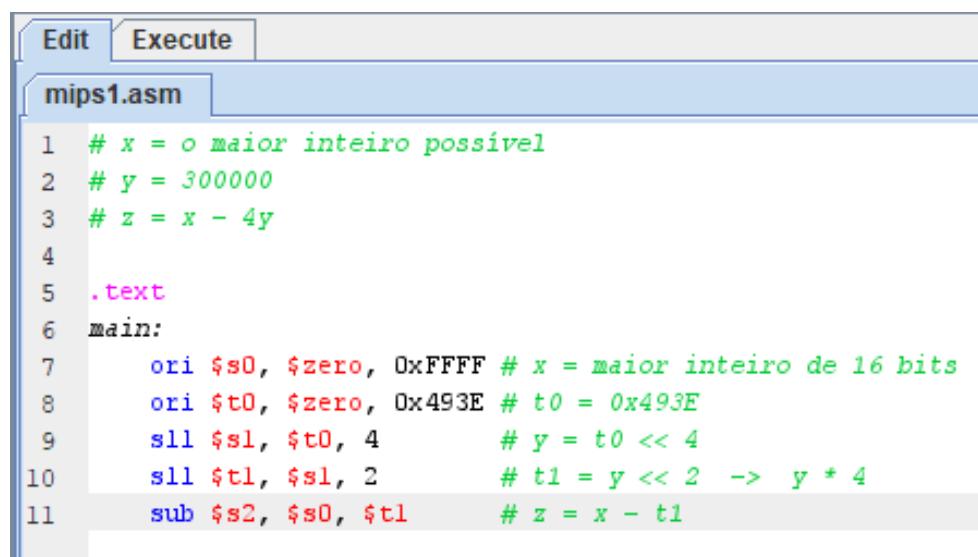
**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

**Registers**

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
<b>\$s2</b>	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0xffffffff
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400014
hi		0x00000000
lo		0x00000000

## Programa 6:



The screenshot shows a window titled "mips1.asm" with two tabs at the top: "Edit" and "Execute". The "Edit" tab is selected. The code is written in MIPS assembly language and performs the calculation  $z = x - 4y$ . The assembly code is as follows:

```
1 # x = o maior inteiro possivel
2 # y = 300000
3 # z = x - 4y
4
5 .text
6 main:
7     ori $s0, $zero, 0xFFFF # x = maior inteiro de 16 bits
8     ori $t0, $zero, 0x493E # t0 = 0x493E
9     sll $s1, $t0, 4        # y = t0 << 4
10    sll $t1, $s1, 2        # t1 = y << 2 -> y * 4
11    sub $s2, $s0, $t1      # z = x - t1
```

**Edit Execute**

**Text Segment**

Bkpt	Address	Code	Basic	Source
	0x00400000	0x3410ffff	ori \$16,\$0,0x0000ffff	7: ori \$a0,\$zero,0xFFFF # x = maior inteiro de 16 bits
	0x00400004	0x3408493e	ori \$9,\$0,0x0000493e	8: ori \$t0,\$zero,0x493E # t0 = 0x493E
	0x00400008	0x00088900	sll \$17,\$8,0x00000004	9: sll \$s1,\$t0,4 # y = c0 << 4
	0x0040000c	0x01145800	sll \$9,\$17,0x00000002	10: sll \$t1,\$s1,2 # t1 = y << 2 -> y * 4
	0x00400010	0x02090022	sub \$18,\$16,\$9	11: sub \$s2,\$s0,\$t1 # z = x - t1

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

0x10010000 (.data)  Hexadecimal Addresses  Hexadecimal Values  ASCII

Registers Coproc 1 Coproc 0

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000493e
\$t1	9	0x0124f80
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x0000ffff
\$s1	17	0x0000493e0
\$s2	18	0xffffeb07f
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0xffffffff
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400014
hi		0x00000000
lo		0x00000000

## Programa 7:

Editor de código MIPS (mips1.asm):

```

1 # Considere a seguinte instrução iniciando um programa:
2 # ori $8, $0, 0x01
3 #Usando apenas instruções reg-reg lógicas e/ou instruções de deslocamento (sll, srl e sra),
4 #continuar o programa de forma que ao final, tenhamos o seguinte conteúdo no registrador $8:
5 # $8 = 0xFFFFFFFF
6
7 .text
8 main:
9     ori $8, $0, 0x01
10    ori $8, $8, 0xFFFF
11    sll $8, $8, 16
12    ori $8, $8, 0xFFFF

```

Registers (Registers View):

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0xffffffff
\$t1	9	0x00000000
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$t8	22	0x00000000
\$t9	23	0x00000000
\$t10	24	0x00000000
\$t11	25	0x00000000
\$t12	26	0x00000000
\$t13	27	0x00000000
\$gp	28	0x10000800
\$sp	29	0xffffeffc
\$fp	30	0x00000000
\$ra	31	0x00000000

Registers View Buttons:

- Backward Step
- Forward Step
- Breakpoint
- Reset
- Run
- Stop

Registers View Options:

- Hexadecimal Addresses
- Hexadecimal Values
- ASCII

## Programa 8:

Editor

mips1.asm

```

1 # Inicialmente escreva um programa que faça:
2 # $8 = 0x12345678.
3 # A partir do registrador $8 acima, usando apenas instruções lógicas (or, ori, and, andi,xor, xori)
4 # e instruções de deslocamento (sll, srl e sra), você deverá obter os seguintes
5 # valores nos respectivos registradores:
6 # $9 = 0x12
7 # $10 = 0x34
8 # $11 = 0x56
9 # $12 = 0x78
10
11 .text
12 main:
13     ori $8, $0, 0x1234 # $8 = 0x00001234
14     sll $8, $8, 16      # $8 = 0x12340000
15     ori $8, $8, 0x5678 # $8 = 0x12345678
16     sra $9, $8, 24      # $9 = $8 >> 24
17     sra $10, $8, 16     # $10 = $8 >> 16
18     andi $10, $10, 0xFF # $10 = and($10, 0xFF)
19     sra $11, $8, 8       # $11 = $8 >> 8
20     andi $11, $11, 0xFF # $11 = and($11, 0xFF)
21     or $12, $12, $8      # $12 = or($12, $8)
22     andi $12, $12, 0xFF # $12 = and($12, 0xFF)

```

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x12345678
\$t1	9	0x00000012
\$t2	10	0x00000034
\$t3	11	0x00000056
\$t4	12	0x00000078
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0xfffffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x04000028
hi		0x00000000
lo		0x00000000

## Programa 9:

Editor de código MIPS (mips1.asm)

```

1 # Considere a memória inicial da seguinte forma:
2 # .text
3 # .data
4 # x1: .word 15
5 # x2: .word 25
6 # x3: .word 13
7 # x4: .word 17
8 # soma: .word -1
9 # Escrever um programa que leia todos os números, calcule e substitua o
10 # valor da variável soma por este valor.
11
12 .text
13 main:
14     ori $t0, $t0, 0x1001
15     sll $t0, $t0, 16
16
17     lw $t1, 0($t0)
18     lw $t2, 4($t0)
19     lw $t3, 8($t0)
20     lw $t4, 12($t0)
21
22     add $t5, $t1, $t2
23     add $t5, $t5, $t3
24     add $t5, $t5, $t4
25
26     sw $t5, 16($t0)
27
28 .data
29     x1: .word 15
30     x2: .word 25
31     x3: .word 13
32     x4: .word 17
33     soma: .word -1

```

Visualização de memória e registradores.

Text Segment			Data Segment			Registers		
Bkpt	Address	Code	Basic	Address	Value (+0)	Name	Number	Value
	0x00400000	0x35081001	ori \$t0, \$t0, 0x1001	0x10000000	0x0000000f	\$zero	0	0x00000000
	0x00400004	0x00084400	sll \$t0, \$t0, 16	0x10000000	0x00000000	\$at	1	0x00000000
	0x00400008	0x8d090000	lw \$t1, 0(\$t0)	0x10000000	0x00000000	\$v0	2	0x00000000
	0x0040000c	0x8d090000	lw \$t2, 4(\$t0)	0x10000000	0x00000000	\$v1	3	0x00000000
	0x00400010	0x8d090000	lw \$t3, 8(\$t0)	0x10000000	0x00000000	\$a0	4	0x00000000
	0x00400014	0x8d090000	lw \$t4, 12(\$t0)	0x10000000	0x00000000	\$a1	5	0x00000000
	0x00400018	0x8d0a0004	add \$t5, \$t1, \$t2	0x10000000	0x00000000	\$a2	6	0x00000000
	0x0040001c	0x8d0a0004	add \$t5, \$t5, \$t3	0x10000000	0x00000000	\$a3	7	0x00000000
	0x00400020	0x8d0a0004	add \$t5, \$t5, \$t4	0x10000000	0x00000000	\$t0	8	0x10010000
	0x00400024	0xad0d0100	sw \$t5, 16(\$t0)	0x10000000	0x00000000	\$t1	9	0x0000000f
						\$t2	10	0x00000019
						\$t3	11	0x0000000d
						\$t4	12	0x00000011
						\$t5	13	0x00000046
						\$t6	14	0x00000000
						\$t7	15	0x00000000
						\$s0	16	0x00000000
						\$s1	17	0x00000000
						\$s2	18	0x00000000
						\$s3	19	0x00000000
						\$s4	20	0x00000000
						\$s5	21	0x00000000
						\$s6	22	0x00000000
						\$s7	23	0x00000000
						\$t8	24	0x00000000
						\$t9	25	0x00000000
						\$k0	26	0x00000000
						\$k1	27	0x00000000
						\$gp	28	0x10000800
						\$sp	29	0x7ffffefc
						\$fp	30	0x00000000
						\$ra	31	0x00000000
						pc		0x04000028
						hi		0x00000000
						lo		0x00000000

## Programa 10:

Editor de código MIPS com interface gráfica.

**Edição:**

**Execute:**

**mips1.asm\***

```

1 # Considere o seguinte programa: y = 127x - 65z + 1
2 # Faça um programa que calcule o valor de y conhecendo os valores de x e z.
3 # estão armazenados na memória e, na posição imediatamente a seguir, o valor de y deverá ser
4 # escrito, ou seja:
5 # .data
6 # x: .word 5
7 # z: .word 7
8 # y: .word 0 # esse valor deverá ser sobreescrito após a execução do programa.
9
10 .text
11 main:
12     ori $t0, $zero, 0x1001 # acessando a primeira posição da memória
13     sll $t0, $t0, 16        # acessando a primeira posição da memória
14
15     lw $t1, 0($t0)         # $t1 = $t[0]
16     lw $t2, 4($t0)         # $t2 = $t[1]
17
18     sll $t3, $t1, 7        # $t3 = $t1 * 128
19     sub $t3, $t3, $t1      # $t3 = $t3 - $t1
20
21     sll $t4, $t2, 6        # $t4 = $t2 * 64
22     add $t4, $t4, $t2      # $t3 = $t4 + $t2
23
24     sub $t5, $t3, $t4      # $t5 = $t3 - $t4
25     addi $t5, $t5, 1        # $t5 = $t5 + 1
26     sw $t5, 8($t0)         # y = 127x - 65z + 1
27
28 .data
29     x: .word 5
30     z: .word 7
31     y: .word 0

```

**Registers:**

Name	Number	Value
\$zero	0	0x00000000
\$t0	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000005
\$t2	10	0x00000007
\$t3	11	0x0000027b
\$t4	12	0x000001c7
\$t5	13	0x000000b5
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$t7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$t10	26	0x00000000
\$t11	27	0x00000000
\$sp	28	0x10000800
\$fp	29	0xffffeffc
\$ra	30	0x00000000
\$pc	31	0x00000000
hi		0x00000000
lo		0x00000000

**Text Segment:**

Bkt	Address	Code	Basic	Source
0x00400000	0x34081001	ori \$t0, \$zero, 0x1001	12:	ori \$t0, \$zero, 0x1001 # acessando a primeira posição da memória
0x00400004	0x00008440	add \$t0, \$t0, 16	13:	add \$t0, \$t0, 16 # acessando a primeira posição da memória
0x00400008	0x8dd90000	lw \$t1, 0(\$t0)	15:	lw \$t1, 0(\$t0) # \$t1 = \$t[0]
0x0040000c	0x8dd90000	lw \$t2, 4(\$t0)	16:	lw \$t2, 4(\$t0) # \$t2 = \$t[1]
0x00400010	0x0000959c	mul \$t1, \$t1, 7	18:	mul \$t1, \$t1, 7 # \$t3 = \$t1 ^ 128
0x00400014	0x01695822	sub \$t3, \$t1, \$t1	19:	sub \$t3, \$t1, \$t1 # \$t3 = \$t3 - \$t1
0x00400018	0x000a6180	mul \$t2, \$t2, 6	21:	mul \$t2, \$t2, 6 # \$t4 = \$t2 ^ 64
0x0040001c	0x018af020	add \$t4, \$t4, \$t2	22:	add \$t4, \$t4, \$t2 # \$t3 = \$t4 + \$t2
0x00400020	0x016c6822	sub \$t5, \$t3, \$t4	24:	sub \$t5, \$t3, \$t4 # \$t5 = \$t3 - \$t4
0x00400024	0x21ad0001	addi \$t5, \$t5, 1	25:	addi \$t5, \$t5, 1 # \$t5 = \$t5 + 1
0x00400028	0x0ad00008	sw \$t5, 8(\$t0)	26:	sw \$t5, 8(\$t0) # y = 127x - 65z + 1

**Data Segment:**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000005	0x00000007	0x00000005	0x00000005	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Opções de visualização: Texto, Hexadecimal, Decimal, Binário, ASCII.

## Programa 11:

Editor de código MIPS (mips1.asm)

```

1 # Considere o seguinte programa: y = x - z + 300000
2 # Faça um programa que calcule o valor de y conhecendo os valores de x e z. Os valores de x e z
3 # estão armazenados na memória e, na posição imediatamente a seguir, o valor de y deverá ser
4 # escrito, ou seja:
5 # .data
6 # x: .word 100000
7 # z: .word 200000
8 # y: .word 0 # esse valor deverá ser sobreescrito após a execução do programa.
9
10 .text
11 main:
12     ori $t0, $0, 0x1001 # Acessando a primeira posição da memória
13     sll $t0, $t0, 16    # Acessando a segunda posição da memória
14
15     lw $t1, 0($t0)      # $t1 = $t0[0]
16     lw $t2, 4($t0)      # $t2 = $t0[1]
17
18     sub $t3, $t1, $t2    # $t3 = $t1 - $t2
19     ori $t4, $0, 0x493E # $t4 = 0x493E
20     sll $t4, $t4, 4      # $t4 = 300000
21     add $t5, $t3, $t4    # $t5 = $t3 + $t4
22
23     sw $t5, 8($t0)      # y = x - z + 300000
24
25 .data
26 x: .word 100000
27 z: .word 200000
28 y: .word 0

```

Registers (Registers Coproc 1 Coproc 0)

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00010000
\$t1	9	0x00018ea0
\$t2	10	0x00030d40
\$t3	11	0xffff7960
\$t4	12	0x000493e0
\$t5	13	0x00030d40
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x00000000
\$sp	29	0xfffffffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x000400024
\$hi		0x00000000
\$lo		0x00000000

Text Segment (0x00400000 - 0x00400020)

Bkpt	Address	Code	Basic	Source
0x00400000	0x34081001	ori \$t0, \$0, 0x000001001	12:	ori \$t0, \$0, 0x1001 # Acessando a primeira posição da memória
0x00400004	0x00084400	sll \$t0, \$t0, 16	13:	sll \$t0, \$t0, 16 # Acessando a segunda posição da memória
0x00400008	0x4d090000	lw \$t1, 0(\$t0)	15:	lw \$t1, 0(\$t0) # \$t1 = \$t0[0]
0x0040000c	0x4d090000	lw \$t2, 4(\$t0)	16:	lw \$t2, 4(\$t0) # \$t2 = \$t0[1]
0x00400010	0x012a5822	sub \$t3, \$t1, \$t2	18:	sub \$t3, \$t1, \$t2 # \$t3 = \$t1 - \$t2
0x00400014	0x3404c93e	ori \$t4, \$0, 0x00000493e	19:	ori \$t4, \$0, 0x493E # \$t4 = 0x493E
0x00400018	0x0006c100	sll \$t4, \$t4, 4	20:	sll \$t4, \$t4, 4 # \$t4 = 300000
0x0040001c	0x016c6820	add \$t5, \$t3, \$t4	21:	add \$t5, \$t3, \$t4 # \$t5 = \$t3 + \$t4
0x00400020	0x4ad0d008	sw \$t5, 8(\$t0)	23:	sw \$t5, 8(\$t0) # y = x - z + 300000

Data Segment (0x10010000 - 0x10010140)

Address	Value (+0)	Value (+4)	Value (+8)	Value (+C)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00018ea0	0x00030d40	0x000030d40	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers (Registers Coproc 1 Coproc 0)

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00010000
\$t1	9	0x00018ea0
\$t2	10	0x00030d40
\$t3	11	0xffff7960
\$t4	12	0x000493e0
\$t5	13	0x00030d40
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x00000000
\$sp	29	0xfffffffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x000400024
\$hi		0x00000000
\$lo		0x00000000

## Programa 12:

Editor window showing assembly code for mips1.asm:

```

1 # Considere a seguinte situação:
2 # int ***x;
3 # onde x contém um ponteiro para um ponteiro para um ponteiro para um inteiro.
4 # Nessa situação, considere que a posição inicial de memória contenha o inteiro em questão.
5 # Coloque todos os outros valores em registradores, use os endereços de memória que quiser dentro
6 # do espaço de endereçamento do Mips.
7 # Resumo do problema:
8 # k = MEM [ MEM [MEM [ x ] ] ] .
9 # Crie um programa que implemente a estrutura de dados acima, leia o valor de K, o multiplique por
10 # 2 e o reescreva no local correto conhecendo-se apenas o valor de x.
11
12 .text
13 main:
14     ori $t0, $0, 0x1001 # Acessando a primeira posição da memória
15     sll $t0, $t0, 16    # Acessando a segunda posição da memória
16
17     lw $t1, 0($t0)      # $t1 = $t0[0]
18     lw $t2, 4($t0)      # $t2 = $t0[1]
19     lw $t3, 8($t0)      # $t3 = $t0[2]
20     lw $t4, 12($t0)     # $t4 = $t0[3]
21
22     sll $t5, $t4, 1      # $t5 = $t4 * 2 -> $t4 << 1
23     sw $t5, 0($t0)      # $t0[0] = $t5
24
25 .data
26 x: .word x1
27 x1: .word x2
28 x2: .word value
29 value: .word 15

```

Registers window showing register values:

Registers	Coproc 1	Coproc 0
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00010004
\$t2	10	0x10010008
\$t3	11	0x1001000c
\$t4	12	0x0000000f
\$t5	13	0x00000010
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0xfffffefc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00400020
hi		0x00000000
lo		0x00000000

## Programa 13:

**Edit   Execute**

**mips1.asm   mips1.asm**

```

1  # Escreva um programa que leia um valor A da memória, identifique se o número é negativo ou
2  # não e encontre o seu módulo. O valor deverá ser reescrito sobre A.
3
4  .text
5  main:
6      ori $t0, $0, 0x1001
7      sll $t0, $t0, 16
8
9      lw $t1, 0($t0)
10     sra $t2, $t1, 31
11     beq $t2, $0, notNegative
12     sub $t1, $0, $t1
13
14     notNegative:
15         sw $t1, 0($t0)
16
17 .data
18     a: .word -7
19

```

**Edit   Execute**

**Text Segment**

Blk#	Address	Code	Basic	Source
0x00400000	0x34081000	ori \$t0, \$0, 0x000001001	6:	ori \$t0, \$0, 0x1001
0x00400004	0x00084400	sll \$t0, \$t0, 16	7:	sll \$t0, \$t0, 16
0x00400008	0xd0d90000	lw \$t1, 0(\$t0)	9:	lw \$t1, 0(\$t0)
0x0040000c	0x000957c3	sra \$t2, \$t1, 31	10:	sra \$t2, \$t1, 31
0x00400010	0x11400001	beq \$t2, \$0, notNegative	11:	beq \$t2, \$0, notNegative
0x00400014	0x00094822	sub \$t1, \$0, \$t1	12:	sub \$t1, \$0, \$t1
0x00400018	0xada090000	sw \$t1, 0(\$t0)	15:	sw \$t1, 0(\$t0)

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000007	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

0x10010000 (.data)  Hexadecimal Addresses  Hexadecimal Values  ASCII

More Memory Run IO

**Registers**

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000007
\$t2	10	0xffffffffff
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040001c
hi		0x00000000
lo		0x00000000

## Programa 14:

Editor interface showing assembly code and debugger windows.

**mips1.asm**

```

1  # Escreva um programa que leia um valor A da memória, identifique se o número é par ou não.
2  # Um valor deverá ser escrito na segunda posição livre da memória (0 para par e 1 para ímpar).
3
4 .text
5 main:
6     ori $t0, $0, 0x1001
7     sll $t0, $t0, 16
8
9     lw $t1, 0($t0)
10    andi $t2, $t1, 1
11    beq $t2, $0, is_par
12    j fim
13
14 is_par:
15    ori $t2, $0, 0
16
17 fim:
18    sw $t2, 4($t0)
19
20 .data
21 a: .word 21
22

```

**Registers** window:

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000015
<b>\$t2</b>	10	0x00000001
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
<b>\$s2</b>	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x1ffffefc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x04000020
hi		0x00000000
lo		0x00000000

**Text Segment** window:

Bkpt	Address	Code	Basic	Source
	0x000400000	0x34030001	ori \$t0, \$0, 0x1001	
	0x000400004	0x00004400	sll \$t0, \$t0, 16	
	0x000400008	0x8d090000	lw \$t1, 0(\$t0)	
	0x00040000c	0x312a0001	andi \$t1, \$t1, 1	
	0x000400010	0x11400001	beq \$t1, \$0, is_par	
	0x000400014	0x08100007	j 0x040001c	
	0x000400018	0x340a0000	ori \$t2, \$0, 0x00000000	
	0x00040001c	0xad0a0004	sw \$t2, 4(\$t0)	

**Data Segment** window:

Address	Value (+0)	Value (+4)	Value (+8)	Value (+C)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x000000015	0x000000001	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

## Programa 15:

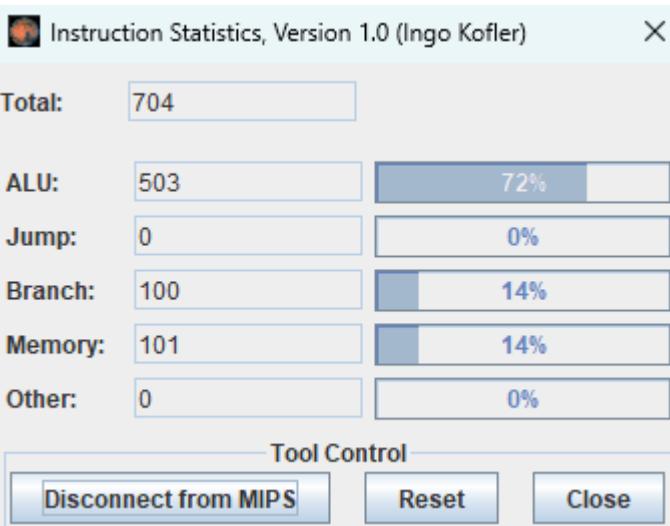
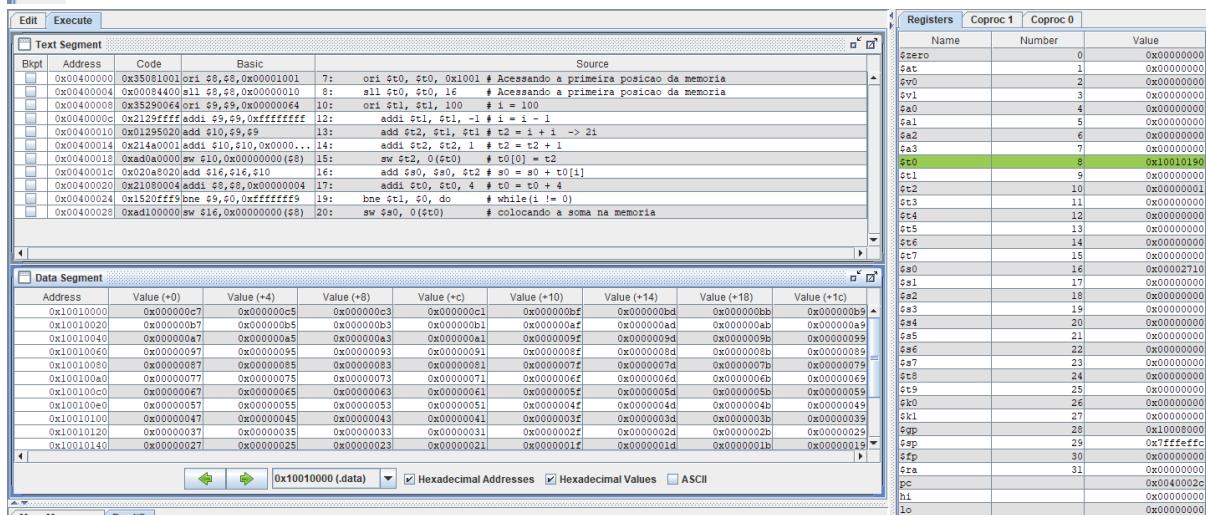
Edit Execute

mips1.asm

```

1 # Escrever um programa que crie um vetor de 100 elementos na memoria onde vetor[i] = 2*i +1.
2 # Ap s a ltima posic o do vetor criado, escrever a soma de todos os valores armazenados do vetor.
3 # Use o MARS para verificar a quantidade de instru es conforme o tipo (ULA, Desvios, Mem ou Outras)
4
5 .text
6 main:
7     ori $t0, $t0, 0x1001 # Acessando a primeira posic o da memoria
8     sll $t0, $t0, 16      # Acessando a primeira posic o da memoria
9
10    ori $t1, $t1, 100    # i = 100
11    do:                 # looping para soma
12        addi $t1, $t1, -1 # i = i - 1
13        add $t2, $t1, $t1 # t2 = i + i -> 2i
14        addi $t2, $t2, 1   # t2 = t2 + 1
15        sw $t2, 0($t0)   # t0[0] = t2
16        add $s0, $s0, $t2 # s0 = s0 + t0[i]
17        addi $t0, $t0, 4   # t0 = t0 + 4
18
19    bne $t1, $0, do      # while(i != 0)
20    sw $s0, 0($t0)      # colocando a soma na memoria

```



## Programa 16:

Editor window showing assembly code for a MIPS program:

```

1 # Escreva um programa que avalie a expressão: (x*y)/z.
2 # Use x = 1600000 (=0x186A00), y = 80000 (=0x13880), e z = 400000 (=0x61A80). Inicializar os
3 # registradores com os valores acima.

4
5 .text
6 main:
7     ori $t0, $t0, 0x1001
8     sll $t0, $t0, 16
9
10    lw $t1, 0($t0)
11    lw $t2, 4($t0)
12    lw $t3, 8($t0)
13
14    div $t1, $t2
15    mflo $t4
16    mult $t4, $t3
17    mflo $t4
18
19    sw $t4, 12($t0)
20
21 .data
22     x: .word 0x186A00
23     y: .word 0x13880
24     z: .word 0x61A80

```

Registers window showing initial register values:

Registers	Coproc 1	Coproc 0
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x0186A000
\$t2	10	0x00013880
\$t3	11	0x00061A80
\$t4	12	0x0007a1200
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0xfffffefffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0400028
hi		0x00000000
lo		0x0007a1200

## Programa 17:

**Edit   Execute**

**mips1.asm**

```

1 # Para a expressão a seguir, escreva um programa que calcule o valor de k:
2 # k = x * y (Você deverá realizar a multiplicação através de somas!)
3 # O valor de x deve ser lido da primeira posição livre da memória e o valor de y deverá lido da
4 # segunda posição livre. O valor de k, após calculado, deverá ainda ser escrito na terceira posição
5 # livre da memória.
6
7 .text
8 main:
9     ori $t0, $t0, 0x1001 # Acessando a primeira posicao da memoria
10    sll $t0, $t0, 16      # Acessando a primeira posicao da memoria
11
12    lw $t1, 0($t0)        # $t1 = $t0[0]
13    lw $t2, 4($t0)        # $t2 = $t0[1]
14
15    calcular:             # looping para calcular
16        add $t3, $t3, $t1    # $t3 = $t3 + $t1
17        addi $t2, $t2, -1    # $t2 = $t2 - 1
18        bne $t2, $0, calcular # if($t2 != $0){goto calcular}
19        sw $t3, 8($t0)       # t0[3] = $t3
20
21 .data
22 x: .word 10
23 y: .word 5
24 k: .word -1

```

**Registers**   **Coproc 1**   **Coproc 0**

Name	Number	Value
\$zero	0	0
\$at	1	0
\$v0	2	0
\$v1	3	0
\$a0	4	0
\$a1	5	0
\$a2	6	0
\$a3	7	0
\$t0	8	268500992
\$t1	9	0
\$t2	10	0
\$t3	11	50
\$t4	12	0
\$t5	13	0
\$t6	14	0
\$t7	15	0
\$a0	16	0
\$s1	17	0
\$s2	18	0
\$s3	19	0
\$s4	20	0
\$s5	21	0
\$s6	22	0
\$s7	23	0
\$t8	24	0
\$t9	25	0
\$k0	26	0
\$k1	27	0
\$gp	28	268468224
\$sp	29	2147479545
\$fp	30	0
\$ra	31	0
pc		4194386
nl		0
lo		0

**Text Segment**

Bkp	Address	Code	Basic	Source
	0x00040000	0x35010001	ori \$t0, \$t0, 0x1001	# Acessando a primeira posicao da memoria
	0x00040004	0x35000000	sll \$t0, \$t0, 16	# Acessando a primeira posicao da memoria
	0x00040008	0x34000000	lw \$t1, 0(\$t0)	# \$t1 = \$t0[0]
	0x0004000c	0x34000004	lw \$t2, 4(\$t0)	# \$t2 = \$t0[1]
	0x00040010	0x14000000	add \$t3, \$t3, \$t1	# \$t3 = \$t3 + \$t1
	0x00040014	0x34000000	addi \$t2, \$t2, -1	# \$t2 = \$t2 - 1
	0x00040018	0x14000000	bne \$t2, \$0, calcular	# if(\$t2 != \$0){goto calcular}
	0x0004001c	0x34000000	sw \$t3, 8(\$t0)	# t0[3] = \$t3

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	10	5	50	0	0	0	0	0
0x10010020	0	0	0	0	0	0	0	0
0x10010040	0	0	0	0	0	0	0	0
0x10010060	0	0	0	0	0	0	0	0
0x10010080	0	0	0	0	0	0	0	0
0x100100a0	0	0	0	0	0	0	0	0
0x100100c0	0	0	0	0	0	0	0	0
0x100100e0	0	0	0	0	0	0	0	0
0x10010100	0	0	0	0	0	0	0	0
0x10010120	0	0	0	0	0	0	0	0
0x10010140	0	0	0	0	0	0	0	0

0x10010000 (.data)    Hexadecimal Addresses    Hexadecimal Values    ASCII

## Programa 18:

**mips1.asm**

```

7  # Dê um valor para x e y (dê valores pequenos !!) e use o MARS para verificar a quantidade de
8  # instruções conforme o tipo (ULA, Desvios, Mem ou Outras)
9
10 .text
11 main:
12     ori $t0, $0, 0x1001
13     sll $t0, $t0, 16
14
15     lw $s0, 0($t0)
16     lw $s1, 4($t0)
17
18     ori $t3, $0, 1
19     ori $t1, $0, 0
20
21 pow:
22     beq $t1, $s1, end
23     addi $t1, $t1, 1
24
25     ori $t2, $0, 0
26     ori $t4, $0, 0
27
28 soma:
29     beq $t2, $t3, pow_next
30     add $t4, $t4, $s0
31     addi $t2, $t2, 1
32     j soma
33
34 pow_next:
35     or $t3, $t4, $zero
36     j pow
37
38 end:
39     sw $t3, 8($t0)

```

**Text Segment**

Bkpt	Address	Code	Basic	Source
	0x04000010	0x340b0001	ori \$t1,\$0,0x00000001	18: ori \$t3,\$0,1
	0x04000014	0x34090001	ori \$t1,\$0,0x00000000	19: ori \$t1,\$0,0
	0x04000018	0x11310009	beq \$t1,\$t1,0x00000009	22: beq \$t1,\$s1,end
	0x0400001c	0x21290001	addi \$t1,\$t1,1	23: addi \$t1,\$t1,1
	0x04000020	0x340a0000	ori \$t0,\$0,0x00000000	25: ori \$t2,\$0,0
	0x04000024	0x340c0000	ori \$t2,\$0,0x00000000	26: ori \$t4,\$0,0
	0x04000028	0x340e0000	ori \$t3,\$0,0x00000000	27: ori \$t3,\$0,0
	0x0400002c	0x010f0000	addi \$t1,\$t1,f16	30: addi \$t1,\$t1,f16
	0x04000030	0x21440001	addi \$t0,\$t0,0x00000000...	31: addi \$t0,\$t0,0x00000000
	0x04000034	0x0810000a	or \$t1,\$t2,\$0	32: j soma
	0x04000038	0x01050825	or \$t1,\$t2,\$0	35: or \$t3,\$t4,\$zero
	0x0400003c	0x0810000e	j 0x04000018	36: j pow
	0x04000040	0xad0b0008	sw \$t1,0x00000008(\$t0)	39: sw \$t3,8(\$t0)

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+12)	Value (+16)	Value (+18)	Value (+1c)
0x10010000	0x00000002	0x00000003	0x00000008	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

**Instruction Statistics, Version 1.0 (Ingo Kofler)**

Total:	57	
ALU:	30	53%
Jump:	10	18%
Branch:	14	25%
Memory:	3	5%
Other:	0	0%

**Tool Control**

**1. Se tivermos 2 inteiros, cada um com 32 bits, quantos bits podemos esperar para o produto?**

- A. 16
- B. 32
- C. 64
- D. 128

**2. Quais os registradores que armazenam os resultados na multiplicação?**

- A. high e low
- B. hi e lo
- C. R0 e R1
- D. \$0 e \$1

**3. Qual a operação usada para multiplicar inteiros em comp. de dois?**

- A. mult
- B. multu
- C. multi
- D. Mutt

**4. Qual instrução move os bits menos significativos da multiplicação para o reg. 8?**

- A. move \$8,lo
- B. mvlo \$8,lo
- C. mflo \$8
- D. addu \$8,\$0,lo

**5. Se tivermos dois inteiros, cada um com 32 bits, quantos bits deveremos estar preparados para receber no quociente?**

- A. 16
- B. 32

C. 64

D. 128

**6. Após a instrução div, qual registrador possui o quociente?**

A. lo

B. hi

C. high

D. \$2

**7. Qual a inst. Usada para dividir dois inteiros em comp. de dois?**

A. dv

B. divide

C. divu

D. div

**8. Faça um arithmetic shift right de dois no seguinte padrão de bits: 1001**

**1011**

A. 1110 0110

**B. 0010 0110**

C. 1100 1101

D. 0011 0111

**9. Qual o efeito de um arithmetic shift right de uma posição?**

A. Se o inteiro for unsigned, o shift o divide por 2. Se o inteiro for signed, o shift o divide por 2.

B. Se o inteiro for unsigned, o shift o divide por 2. Se o inteiro for signed, o shift pode resultar em um valor errado.

C. Se o inteiro for unsigned, o shift pode ocasionar um valor errado. Se o inteiro for signed, o shift o divide por 2.

D. O shift multiplica o número por dois.

**10. Qual sequencia de instruções avalia  $3x+7$ , onde x é iniciado no reg. \$8 e o resultado armazenado em \$9?**

A.

```
ori $3,$0,3  
mult $8,$3  
mflo $9  
addi $9,$9,7
```

B.

```
ori $3,$0,3  
mult $8,$3  
addi $9,$8,7
```

C.

```
ori $3,$0,3  
mult $8,$3  
mfhi $9  
addi $9,$9,7
```

D.

```
mult $8,3  
mflo $9  
addi $9,$9,7
```

**Programa 19:**

Editor Execute

mips1.asm\*

```

1 # Escrever um programa que leia dois números da memória, a primeira e segunda posições
2 # respectivamente (os coloque em $s0 e $s1) e determine a quantidade de bits significantes de cada
3 # um. Coloque as respostas em $t0 e $t1, a partir desse resultado faça a multiplicação. Caso o número
4 # de bits significantes de ambos seja menor do que 32 a resposta deverá estar apenas em $s2, caso
5 # contrário a resposta estará em $s2 e $s3 (LO e HI respectivamente).
6
7 .text
8 main:
9    ori $t0, $t0, 0x1001 # Acessando a primeira posição da memória
10   sll $t0, $t0, 16 # Acessando a segunda posição da memória
11
12  lw $s0, 0($t0)      # $s0 = x
13  lw $s1, 4($t0)      # $s1 = y
14  ori $t1, $t1, 0      # contador de bits de x
15  ori $t2, $t2, 0      # contador de bits de y
16  or $t3, $0, $s0      # $t3 = x
17
18 countX:
19    beq $t3, $0, countY # if(t3 == 0){goto countY}
20    srl $t3, $t3, 1      # $t3 = $t3 >> 1
21    addi $t1, $t1, 1      # $t1 = $t1 + 1
22    j countX            # goto countX
23
24 countY:
25    or $t3, $0, $s1      # $t3 = y
26
27 do:
28    beq $t3, $0, multi  # if(t3 == 0){goto multi}
29    srl $t3, $t3, 1      # $t2 = $t2 >> 1
30    addi $t2, $t2, 1      # $t2 = $t2 + 1
31    j do                # goto do
32
33 multi:
34    mult $t1, $t2        # x * y
35    mflo $s2              # $s2 = LOW
36    mfhi $s3              # $s3 = HIGH
37
38    slti $t5, $t1, 32     # if($t1 < 32){$t5 = 1}
39    slti $t6, $t2, 32     # if($t2 < 32){$t6 = 1}
40    and $t7, $t5, $t6     # $t7 = 1
41
42    beq $t7, $0, save    # if($t7 == 0){goto save}
43    sw $s2, 8($t0)       # else if($t7 == 1){t0[3] = $s2}
44    j end                # goto end
45
46 save:
47    sw $s2, 8($t0)       # t0[3] = $s2
48    sw $s3, 12($t0)      # t0[4] = $s3
49
50 end:
51    nop                  # Null operation
52
53 .data
54 x: .word 4
55 y: .word 3

```

Editor Execute

Text Segment		Data Segment		Registers		Coproc 1		Coproc 0	
Bkpt	Address	Code	Basic	Name	Number	Value			
	0x00000000	0x35000001lw \$t0, 0(\$t0)	0x1001 # Acessando a primeira posição da memória	\$zero	0	0x00000000			
	0x00000004	0x35000004ori \$t0, \$t0, 16	# Acessando a segunda posição da memória	\$at	1	0x00000000			
	0x00000008	0x35000008sll \$t0, \$t0, 16		\$v0	2	0x00000000			
	0x0000000c	0x3500000caddi \$t1, \$t1, 0		\$v1	3	0x00000000			
	0x00000010	0x35000010ori \$t1, \$t1, 0		\$a0	4	0x00000000			
	0x00000014	0x35000014addi \$t2, \$t2, 0		\$a1	5	0x00000000			
	0x00000018	0x35000018or \$t3, \$0, \$t0		\$a2	6	0x00000000			
	0x00000020	0x35000020beq \$t3, \$0, 0x00000001		\$a3	7	0x00000000			
	0x00000024	0x35000024srl \$t3, \$t3, 1	# \$t3 = \$t3 >> 1	\$t0	8	0x00000000			
	0x00000028	0x35000028addi \$t1, \$t1, 1	# \$t1 = \$t1 + 1	\$t1	9	0x00000001			
	0x00000032	0x35000032j 0x00040001c		\$t2	10	0x00000002			
	0x00000036	0x35000036and \$t1, \$t1, 1	# \$t1 = \$t1 + 1	\$t3	11	0x00000000			
	0x00000040	0x35000040j 0x00040001c		\$t4	12	0x00000000			
	0x00000044	0x35000044or \$t3, \$0, \$t1		\$t5	13	0x00000001			
	0x00000048	0x35000048beq \$t3, \$0, 0x00000003		\$t6	14	0x00000001			
	0x00000052	0x35000052mult \$t1, \$t2	# x * y	\$t7	15	0x00000001			
	0x00000056	0x35000056mflo \$s2	# \$s2 = LOW	\$t8	16	0x00000000			
	0x00000060	0x35000060mfhi \$s3	# \$s3 = HIGH	\$t9	17	0x00000003			
	0x00000064	0x35000064slti \$t5, \$t1, 32	# if(\$t1 < 32){\$t5 = 1}	\$t10	18	0x00000006			
	0x00000068	0x35000068slti \$t6, \$t2, 32	# if(\$t2 < 32){\$t6 = 1}	\$t11	19	0x00000000			
	0x00000072	0x35000072and \$t7, \$t5, \$t6	# \$t7 = 1	\$t12	20	0x00000000			
	0x00000076	0x35000076beq \$t7, \$0, 0x00000001		\$t13	21	0x00000000			
	0x00000080	0x35000080srl \$t3, \$t3, 1	# \$t3 = \$t3 >> 1	\$t14	22	0x00000000			
	0x00000084	0x35000084addi \$t1, \$t1, 1	# \$t1 = \$t1 + 1	\$t15	23	0x00000000			
	0x00000088	0x35000088j 0x00040001c		\$t16	24	0x00000000			
	0x00000092	0x35000092or \$t3, \$0, \$t1		\$t17	25	0x00000000			
	0x00000096	0x35000096beq \$t3, \$0, 0x00000003		\$t18	26	0x00000000			
	0x000000a0	0x350000a0mult \$t1, \$t2	# x * y	\$t19	27	0x00000000			
	0x000000a4	0x350000a4mflo \$s2	# \$s2 = LOW	\$t20	28	0x00000000			
	0x000000a8	0x350000a8mfhi \$s3	# \$s3 = HIGH	\$t21	29	0xffffffff			
	0x000000b2	0x350000b2nop	# Null operation	\$t22	30	0x00000000			
	0x000000b6	0x350000b6nop		\$t23	31	0x00000000			
	0x000000c0	0x350000c0nop							
	0x000000c4	0x350000c4nop							
	0x000000c8	0x350000c8nop							
	0x000000d2	0x350000d2nop							
	0x000000d6	0x350000d6nop							
	0x000000e0	0x350000e0nop							
	0x000000e4	0x350000e4nop							
	0x000000e8	0x350000e8nop							
	0x000000f2	0x350000f2nop							
	0x000000f6	0x350000f6nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
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	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
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	0x000000f4	0x350000f4nop							
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	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
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	0x000000f4	0x350000f4nop							
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	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
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	0x000000f8	0x350000f8nop							
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	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
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	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
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	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
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	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							
	0x000000f4	0x350000f4nop							
	0x000000f8	0x350000f8nop							
	0x000000f0	0x350000f0nop							

**Edit   Execute**

**mips1.asm**

```

1 # Os valores de x devem ser lidos da primeira posição livre da memória e o
2 # valor de y deverá ser escrito na segunda posição livre.
3
4 .text
5 main:
6     ori $t0, $t0, 0x1001 # Acessando a primeira posição da memória
7     sll $t0, $t0, 16      # Acessando a segunda posição da memória
8
9     lw $s0, 0($t0)       # x = t0[0]
10    and $t1, $s0, 1      # Verificar se é ímpar ou par
11
12    mult $s0, $s0
13    mflo $s1             # x^2
14
15    mult $s1, $s0
16    mflo $s2             # x^3
17
18    mult $s1, $s1
19    mflo $s3             # x^4
20
21    mult $s3, $s0
22    mflo $s4             # x^5
23    bne $t1, $0, impar   # if(x != 0){goto impar}
24
25 par:                  # x^4 + x^3 - 2x^2
26        add $t2, $s3, $s2
27        add $t3, $s1, $s1
28        sub $t4, $t2, $t3
29        sw $t4, 4($t0)
30    j end
31
32 impar:                 # x^5 - x^3 + 1
33        sub $t5, $s4, $s2
34        addi $t6, $t5, 1
35        sw $t6, 4($t0)
36
37 end:
38     nop
39 .data
40 x: .word 5

```

**Registers   Coproc 1   Coproc 0**

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000001
\$t2	10	0x00000000
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000005
\$s1	17	0x00000019
\$s2	18	0x0000007d
\$s3	19	0x00000271
\$s4	20	0x00000c35
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffffeffc
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x00000059
hi		0x00000000
lo		0x00000059

## Programa 21:

Editor | Execute

mips1.asm

```

1 # Os valores de x devem ser lidos da primeira posição livre da memória e o
2 # valor de y deverá ser escrito na segunda posição livre.
3
4 .text
5 main:
6     ori $t0, $t0, 0x1001 # Acessando a primeira posição da memória
7     sll $t0, $t0, 16      # Acessando a primeira posição da memória
8
9     lw $s0, 0($t0)       # x = t0[0]
10    and $t1, $s0, 1      # Verificar se é ímpar ou par
11
12    mult $s0, $s0
13    mflo $s1              # x^2
14
15    mult $s1, $s0
16    mflo $s2              # x^3
17
18    mult $s1, $s1
19    mflo $s3              # x^4
20
21    slt $t1, $s0, $s0
22    beq $t1, $0, menor   # if(x <= 0){goto menor}
23
24    maior:                # x^3 + 1
25        addi $t2, $s2, 1
26        sw $t2, 4($t0)
27        j end
28
29    menor:                 # x^4 - 1
30        addi $t3, $s3, -1
31        sw $t3, 4($t0)
32
33    end:
34        nop
35 .data
36    x: .word 2

```

Registers | Coproc 1 | Coproc 0

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x00000000
\$t1	9	0x00000001
\$t2	10	0x00000009
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000002
\$s1	17	0x00000004
\$s2	18	0x00000008
\$s3	19	0x00000010
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10000000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00000048
\$hi		0x00000000
\$lo		0x00000010

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00000000	0x3c030001	ori \$t0, \$t0, 0x1001	# Acessando a primeira posição da memória
	0x00000004	0x3d030440	sll \$t0, \$t0, 16	# Acessando a primeira posição da memória
	0x00000008	0xd4d10000	lw \$s0, 0(\$t0)	# x = t0[0]
	0x0000000c	0x32090001	andi \$t1, \$s0, 1	# Verificar se é ímpar ou par
	0x00000010	0x31200018	mult \$s0, \$s0	
	0x00000014	0x00000811	mflo \$s1	# x^2
	0x00000018	0x32300018	mult \$t2, \$t1	
	0x0000001c	0x00000912	mflo \$s2	# x^3
	0x00000020	0x32310010	mult \$t2, \$t1	
	0x00000024	0x00000912	mflo \$s3	# x^4
	0x00000028	0x00010482	slt \$t1, \$s0, \$s0	
	0x0000002c	0x11200003	beq \$t1, \$0, menor	# if(x <= 0){goto menor}
	0x00000030	0x224a001	addi \$t2, \$s2, 1	

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+C)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000002	0x00000009	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

0x10010000 (.data) Hexadecimal Addresses Hexadecimal Values ASCII