

## Exercício Prático 6:

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### Parte 1:

#### 1. O que é um arquivo fonte?

- A. um arquivo de texto que contém instruções de linguagem de programação.
- B. um subdiretório que contém os programas.
- C. um arquivo que contém dados para um programa.
- D. um documento que contém os requisitos para um projeto.

#### 2. O que é um registrador?

- A. parte do sistema de computador que mantém o controle dos parâmetros do sistema.
- B. uma parte do processador que possui um padrão de bits.
- C. parte do processador que contém o seu número de série único.
- D. parte do bus de sistema que contém dados.

#### 3. Qual o caracter que, na linguagem assembly do SPIM, inicia um comentário?

- A. #
- B. \$
- C. //
- D. \*

#### 4. Quantos bits há em cada instrução de máquina MIPS?

- A. 8
- B. 16
- C. 32
- D. instruções diferentes possuem diferentes comprimentos.

**5. O que é o contador de programa?**

- A. um registrador que mantém a conta do número de erros durante a execução de um programa.
- B. uma parte do processador que contém o endereço da primeira palavra de dados.
- C. uma variável na montadora que os números das linhas do arquivo de origem.
- D. parte do processador que contém o endereço da próxima instrução de máquina para ser obtida.

**6. Ao executarmos uma instrução, quanto será adicionado ao contador de programa?**

- A. 1
- B. 2
- C. 4
- D. 8

**7. O que é uma diretiva, tal como a diretiva .text?**

- A. uma instrução em linguagem assembly que resulta em uma instrução em linguagem de máquina.
- B. uma das opções de menu do sistema SPIM.
- C. uma instrução em linguagem de máquina que faz com que uma operação sobre os dados ocorra.
- D. uma declaração que diz o montador algo sobre o que o programador quer, mas não corresponde diretamente a uma instrução de máquina.

**8. O que é um endereço simbólico?**

- A. um local de memória que contém dados simbólicos.
- B. um byte na memória que contém o endereço de dados.
- C. símbolo dado como argumento para uma directiva.
- D. um nome usado no código-fonte em linguagem assembly para um local na memória.

**9. Em qual endereço o simulador SPIM coloca a primeira instrução de máquina quando ele está sendo executado?**

- A. 0x00000000
- B. 0x00400000
- C. 0x10000000
- D. 0xFFFFFFFF

**10. Algumas instruções de máquina possuem uma constante como um dos operandos. Como é chamado tal operando?**

- A. operando imediato
- B. operando embutido
- C. operando binário
- D. operando de máquina

**11. Como é chamada uma operação lógica executada entre bits de cada coluna dos operandos para produzir um bit de resultado para cada coluna?**

- A. operação lógica
- B. operação bitwise
- C. operação binária
- D. operação coluna

**12. Quando uma operação é de fato executada, como estão os operandos na ALU?**

- A. Pelo menos um operando deve ser de 32 bit.
- B. Cada operando pode ser de qualquer tamanho.
- C. Ambos operandos devem que vir de registros.
- D. Cada um dos registradores deve possuir 32 bit.

**13. Dezesesseis bits de dados de uma instrução de ori são usados como um operando imediato. Durante execução, o que deve ser feito primeiro?**

- A. Os dados são estendidos em zero à direita por 16 bits.
- B. Os dados são estendidos em zero à esquerda por 16 bits.
- C. Nada precisa ser feito.
- D. Apenas 16 bits são usados pelo outro operando.

**14. Qual das instruções seguintes armazenam no registrador \$5 um padrão de bits que representa positivo 48?**

- A. ori \$5,\$0,0x48
- B. ori \$5,\$5,0x48
- C. ori \$5,\$0,48
- D. ori \$0,\$5,0x48

**15. A instrução de ori pode armazenar o complemento de dois de um número em um registrador?**

- A. Não.
- B. Sim.

**16. Qual das instruções seguintes limpa todos os bits no registrador \$8 com exceção do byte de baixa ordem que fica inalterado?**

- A. ori \$8,\$8,0xFF
- B. ori \$8,\$0,0x00FF
- C. xori \$8,\$8,0xFF
- D. andi \$8,\$8,0xFF

**17. Qual é o resultado de um ou exclusivo de padrão sobre ele mesmo?**

- A. Todos os bits em zero.
- B. Todos os bits em um.
- C. O padrão original utilizado.
- D. O resultado é o contrário do original.

**18. Todas as instruções de máquina têm os mesmos campos?**

- A. Não. Diferentes de instruções de máquina possuem campos diferentes.
- B. Não. Cada instrução de máquina é completamente diferente de qualquer outra.
- C. Sim. Todas as instruções de máquina têm os mesmos campos na mesma ordem.
- D. Sim. Todas as instruções de máquina têm os mesmos campos, mas eles podem estar em ordens diferentes

## Parte 2:

### Programa 1:

```

mips1.asm
1  #a = 2
2  #b = 3
3  #c = 4
4  #d = 5
5  #x = (a+b) - (c+d)
6  #y = a - b + x
7  #b = x - y
8
9  .text
10 main:
11     ori $s0, $zero, 2 # a = 2
12     ori $s1, $zero, 3 # b = 3
13     ori $s2, $zero, 4 # c = 4
14     ori $s3, $zero, 5 # d = 5
15     add $t0, $s0, $s1 # t0 = a + b
16     add $t1, $s2, $s3 # t1 = c + d
17     sub $t0, $t0, $t1 # t0 = t0 - t1
18     sub $t1, $s0, $s1 # t1 = a - b
19     add $t1, $t1, $t0 # t1 = t1 + t0
20     sub $s1, $t0, $t1 # b = t0 - t1

```

The screenshot shows a MIPS simulator interface with three main windows:

- Text Segment:** Displays the assembly code with addresses and hex values. The code is as follows:
 

```

      0x00400000: ori $s0, $zero, 2 # a = 2
      0x00400004: ori $s1, $zero, 3 # b = 3
      0x00400008: ori $s2, $zero, 4 # c = 4
      0x0040000c: ori $s3, $zero, 5 # d = 5
      0x00400010: add $t0, $s0, $s1 # t0 = a + b
      0x00400014: add $t1, $s2, $s3 # t1 = c + d
      0x00400018: sub $t0, $t0, $t1 # t0 = t0 - t1
      0x0040001c: sub $t1, $s0, $s1 # t1 = a - b
      0x00400020: add $t1, $t1, $t0 # t1 = t1 + t0
      0x00400024: sub $s1, $t0, $t1 # b = t0 - t1
      
```
- Data Segment:** Shows memory values for addresses from 0x10010000 to 0x10010140. All values are 0.
- Registers:** Shows the state of registers. The \$s1 register is highlighted with a value of 17. Other registers like \$zero, \$at, \$v0, \$v1, \$a0, \$a1, \$a2, \$a3, \$t0, \$t1, \$t2, \$t3, \$t4, \$t5, \$t6, \$t7, \$s0, \$s2, \$s3, \$s4, \$s5, \$s6, \$s7, \$s8, \$s9, \$k0, \$k1, \$gp, \$sp, \$fp, \$ra, \$pc, \$hi, and \$lo are also shown with their respective values.



### Programa 3:

```

Edit  Execute
-----
mips1.asm
1  #x = 3
2  #y = 4
3  #z = ( 15*x + 67*y)*4
4
5  .text
6  main:
7      ori $s0, $zero, 3 # x = 3
8      ori $s1, $zero, 4 # y = 4
9      add $t0, $s0, $s0 # t0 = x + x  -> 2x
10     add $t0, $t0, $t0 # t0 = t0 + t0 -> 4x
11     add $t0, $t0, $t0 # t0 = t0 + t0 -> 8x
12     add $t0, $t0, $t0 # t0 = t0 + t0 -> 16x
13     sub $t0, $t0, $s0 # t0 = t0 - x  -> 15x
14
15     add $t1, $s1, $s1 # t1 = y + y  -> 2y
16     add $t1, $t1, $t1 # t1 = t1 + t1 -> 4y
17     add $t1, $t1, $t1 # t1 = t1 + t1 -> 8y
18     add $t1, $t1, $t1 # t1 = t1 + t1 -> 16y
19     add $t1, $t1, $t1 # t1 = t1 + t1 -> 32y
20     add $t1, $t1, $t1 # t1 = t1 + t1 -> 64y
21     add $t1, $t1, $s1 # t1 = t1 + y  -> 65y
22     add $t1, $t1, $s1 # t1 = t1 + y  -> 66y
23     add $t1, $t1, $s1 # t1 = t1 + y  -> 67y
24
25     add $t2, $t0, $t1 # t2 = t0 + t1
26     add $t2, $t2, $t2 # t2 = t2 + t2 -> 2 * t2
27     add $t2, $t2, $t2 # t2 = t2 + t2 -> 4 * t2
28     add $s2, $zero, $t2 # z = t2

```

Edit Execute					Registers Coproc 1 Coproc 0		
Text Segment					Name	Number	Value
Bkpt	Address	Code	Basic	Source			
	0x0040001c	0x02314820	add \$9,\$17,\$17	15: add \$t1, \$s1, \$s1 # t1 = y + y -> 2y	\$zero	0	0x00000000
	0x00400020	0x01294820	add \$9,\$9,\$9	16: add \$t1, \$t1, \$t1 # t1 = t1 + t1 -> 4y	\$at	1	0x00000000
	0x00400024	0x01294820	add \$9,\$9,\$9	17: add \$t1, \$t1, \$t1 # t1 = t1 + t1 -> 9y	\$v0	2	0x00000000
	0x00400028	0x01294820	add \$9,\$9,\$9	18: add \$t1, \$t1, \$t1 # t1 = t1 + t1 -> 16y	\$v1	3	0x00000000
	0x00400030	0x01294820	add \$9,\$9,\$9	19: add \$t1, \$t1, \$t1 # t1 = t1 + t1 -> 32y	\$a0	4	0x00000000
	0x00400034	0x01294820	add \$9,\$9,\$9	20: add \$t1, \$t1, \$t1 # t1 = t1 + t1 -> 64y	\$a1	5	0x00000000
	0x00400038	0x01314820	add \$9,\$9,\$17	21: add \$t1, \$t1, \$s1 # t1 = t1 + y -> 65y	\$a2	6	0x00000000
	0x0040003c	0x01314820	add \$9,\$9,\$17	22: add \$t1, \$t1, \$s1 # t1 = t1 + y -> 66y	\$a3	7	0x00000000
	0x00400040	0x01095020	add \$10,\$9,\$9	23: add \$t1, \$t1, \$s1 # t1 = t1 + y -> 67y	\$t0	8	0x0000002d
	0x00400044	0x01445020	add \$10,\$10,\$10	24: add \$t2, \$t0, \$t1 # t2 = t0 + t1	\$t1	9	0x0000001c
	0x00400048	0x01445020	add \$10,\$10,\$10	26: add \$t2, \$t2, \$t2 # t2 = t2 + t2 -> 2 * t2	\$t2	10	0x0000004e
	0x0040004c	0x000a9020	add \$18,\$0,\$10	27: add \$t2, \$t2, \$t2 # t2 = t2 + t2 -> 4 * t2	\$t3	11	0x00000000
				28: add \$a2, \$zero, \$t2 # z = t2	\$t4	12	0x00000000
					\$t5	13	0x00000000
					\$t6	14	0x00000000
					\$t7	15	0x00000000
					\$a0	16	0x00000003
					\$a1	17	0x00000004
					\$a2	18	0x0000004e
					\$a3	19	0x00000000
					\$a4	20	0x00000000
					\$a5	21	0x00000000
					\$a6	22	0x00000000
					\$a7	23	0x00000000
					\$t8	24	0x00000000
					\$t9	25	0x00000000
					\$k0	26	0x00000000
					\$k1	27	0x00000000
					\$gp	28	0x10008000
					\$sp	29	0x7ffffefc
					\$fp	30	0x00000000
					\$ra	31	0x00000000
					\$pc		0x00400050
					\$hi		0x00000000
					\$lo		0x00000000

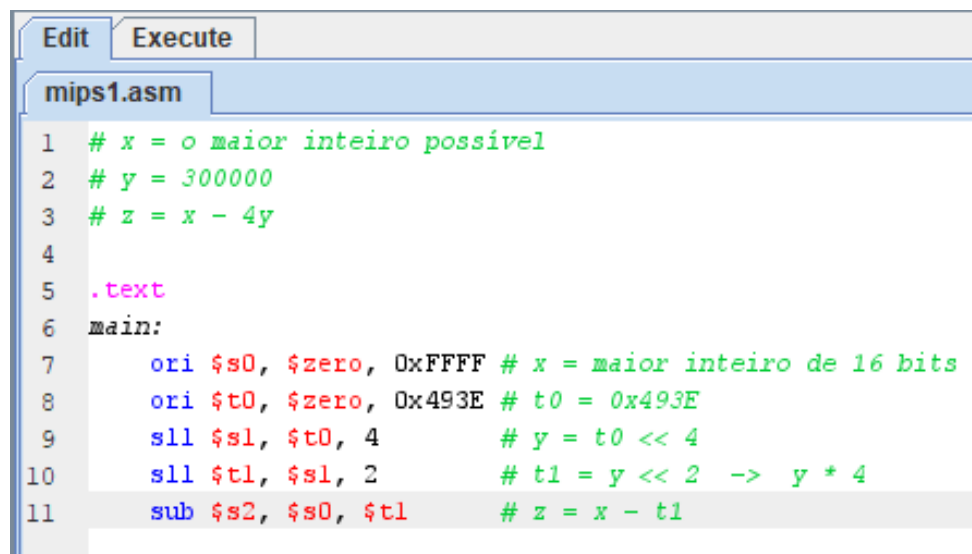
Programa 4:

Edit Execute	
mips1.asm	
1	<code>#x = 3</code>
2	<code>#y = 4</code>
3	<code>#z = ( 15*x + 67*y)*4</code>
4	
5	<code>.text</code>
6	<code>main:</code>
7	<code>ori \$s0, \$zero, 3 # x = 3</code>
8	<code>ori \$s1, \$zero, 4 # y = 4</code>
9	<code>sll \$t0, \$s0, 4 # t0 = x &lt;&lt; 4 -&gt; 16x</code>
10	<code>sub \$t0, \$t0, \$s0 # t0 = t0 - x -&gt; 15x</code>
11	
12	<code>sll \$t1, \$s1, 6 # t1 = y &lt;&lt; 6 -&gt; 64y</code>
13	<code>add \$t1, \$t1, \$s1 # t1 = t1 + y -&gt; 65y</code>
14	<code>add \$t1, \$t1, \$s1 # t1 = t1 + y -&gt; 66y</code>
15	<code>add \$t1, \$t1, \$s1 # t1 = t1 + y -&gt; 67y</code>
16	
17	<code>add \$t2, \$t0, \$t1 # t2 = t0 + t1</code>
18	<code>sll \$t2, \$t2, 2 # t2 = t2 &lt;&lt; 2 -&gt; 4 * t2</code>
19	<code>add \$s2, \$zero, \$t2 # z = t2</code>
20	





### Programa 6:



```
1  # x = o maior inteiro possível
2  # y = 300000
3  # z = x - 4y
4
5  .text
6  main:
7      ori $s0, $zero, 0xFFFF # x = maior inteiro de 16 bits
8      ori $t0, $zero, 0x493E # t0 = 0x493E
9      sll $s1, $t0, 4         # y = t0 << 4
10     sll $t1, $s1, 2          # t1 = y << 2 -> y * 4
11     sub $s2, $s0, $t1        # z = x - t1
```



EditExecute

mips1.asm

```

1  # Considere a seguinte instrução iniciando um programa:
2  # ori $8, $0, 0x01
3  #Usando apenas instruções reg-reg lógicas e/ou instruções de deslocamento (sll, srl e sra),
4  #continuar o programa de forma que ao final, tenhamos o seguinte conteúdo no registrador $8:
5  # $8 = 0xFFFFFFFF
6
7  .text
8  main:
9      ori $8, $0, 0x01
10     ori $8, $8, 0xFFFF
11     sll $8, $8, 16
12     ori $8, $8, 0xFFFF

```

EditExecute

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00400000	0x34080001	ori \$8,\$0,0x00000001	9: ori \$8, \$0, 0x01
	0x00400004	0x3508ffff	ori \$8,\$8,0x0000ffff	10: ori \$8, \$8, 0xFFFF
	0x00400008	0x00084400	sll \$8,\$8,0x00000010	11: sll \$8, \$8, 16
	0x0040000c	0x3508ffff	ori \$8,\$8,0x0000ffff	12: ori \$8, \$8, 0xFFFF

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Coproc 1	Coproc 0	Value
\$zero	0			0x00000000
\$at	1			0x00000000
\$v0	2			0x00000000
\$v1	3			0x00000000
\$a0	4			0x00000000
\$a1	5			0x00000000
\$a2	6			0x00000000
\$a3	7			0x00000000
\$t0	8			0xffffffff
\$t1	9			0x00000000
\$t2	10			0x00000000
\$t3	11			0x00000000
\$t4	12			0x00000000
\$t5	13			0x00000000
\$t6	14			0x00000000
\$t7	15			0x00000000
\$a0	16			0x00000000
\$a1	17			0x00000000
\$a2	18			0x00000000
\$a3	19			0x00000000
\$a4	20			0x00000000
\$a5	21			0x00000000
\$a6	22			0x00000000
\$a7	23			0x00000000
\$t8	24			0x00000000
\$t9	25			0x00000000
\$k0	26			0x00000000
\$k1	27			0x00000000
\$gp	28			0x10000000
\$sp	29			0x7ffffc00
\$fp	30			0x00000000
\$ra	31			0x00000000
\$pc				0x00400010
\$hi				0x00000000
\$lo				0x00000000

Programa 8:

EditExecute

mips1.asm

```

1  # Inicialmente escreva um programa que faça:
2  # $8 = 0x12345678.
3  # A partir do registrador $8 acima, usando apenas instruções lógicas (or, ori, and, andi,xor, xori)
4  # e instruções de deslocamento (sll, srl e sra), você deverá obter os seguintes
5  # valores nos respectivos registradores:
6  # $9 = 0x12
7  # $10 = 0x34
8  # $11 = 0x56
9  # $12 = 0x78
10
11 .text
12 main:
13     ori $8, $0, 0x1234 # $8 = 0x00001234
14     sll $8, $8, 16      # $8 = 0x12340000
15     ori $8, $8, 0x5678 # $8 = 0x12345678
16     sra $9, $8, 24      # $9 = $8 >> 24
17     sra $10, $8, 16     # $10 = $8 >> 16
18     andi $10, $10, 0xFF # $10 = and($10, 0xFF)
19     sra $11, $8, 8       # $11 = $8 >> 8
20     andi $11, $11, 0xFF # $11 = and($11, 0xFF)
21     or $12, $12, $8      # $12 = or($12, $8)
22     andi $12, $12, 0xFF # $12 = and($12, 0xFF)

```

EditExecute

Text Segment

Byte	Address	Code	Basic	Source
	0x00400000	0x34081234	ori \$8,\$0,0x00001234	13: ori \$8, \$0, 0x1234 # \$8 = 0x00001234
	0x00400004	0x00084400	sll \$8,\$8,16	14: sll \$8, \$8, 16 # \$8 = 0x12340000
	0x00400008	0x35085678	ori \$8,\$8,0x00005678	15: ori \$8, \$8, 0x5678 # \$8 = 0x12345678
	0x0040000c	0x00084e03	sra \$9,\$8,24	16: sra \$9, \$8, 24 # \$9 = \$8 >> 24
	0x00400010	0x00085403	sra \$10,\$8,16	17: sra \$10, \$8, 16 # \$10 = \$8 >> 16
	0x00400014	0x314e00ff	andi \$10,\$10,0x000000ff	18: andi \$10, \$10, 0xFF # \$10 = and(\$10, 0xFF)
	0x00400018	0x00085a03	sra \$11,\$8,8	19: sra \$11, \$8, 8 # \$11 = \$8 >> 8
	0x0040001c	0x314e00ff	andi \$11,\$11,0x000000ff	20: andi \$11, \$11, 0xFF # \$11 = and(\$11, 0xFF)
	0x00400020	0x01886025	or \$12,\$12,\$8	21: or \$12, \$12, \$8 # \$12 = or(\$12, \$8)
	0x00400024	0x318c00ff	andi \$12,\$12,0x000000ff	22: andi \$12, \$12, 0xFF # \$12 = and(\$12, 0xFF)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$a0	8	0x12345678
\$t1	9	0x00000012
\$t2	10	0x00000034
\$t3	11	0x00000056
\$t4	12	0x00000078
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$t0	16	0x00000000
\$t1	17	0x00000000
\$t2	18	0x00000000
\$t3	19	0x00000000
\$t4	20	0x00000000
\$t5	21	0x00000000
\$t6	22	0x00000000
\$t7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$t0	26	0x00000000
\$t1	27	0x00000000
\$fp	28	0x10008000
\$sp	29	0x7ffffc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$c		0x00400028
\$t1		0x00000000
\$t0		0x00000000

Programa 9:

```

Edit Execute
mips1.asm

1  # Considere a memória inicial da seguinte forma:
2  # .text
3  # .data
4  # x1: .word 15
5  # x2: .word 25
6  # x3: .word 13
7  # x4: .word 17
8  # soma: .word -1
9  # Escrever um programa que leia todos os números, calcule e substitua o
10 # valor da variável soma por este valor.
11
12 .text
13 main:
14     ori $t0, $t0, 0x1001
15     sll $t0, $t0, 16
16
17     lw $t1, 0($t0)
18     lw $t2, 4($t0)
19     lw $t3, 8($t0)
20     lw $t4, 12($t0)
21
22     add $t5, $t1, $t2
23     add $t5, $t5, $t3
24     add $t5, $t5, $t4
25
26     sw $t5, 16($t0)
27
28 .data
29     x1: .word 15
30     x2: .word 25
31     x3: .word 13
32     x4: .word 17
33     soma: .word -1

```

Edit Execute

Text Segment

Byte	Address	Code	Basic	Source
0x00400000	0x35081001	ori \$t0, \$t0, 0x1001	14:	ori \$t0, \$t0, 0x1001
0x00400004	0x00084400	sll \$t0, \$t0, 16	15:	sll \$t0, \$t0, 16
0x00400008	0x8d090000	lw \$t1, 0(\$t0)	17:	lw \$t1, 0(\$t0)
0x0040000c	0x8d0a0004	lw \$t2, 4(\$t0)	18:	lw \$t2, 4(\$t0)
0x00400010	0x8d0b0008	lw \$t3, 8(\$t0)	19:	lw \$t3, 8(\$t0)
0x00400014	0x8d0c000c	lw \$t4, 12(\$t0)	20:	lw \$t4, 12(\$t0)
0x00400018	0x01a68200	add \$t5, \$t1, \$t2	22:	add \$t5, \$t1, \$t2
0x0040001c	0x01a68200	add \$t5, \$t5, \$t3	23:	add \$t5, \$t5, \$t3
0x00400020	0x01a68200	add \$t5, \$t5, \$t4	24:	add \$t5, \$t5, \$t4
0x00400024	0xad0d0010	sw \$t5, 16(\$t0)	26:	sw \$t5, 16(\$t0)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x0000000f	0x00000019	0x0000000d	0x00000011	0x00000046	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x0000000f
\$t2	10	0x00000019
\$t3	11	0x0000000d
\$t4	12	0x00000011
\$t5	13	0x00000046
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$t0	26	0x00000000
\$t1	27	0x00000000
\$sp	28	0x10008000
\$fp	29	0x7ffffc00
\$ra	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400028
\$hi		0x00000000
\$lo		0x00000000

Programa 10:

Edit
Execute

mips1.asm\*

```

1  # Considere o seguinte programa:  $y = 127x - 65z + 1$ 
2  # Faça um programa que calcule o valor de y conhecendo os valores de x e z. Os valores de x e z
3  # estão armazenados na memória e, na posição imediatamente a seguir, o valor de y deverá ser
4  # escrito, ou seja:
5  # .data
6  # x: .word 5
7  # z: .word 7
8  # y: .word 0 # esse valor deverá ser sobrescrito após a execução do programa.
9
10 .text
11 main:
12     ori $t0, $zero, 0x1001 # acessando a primeira posicao da memoria
13     sll $t0, $t0, 16       # acessando a primeira posicao da memoria
14
15     lw $t1, 0($t0)         # $t1 = $t[0]
16     lw $t2, 4($t0)         # $t2 = $t[1]
17
18     sll $t3, $t1, 7        # $t3 = $t1 * 128
19     sub $t3, $t3, $t1      # $t3 = $t3 - $t1
20
21     sll $t4, $t2, 6        # $t4 = $t2 * 64
22     add $t4, $t4, $t2      # $t3 = $t4 + $t2
23
24     sub $t5, $t3, $t4      # $t5 = $t3 - $t4
25     addi $t5, $t5, 1       # $t5 = $t5 + 1
26     sw $t5, 8($t0)        # y = 127x - 65z + 1
27
28 .data
29     x: .word 5
30     z: .word 7
31     y: .word 0

```

Text Segment

Byte	Address	Code	Basic	Source
0x00400000	0x34081001	ori \$t0, \$zero, 0x00001001	12:	ori \$t0, \$zero, 0x1001 # acessando a primeira posicao da memoria
0x00400004	0x00084400	sll \$t0, \$t0, 16	13:	sll \$t0, \$t0, 16 # acessando a primeira posicao da memoria
0x00400008	0x8d090000	lw \$t1, 0(\$t0)	15:	lw \$t1, 0(\$t0) # \$t1 = \$t[0]
0x0040000c	0x8d0a0004	lw \$t2, 4(\$t0)	16:	lw \$t2, 4(\$t0) # \$t2 = \$t[1]
0x00400010	0x00095800	sll \$t3, \$t1, 7	18:	sll \$t3, \$t1, 7 # \$t3 = \$t1 * 128
0x00400014	0x01695222	sub \$t3, \$t3, \$t1	19:	sub \$t3, \$t3, \$t1 # \$t3 = \$t3 - \$t1
0x00400018	0x000a6180	sll \$t4, \$t2, 6	21:	sll \$t4, \$t2, 6 # \$t4 = \$t2 * 64
0x0040001c	0x019a6020	add \$t4, \$t4, \$t2	22:	add \$t4, \$t4, \$t2 # \$t3 = \$t4 + \$t2
0x00400020	0x016c6822	sub \$t5, \$t3, \$t4	24:	sub \$t5, \$t3, \$t4 # \$t5 = \$t3 - \$t4
0x00400024	0x21ad0001	addi \$t5, \$t5, 1	25:	addi \$t5, \$t5, 1 # \$t5 = \$t5 + 1
0x00400028	0xad0a0008	sw \$t5, 8(\$t0)	26:	sw \$t5, 8(\$t0) # y = 127x - 65z + 1

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000005	0x00000007	0x000000b5	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Coproc 1	Coproc 0	Value
\$zero		0	0x00000000
\$at		1	0x00000000
\$v0		2	0x00000000
\$v1		3	0x00000000
\$a0		4	0x00000000
\$a1		5	0x00000000
\$a2		6	0x00000000
\$a3		7	0x00000000
\$t0		8	0x10010000
\$t1		9	0x00000005
\$t2		10	0x00000007
\$t3		11	0x0000027b
\$t4		12	0x000001c7
\$t5		13	0x000000b5
\$t6		14	0x00000000
\$t7		15	0x00000000
\$a0		16	0x00000000
\$a1		17	0x00000000
\$a2		18	0x00000000
\$a3		19	0x00000000
\$s4		20	0x00000000
\$s5		21	0x00000000
\$s6		22	0x00000000
\$s7		23	0x00000000
\$t8		24	0x00000000
\$t9		25	0x00000000
\$t0		26	0x00000000
\$t1		27	0x00000000
\$gp		28	0x10008000
\$fp		29	0x7ffffcfc
\$fp		30	0x00000000
\$ra		31	0x00000000
\$c1			0x0040002c
\$t1			0x00000000
\$t0			0x00000000

Programa 11:

EditExecute

mips1.asm

```

1  # Considere o seguinte programa:  $y = x - z + 300000$ 
2  # Faça um programa que calcule o valor de y conhecendo os valores de x e z. Os valores de x e z
3  # estão armazenados na memória e, na posição imediatamente a seguir, o valor de y deverá ser
4  # escrito, ou seja:
5  # .data
6  # x: .word 100000
7  # z: .word 200000
8  # y: .word 0 # esse valor deverá ser sobrescrito após a execução do programa.
9
10 .text
11 main:
12     ori $t0, $0, 0x1001 # Acessandi a primeira posicao da memoria
13     sll $t0, $t0, 16    # Acessandi a primeira posicao da memoria
14
15     lw $t1, 0($t0)      # $t1 = $t0[0]
16     lw $t2, 4($t0)      # $t2 = $t0[1]
17
18     sub $t3, $t1, $t2   # $t3 = $t1 - $t2
19     ori $t4, $0, 0x493E # $t4 = 0x493E
20     sll $t4, $t4, 4     # $t4 = 300000
21     add $t5, $t3, $t4   # $t5 = $t3 + $t4
22
23     sw $t5, 8($t0)      # y = x - z + 300000
24
25 .data
26     x: .word 100000
27     z: .word 200000
28     y: .word 0

```

EditExecute

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00400000	0x34081001	ori \$t0, \$0, 0x00001001	12: ori \$t0, \$0, 0x1001 # Acessandi a primeira posicao da memoria
	0x00400004	0x00084400	sll \$t0, \$t0, 16	13: sll \$t0, \$t0, 16 # Acessandi a primeira posicao da memoria
	0x00400008	0x8d090000	lw \$t1, 0(\$t0)	15: lw \$t1, 0(\$t0) # \$t1 = \$t0[0]
	0x0040000c	0x8d0a0004	lw \$t2, 4(\$t0)	16: lw \$t2, 4(\$t0) # \$t2 = \$t0[1]
	0x00400010	0x01a5822	sub \$t3, \$t1, \$t2	18: sub \$t3, \$t1, \$t2 # \$t3 = \$t1 - \$t2
	0x00400014	0x340c493e	ori \$t4, \$0, 0x493E	19: ori \$t4, \$0, 0x493E # \$t4 = 0x493E
	0x00400018	0x000c6100	sll \$t4, \$t4, 4	20: sll \$t4, \$t4, 4 # \$t4 = 300000
	0x0040001c	0x016c6820	add \$t5, \$t3, \$t4	21: add \$t5, \$t3, \$t4 # \$t5 = \$t3 + \$t4
	0x00400020	0xad0d0008	sw \$t5, 8(\$t0)	23: sw \$t5, 8(\$t0) # y = x - z + 300000

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x000186a0	0x00030d40	0x00030d40	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x000186a0
\$t2	10	0x00030d40
\$t3	11	0xffff7960
\$t4	12	0x00493960
\$t5	13	0x00030d40
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$t0	26	0x00000000
\$t1	27	0x00000000
\$fp	28	0x10080000
\$sp	29	0x7ffffeffc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400024
\$hi		0x00000000
\$lo		0x00000000

## Programa 12:



Edit

Execute

mips1.asm

```

1  # Considere a seguinte situação:
2  # int ***x;
3  # onde x contém um ponteiro para um ponteiro para um ponteiro para um inteiro.
4  # Nessa situação, considere que a posição inicial de memória contenha o inteiro em questão.
5  # Coloque todos os outros valores em registradores, use os endereços de memória que quiser dentro
6  # do espaço de endereçamento do Mips.
7  # Resumo do problema:
8  # k = MEM [ MEM [ MEM [ x ] ] ].
9  # Crie um programa que implemente a estrutura de dados acima, leia o valor de K, o multiplique por
10 # 2 e o reescreva no local correto conhecendo-se apenas o valor de x.
11
12 .text
13 main:
14     ori $t0, $0, 0x1001 # Acessando a primeira posicao da memoria
15     sll $t0, $t0, 16    # Acessando a primeira posicao da memoria
16
17     lw $t1, 0($t0)      # $t1 = $t0[0]
18     lw $t2, 4($t0)      # $t2 = $t0[1]
19     lw $t3, 8($t0)      # $t3 = $t0[2]
20     lw $t4, 12($t0)     # $t4 = $t0[3]
21
22     sll $t5, $t4, 1     # $t5 = $t4 * 2  -> $t4 << 1
23     sw $t5, 0($t0)     # $t0[0] = $t5
24
25 .data
26     x: .word x1
27     x1: .word x2
28     x2: .word value
29     value: .word 15

```

Edit

Execute

Text Segment

Brpt	Address	Code	Basic	Source
	0x00400000	0x34081001	ori \$t0,\$0,0x0001001	14: ori \$t0, \$0, 0x1001 # Acessando a primeira posicao da memoria
	0x00400004	0x00084400	sll \$t0,\$t0,16	15: sll \$t0, \$t0, 16 # Acessando a primeira posicao da memoria
	0x00400008	0x8d090000	lw \$t1,0(\$t0)	17: lw \$t1, 0(\$t0) # \$t1 = \$t0[0]
	0x0040000c	0x8d0a0004	lw \$t2,4(\$t0)	18: lw \$t2, 4(\$t0) # \$t2 = \$t0[1]
	0x00400010	0x8d0b0008	lw \$t3,8(\$t0)	19: lw \$t3, 8(\$t0) # \$t3 = \$t0[2]
	0x00400014	0x8d0c000c	lw \$t4,12(\$t0)	20: lw \$t4, 12(\$t0) # \$t4 = \$t0[3]
	0x00400018	0x0008c840	sll \$t5,\$t4,1	22: sll \$t5, \$t4, 1 # \$t5 = \$t4 * 2 -> \$t4 << 1
	0x0040001c	0xad0d0000	sw \$t5,0(\$t0)	23: sw \$t5, 0(\$t0) # \$t0[0] = \$t5

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x0000001e	0x10010008	0x1001000c	0x0000000f	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Coproc 1

Coproc 0

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x10010004
\$t2	10	0x10010008
\$t3	11	0x1001000c
\$t4	12	0x0000000f
\$t5	13	0x0000001e
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10080000
\$sp	29	0x7ffffc00
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400020
\$hi		0x00000000
\$lo		0x00000000

Programa 13:

Edit

Execute

mips1.asm

mips1.asm

```

1  # Escreva um programa que leia um valor A da memória, identifique se o número é negativo ou
2  # não e encontre o seu módulo. O valor deverá ser reescrito sobre A.
3
4  .text
5  main:
6      ori $t0, $0, 0x1001
7      sll $t0, $t0, 16
8
9      lw $t1, 0($t0)
10     sra $t2, $t1, 31
11     beq $t2, $0, notNegative
12     sub $t1, $0, $t1
13
14     notNegative:
15         sw $t1, 0($t0)
16
17 .data
18     a: .word -7
19

```

Text Segment

Brkt	Address	Code	Basic	Source
	0x00400000	0x34081001	ori \$t0, \$0, 0x00001001	6: ori \$t0, \$0, 0x1001
	0x00400004	0x00094400	sll \$t0, \$t0, 16	7: sll \$t0, \$t0, 16
	0x00400008	0x8d090000	lw \$t1, 0(\$t0)	9: lw \$t1, 0(\$t0)
	0x0040000c	0x000957c3	sra \$t2, \$t1, 31	10: sra \$t2, \$t1, 31
	0x00400010	0x11400001	beq \$t2, \$0, notNegative	11: beq \$t2, \$0, notNegative
	0x00400014	0x00094822	sub \$t1, \$0, \$t1	12: sub \$t1, \$0, \$t1
	0x00400018	0xad090000	sw \$t1, 0(\$t0)	15: sw \$t1, 0(\$t0)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000007	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000007
\$t2	10	0xffffffff
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x00000000
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
pc		0x0040001c
hi		0x00000000
lo		0x00000000

0x10010000 (.data)

Hexadecimal Addresses

Hexadecimal Values

ASCII

More Machinecode

Run W/

Programa 14:

EditExecute

mips1.asm

```

1  # Escreva um programa que leia um valor A da memória, identifique se o número é par ou não.
2  # Um valor deverá ser escrito na segunda posição livre da memória (0 para par e 1 para ímpar).
3
4  .text
5  main:
6      ori $t0, $0, 0x1001
7      sll $t0, $t0, 16
8
9      lw $t1, 0($t0)
10     andi $t2, $t1, 1
11     beq $t2, $0, is_par
12     j fim
13
14     is_par:
15         ori $t2, $0, 0
16
17     fim:
18         sw $t2, 4($t0)
19
20     .data
21     a: .word 21
22

```

EditExecute

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00400000	0x34081001	ori \$t0, \$0, 0x1001	6: ori \$t0, \$0, 0x1001
	0x00400004	0x00084400	sll \$t0, \$t0, 16	7: sll \$t0, \$t0, 16
	0x00400008	0x8d090000	lw \$t1, 0(\$t0)	9: lw \$t1, 0(\$t0)
	0x0040000c	0x12a00011	andi \$t2, \$t1, 1	10: andi \$t2, \$t1, 1
	0x00400010	0x11400001	beq \$t2, \$0, is_par	11: beq \$t2, \$0, is_par
	0x00400014	0x08100007	j 0x0040001c	12: j fim
	0x00400018	0x340a0000	ori \$t2, \$0, 0	15: ori \$t2, \$0, 0
	0x0040001c	0xad0a0004	sw \$t2, 4(\$t0)	18: sw \$t2, 4(\$t0)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000015	0x00000001	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000015
\$t2	10	0x00000001
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7fffffc0
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400020
\$hi		0x00000000
\$lo		0x00000000

Programa 15:

```

1  # Escrever um programa que crie um vetor de 100 elementos na memória onde vetor[i] = 2*i + 1.
2  # Após a última posição do vetor criado, escrever a soma de todos os valores armazenados do vetor.
3  # Use o MARS para verificar a quantidade de instruções conforme o tipo (ULA, Desvios, Mem ou Outras)
4
5  .text
6  main:
7      ori $t0, $t0, 0x1001 # Acessando a primeira posicao da memoria
8      sll $t0, $t0, 16      # Acessando a primeira posicao da memoria
9
10     ori $t1, $t1, 100     # i = 100
11     do:                  # looping para soma
12         addi $t1, $t1, -1 # i = i - 1
13         add $t2, $t1, $t1 # t2 = i + i -> 2i
14         addi $t2, $t2, 1  # t2 = t2 + 1
15         sw $t2, 0($t0)    # t0[0] = t2
16         add $s0, $s0, $t2 # s0 = s0 + t0[i]
17         addi $t0, $t0, 4  # t0 = t0 + 4
18
19     bne $t1, $0, do       # while(i != 0)
20     sw $s0, 0($t0)        # colocando a soma na memoria

```

Text Segment

Bkpt	Address	Code	Basic	Source
0x00400000	0x39081001	ori \$t0, \$t0, 0x1001	7:	ori \$t0, \$t0, 0x1001 # Acessando a primeira posicao da memoria
0x00400004	0x00084001	sll \$t0, \$t0, 16	8:	sll \$t0, \$t0, 16 # Acessando a primeira posicao da memoria
0x00400008	0x35290064	ori \$t1, \$t1, 100	10:	ori \$t1, \$t1, 100 # i = 100
0x0040000c	0x129fffff	addi \$t1, \$t1, -1	12:	addi \$t1, \$t1, -1 # i = i - 1
0x00400010	0x01295020	add \$t2, \$t1, \$t1	13:	add \$t2, \$t1, \$t1 # t2 = i + i -> 2i
0x00400014	0x214a0001	addi \$t2, \$t2, 1	14:	addi \$t2, \$t2, 1 # t2 = t2 + 1
0x00400018	0xad0a0000	sw \$t2, 0(\$t0)	15:	sw \$t2, 0(\$t0) # t0[0] = t2
0x0040001c	0x020a8020	add \$s0, \$s0, \$t2	16:	add \$s0, \$s0, \$t2 # s0 = s0 + t0[i]
0x00400020	0x21080004	addi \$t0, \$t0, 4	17:	addi \$t0, \$t0, 4 # t0 = t0 + 4
0x00400024	0x1520ffff	bne \$t1, \$0, do	19:	bne \$t1, \$0, do # while(i != 0)
0x00400028	0xad100000	sw \$s0, 0(\$t0)	20:	sw \$s0, 0(\$t0) # colocando a soma na memoria

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x000000c7	0x000000c5	0x000000c3	0x000000c1	0x000000bf	0x000000bd	0x000000bb	0x000000b9
0x10010020	0x000000b7	0x000000b5	0x000000b3	0x000000b1	0x000000af	0x000000ad	0x000000ab	0x000000a9
0x10010040	0x000000a7	0x000000a5	0x000000a3	0x000000a1	0x0000009f	0x0000009d	0x0000009b	0x00000099
0x10010060	0x00000097	0x00000095	0x00000093	0x00000091	0x0000008f	0x0000008d	0x0000008b	0x00000089
0x10010080	0x00000087	0x00000085	0x00000083	0x00000081	0x0000007f	0x0000007d	0x0000007b	0x00000079
0x100100a0	0x00000077	0x00000075	0x00000073	0x00000071	0x0000006f	0x0000006d	0x0000006b	0x00000069
0x100100c0	0x00000067	0x00000065	0x00000063	0x00000061	0x0000005f	0x0000005d	0x0000005b	0x00000059
0x100100e0	0x00000057	0x00000055	0x00000053	0x00000051	0x0000004f	0x0000004d	0x0000004b	0x00000049
0x10010100	0x00000047	0x00000045	0x00000043	0x00000041	0x0000003f	0x0000003d	0x0000003b	0x00000039
0x10010120	0x00000037	0x00000035	0x00000033	0x00000031	0x0000002f	0x0000002d	0x0000002b	0x00000029
0x10010140	0x00000027	0x00000025	0x00000023	0x00000021	0x0000001f	0x0000001d	0x0000001b	0x00000019

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010190
\$t1	9	0x00000000
\$t2	10	0x00000001
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$s0	16	0x000002710
\$s1	17	0x00000000
\$s2	18	0x00000000
\$s3	19	0x00000000
\$s4	20	0x00000000
\$s5	21	0x00000000
\$s6	22	0x00000000
\$s7	23	0x00000000
\$s8	24	0x00000000
\$s9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$fp	28	0x10080000
\$sp	29	0x7ffffeffc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400028
\$hi		0x00000000
\$lo		0x00000000

Instruction Statistics, Version 1.0 (Ingo Kofler)

Total: 704

ALU: 503

72%

Jump: 0

0%

Branch: 100

14%

Memory: 101

14%

Other: 0

0%

Tool Control

Disconnect from MIPS

Reset

Close

Programa 16:

EditExecute

mips1.asm\*

```

1  # Escreva um programa que avalie a expressão: (x*y)/z.
2  # Use x = 1600000 (=0x186A00), y = 80000 (=0x13880), e z = 400000 (=0x61A80). Inicializar os
3  # registradores com os valores acima.
4
5  .text
6  main:
7      ori $t0, $t0, 0x1001
8      sll $t0, $t0, 16
9
10     lw $t1, 0($t0)
11     lw $t2, 4($t0)
12     lw $t3, 8($t0)
13
14     div $t1, $t2
15     mflo $t4
16     mult $t4, $t3
17     mflo $t4
18
19     sw $t4, 12($t0)
20
21     .data
22     x: .word 0x186A00
23     y: .word 0x13880
24     z: .word 0x61A80

```

EditExecute

Text Segment

Dispt	Address	Code	Basic	Source
<input type="checkbox"/>	0x00400000	0x35081001	ori \$t0, \$t0, 0x0001001	7: ori \$t0, \$t0, 0x1001
<input type="checkbox"/>	0x00400004	0x00084400	sll \$t0, \$t0, 16	8: sll \$t0, \$t0, 16
<input type="checkbox"/>	0x00400008	0x8d090000	lw \$t1, 0(\$t0)	10: lw \$t1, 0(\$t0)
<input type="checkbox"/>	0x0040000c	0x8d0a0004	lw \$t2, 4(\$t0)	11: lw \$t2, 4(\$t0)
<input type="checkbox"/>	0x00400010	0x8d0b0008	lw \$t3, 8(\$t0)	12: lw \$t3, 8(\$t0)
<input type="checkbox"/>	0x00400014	0x01a001a	div \$t1, \$t2	14: div \$t1, \$t2
<input type="checkbox"/>	0x00400018	0x00006012	mflo \$t4	15: mflo \$t4
<input type="checkbox"/>	0x0040001c	0x018b0018	mult \$t4, \$t3	16: mult \$t4, \$t3
<input type="checkbox"/>	0x00400020	0x00006012	mflo \$t4	17: mflo \$t4
<input type="checkbox"/>	0x00400024	0xad0c000c	sw \$t4, 12(\$t0)	19: sw \$t4, 12(\$t0)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00186A00	0x00013880	0x00061A80	0x007A1200	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$a0	8	0x10010000
\$t1	9	0x01186A00
\$t2	10	0x00013880
\$t3	11	0x00061A80
\$t4	12	0x007A1200
\$t5	13	0x00000000
\$t6	14	0x00000000
\$t7	15	0x00000000
\$a0	16	0x00000000
\$a1	17	0x00000000
\$a2	18	0x00000000
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000000
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$t0	26	0x00000000
\$t1	27	0x00000000
\$gp	28	0x10008000
\$sp	29	0x7ffffcfc
\$fp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400028
\$t1		0x00000000
\$t0		0x007A1200

Programa 17:

EditExecute

mips1.asm

```

1  # Para a expressão a seguir, escreva um programa que calcule o valor de k:
2  # k = x * y (Você deverá realizar a multiplicação através de somas!)
3  # O valor de x deve ser lido da primeira posição livre da memória e o valor de y deverá lido da
4  # segunda posição livre. O valor de k, após calculado, deverá ainda ser escrito na terceira posição
5  # livre da memória.
6
7  .text
8  main:
9      ori $t0, $t0, 0x1001 # Acessando a primeira posicao da memoria
10     sll $t0, $t0, 16      # Acessando a primeira posicao da memoria
11
12     lw $t1, 0($t0)        # $t1 = $t0[0]
13     lw $t2, 4($t0)        # $t2 = $t0[1]
14
15     calcular:             # looping para calcular
16         add $t3, $t3, $t1  # $t3 = $t3 + $t1
17         addi $t2, $t2, -1   # $t2 = $t2 - 1
18         bne $t2, $0, calcular # if($t2 != $0){goto calcular}
19         sw $t3, 8($t0)      # $t0[3] = $t3
20
21     .data
22     x: .word 10
23     y: .word 5
24     k: .word -1

```

EditExecute

Text Segment

Addr	Address	Code	Basic	Source
0x00400000	0x00400000	ori \$t0, \$t0, 0x1001	9:	ori \$t0, \$t0, 0x1001 # Acessando a primeira posicao da memoria
0x00400004	0x00400004	sll \$t0, \$t0, 16	10:	sll \$t0, \$t0, 16 # Acessando a primeira posicao da memoria
0x00400008	0x00400008	lw \$t1, 0(\$t0)	12:	lw \$t1, 0(\$t0) # \$t1 = \$t0[0]
0x0040000c	0x0040000c	lw \$t2, 4(\$t0)	13:	lw \$t2, 4(\$t0) # \$t2 = \$t0[1]
0x00400010	0x00400010	add \$t3, \$t3, \$t1	16:	add \$t3, \$t3, \$t1 # \$t3 = \$t3 + \$t1
0x00400014	0x00400014	addi \$t2, \$t2, -1	17:	addi \$t2, \$t2, -1 # \$t2 = \$t2 - 1
0x00400018	0x00400018	bne \$t2, \$0, calcular	18:	bne \$t2, \$0, calcular # if(\$t2 != \$0){goto calcular}
0x0040001c	0x0040001c	sw \$t3, 8(\$t0)	19:	sw \$t3, 8(\$t0) # \$t0[3] = \$t3

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	10	5	50	0	0	0	0	0
0x10010020	0	0	0	0	0	0	0	0
0x10010040	0	0	0	0	0	0	0	0
0x10010060	0	0	0	0	0	0	0	0
0x10010080	0	0	0	0	0	0	0	0
0x100100a0	0	0	0	0	0	0	0	0
0x100100c0	0	0	0	0	0	0	0	0
0x100100e0	0	0	0	0	0	0	0	0
0x10010100	0	0	0	0	0	0	0	0
0x10010120	0	0	0	0	0	0	0	0
0x10010140	0	0	0	0	0	0	0	0

Registers

Name	Coproc 1	Coproc 0	Value
\$zero	0	0	0
\$at	1	0	0
\$v0	2	0	0
\$v1	3	0	0
\$a0	4	0	0
\$a1	5	0	0
\$a2	6	0	0
\$a3	7	0	0
\$t0	8	268500992	0
\$t1	9	10	0
\$t2	10	5	0
\$t3	11	50	0
\$t4	12	0	0
\$t5	13	0	0
\$t6	14	0	0
\$t7	15	0	0
\$s0	16	0	0
\$s1	17	0	0
\$s2	18	0	0
\$s3	19	0	0
\$s4	20	0	0
\$s5	21	0	0
\$s6	22	0	0
\$s7	23	0	0
\$s8	24	0	0
\$s9	25	0	0
\$k0	26	0	0
\$k1	27	0	0
\$gp	28	268468224	0
\$sp	29	2147479544	0
\$fp	30	0	0
\$ra	31	0	0
\$pc		4194336	0
\$hi		0	0
\$lo		0	0

## Programa 18:

```

Edit Execute
mips1.asm
7  # Dê um valor para x e y (dê valores pequenos !!) e use o MARS para verificar a quantidade de
8  # instruções conforme o tipo (ULA, Desvios, Mem ou Outras)
9
10 .text
11 main:
12     ori $t0, $0, 0x1001
13     sll $t0, $t0, 16
14
15     lw $s0, 0($t0)
16     lw $s1, 4($t0)
17
18     ori $t3, $0, 1
19     ori $t1, $0, 0
20
21 pow:
22     beq $t1, $s1, end
23     addi $t1, $t1, 1
24
25     ori $t2, $0, 0
26     ori $t4, $0, 0
27
28 soma:
29     beq $t2, $t3, pow_next
30     add $t4, $t4, $s0
31     addi $t2, $t2, 1
32     j soma
33
34 pow_next:
35     or $t3, $t4, $zero
36     j pow
37
38 end:
39     sw $t3, 8($t0)

```

Registers

Name	Number	Value
\$zero	0	0x00000000
\$t0	1	0x00000000
\$t1	2	0x00000000
\$t2	3	0x00000000
\$t3	4	0x00000000
\$t4	5	0x00000000
\$t5	6	0x00000000
\$t6	7	0x00000000
\$t7	8	0x00000000
\$t8	9	0x00000000
\$t9	10	0x00000000
\$t10	11	0x00000000
\$t11	12	0x00000000
\$t12	13	0x00000000
\$t13	14	0x00000000
\$t14	15	0x00000000
\$t15	16	0x00000000
\$t16	17	0x00000000
\$t17	18	0x00000000
\$t18	19	0x00000000
\$t19	20	0x00000000
\$t20	21	0x00000000
\$t21	22	0x00000000
\$t22	23	0x00000000
\$t23	24	0x00000000
\$t24	25	0x00000000
\$t25	26	0x00000000
\$t26	27	0x00000000
\$t27	28	0x00000000
\$t28	29	0x00000000
\$t29	30	0x00000000
\$t30	31	0x00000000
\$t31	32	0x00000000
\$t32	33	0x00000000
\$t33	34	0x00000000
\$t34	35	0x00000000
\$t35	36	0x00000000
\$t36	37	0x00000000
\$t37	38	0x00000000
\$t38	39	0x00000000
\$t39	40	0x00000000
\$t40	41	0x00000000
\$t41	42	0x00000000
\$t42	43	0x00000000
\$t43	44	0x00000000
\$t44	45	0x00000000
\$t45	46	0x00000000
\$t46	47	0x00000000
\$t47	48	0x00000000
\$t48	49	0x00000000
\$t49	50	0x00000000
\$t50	51	0x00000000
\$t51	52	0x00000000
\$t52	53	0x00000000
\$t53	54	0x00000000
\$t54	55	0x00000000
\$t55	56	0x00000000
\$t56	57	0x00000000
\$t57	58	0x00000000
\$t58	59	0x00000000
\$t59	60	0x00000000
\$t60	61	0x00000000
\$t61	62	0x00000000
\$t62	63	0x00000000
\$t63	64	0x00000000
\$t64	65	0x00000000
\$t65	66	0x00000000
\$t66	67	0x00000000
\$t67	68	0x00000000
\$t68	69	0x00000000
\$t69	70	0x00000000
\$t70	71	0x00000000
\$t71	72	0x00000000
\$t72	73	0x00000000
\$t73	74	0x00000000
\$t74	75	0x00000000
\$t75	76	0x00000000
\$t76	77	0x00000000
\$t77	78	0x00000000
\$t78	79	0x00000000
\$t79	80	0x00000000
\$t80	81	0x00000000
\$t81	82	0x00000000
\$t82	83	0x00000000
\$t83	84	0x00000000
\$t84	85	0x00000000
\$t85	86	0x00000000
\$t86	87	0x00000000
\$t87	88	0x00000000
\$t88	89	0x00000000
\$t89	90	0x00000000
\$t90	91	0x00000000
\$t91	92	0x00000000
\$t92	93	0x00000000
\$t93	94	0x00000000
\$t94	95	0x00000000
\$t95	96	0x00000000
\$t96	97	0x00000000
\$t97	98	0x00000000
\$t98	99	0x00000000
\$t99	100	0x00000000

Text Segment

Bkpt	Address	Code	Basic	Source
	0x00400010	0x340b0001	ori \$t1,\$0,0x00000001	18: ori \$t3, \$0, 1
	0x00400014	0x34090000	ori \$t2,\$0,0x00000000	19: ori \$t1, \$0, 0
	0x00400018	0x13100009	beq \$t2,\$t1,0x00000009	22: beq \$t1, \$t1, end
	0x0040001c	0x21390001	addi \$t1,\$t1,1	23: addi \$t1, \$t1, 1
	0x00400020	0x340a0000	ori \$t2,\$0,0x00000000	25: ori \$t2, \$0, 0
	0x00400024	0x340c0000	ori \$t4,\$0,0x00000000	26: ori \$t4, \$0, 0
	0x00400028	0x114b0003	beq \$t2,\$t3,0x00000003	29: beq \$t2, \$t3, pow_next
	0x0040002c	0x019c0020	addi \$t2,\$t2,16	30: addi \$t2, \$t2, 16
	0x00400030	0x214a0001	addi \$t2,\$t2,1	31: addi \$t2, \$t2, 1
	0x00400034	0x0810000a	j 0x00400028	32: j soma
	0x00400038	0x01808025	or \$t3,\$t4,0x00000025	35: or \$t3, \$t4, \$zero
	0x0040003c	0x08100004	j 0x0040001b	36: j pow
	0x00400040	0xad3b0008	sw \$t3,0x00000008(\$t0)	39: sw \$t3, 8(\$t0)

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Instruction Statistics, Version 1.0 (Ingo Kofler)

Total: 57

ALU: 30

53%

Jump: 10

18%

Branch: 14

25%

Memory: 3

5%

Other: 0

0%

Tool Control

Disconnect from MIPS

Reset

Close

**1. Se tivermos 2 inteiros, cada um com 32 bits, quantos bits podemos esperar para o produto?**

- A. 16
- B. 32
- C. 64
- D. 128

**2. Quais os registradores que armazenam os resultados na multiplicação?**

- A. high e low
- B. hi e lo
- C. R0 e R1
- D. \$0 e \$1

**3. Qual a operação usada para multiplicar inteiros em comp. de dois?**

- A. mult
- B. multu
- C. multi
- D. Mutt

**4. Qual instrução move os bits menos significativos da multiplicação para o reg. 8?**

- A. move \$8,lo
- B. mvlo \$8,lo
- C. mflo \$8
- D. addu \$8,\$0,lo

**5. Se tivermos dois inteiros, cada um com 32 bits, quantos bits deveremos estar preparados para receber no quociente?**

- A. 16
- B. 32



- C. 64
- D. 128

**6. Após a instrução div, qual registrador possui o quociente?**

- A. lo
- B. hi
- C. high
- D. \$2

**7. Qual a inst. Usada para dividir dois inteiros em comp. de dois?**

- A. dv
- B. divide
- C. divu
- D. div

**8. Faça um arithmetic shift right de dois no seguinte padrão de bits: 1001 1011**

- A. 1110 0110
- B. 0010 0110
- C. 1100 1101
- D. 0011 0111

**9. Qual o efeito de um arithmetic shift right de uma posição?**

- A. Se o inteiro for unsigned, o shift o divide por 2. Se o inteiro for signed, o shift o divide por 2.
- B. Se o inteiro for unsigned, o shift o divide por 2. Se o inteiro for signed, o shift pode resultar em um valor errado.
- C. Se o inteiro for unsigned, o shift pode ocasionar um valor errado. Se o inteiro for signed, o shift o divide por 2.
- D. O shift multiplica o número por dois.

**10. Qual sequencia de instruções avalia  $3x+7$ , onde  $x$  é iniciado no reg. \$8 e o resultado armazenado em \$9?**

A.

ori \$3,\$0,3  
mult \$8,\$3  
mflo \$9  
addi \$9,\$9,7

B.

ori \$3,\$0,3  
mult \$8,\$3  
addi \$9,\$8,7

C.

ori \$3,\$0,3  
mult \$8,\$3  
mfhi \$9  
addi \$9,\$9,7

D.

mult \$8,3  
mflo \$9  
addi \$9,\$9,7

## **Programa 19:**

EditExecute

mips1.asm\*

```

1  # Escrever um programa que leia dois números da memória, a primeira e segunda posições
2  # respectivamente (os coloque em $s0 e $s1) e determine a quantidade de bits significantes de cada
3  # um. Coloque as respostas em $t0 e $t1, a partir desse resultado faça a multiplicação. Caso o número
4  # de bits significantes de ambos seja menor do que 32 a resposta deverá estar apenas em $s2, caso
5  # contrário a resposta estará em $s2 e $s3 (LO e HI respectivamente).
6
7  .text
8  main:
9      ori $t0, $t0, 0x1001 # Acessando a primeira poicao da memoria
10     sll $t0, $t0, 16      # Acessando a primeira poicao da memoria
11
12     lw $s0, 0($t0)        # $s0 = x
13     lw $s1, 4($t0)        # $s1 = y
14     ori $t1, $t1, 0       # contador de bits de x
15     ori $t2, $t2, 0       # contador de bits de y
16     or $t3, $0, $s0       # $t3 = x
17
18     countX:
19         beq $t3, $0, countY # if($t3 == 0){goto countY}
20         srl $t3, $t3, 1     # $t3 = $t3 >> 1
21         addi $t1, $t1, 1    # $t1 = $t1 + 1
22         j countX           # goto countX
23
24     countY:
25         or $t3, $0, $s1    # $t3 = y
26
27     do:
28         beq $t3, $0, multi # if($t3 == 0){goto multi}
29         srl $t3, $t3, 1    # $t2 = $t2 >> 1
30         addi $t2, $t2, 1    # $t2 = $t2 + 1
31         j do              # goto do
32
33     multi:
34         mult $t1, $t2      # x * y
35         mflo $s2           # $s2 = LOW
36         mfhi $s3           # $s3 = HIGH
37
38         slti $t5, $t1, 32  # if($t1 < 32){$t5 = 1}
39         slti $t6, $t2, 32  # if($t2 < 32){$t6 = 1}
40         and $t7, $t5, $t6  # $t7 = 1
41
42         beq $t7, $0, save  # if($t7 == 0){goto save}
43         sw $s2, 8($t0)     # else if($t7 == 1){t0[3] = $s2}
44         j end             # goto end
45
46     save:
47         sw $s2, 8($t0)     # t0[3] = $s2
48         sw $s3, 12($t0)   # t0[4] = $s3
49
50     end:
51         nop               # Null operation
52
53     .data
54     x: .word 4
55     y: .word 3

```

EditExecute

Registers Coproc 1 Coproc 0

Name	Number	Value
\$zero	0	0x00000000
\$at	1	0x00000000
\$v0	2	0x00000000
\$v1	3	0x00000000
\$a0	4	0x00000000
\$a1	5	0x00000000
\$a2	6	0x00000000
\$a3	7	0x00000000
\$t0	8	0x10010000
\$t1	9	0x00000003
\$t2	10	0x00000002
\$t3	11	0x00000000
\$t4	12	0x00000000
\$t5	13	0x00000001
\$t6	14	0x00000001
\$t7	15	0x00000001
\$a0	16	0x00000004
\$a1	17	0x00000003
\$a2	18	0x00000006
\$a3	19	0x00000000
\$a4	20	0x00000000
\$a5	21	0x00000000
\$a6	22	0x00000006
\$a7	23	0x00000000
\$t8	24	0x00000000
\$t9	25	0x00000000
\$k0	26	0x00000000
\$k1	27	0x00000000
\$sp	28	0x10000000
\$fp	29	0x7ffffc00
\$gp	30	0x00000000
\$ra	31	0x00000000
\$pc		0x00400070
\$hi		0x00000000
\$lo		0x00000006

Text Segment

Offset	Address	Code	Basic	Source
0x00400000	0x35081001	ori \$t0, \$t0, 0x1001	9:	ori \$t0, \$t0, 0x1001 # Acessando a primeira poicao da memoria
0x00400004	0x00084400	sll \$t0, \$t0, 16	10:	sll \$t0, \$t0, 16 # Acessando a primeira poicao da memoria
0x00400008	0x8d100000	lw \$s0, 0(\$t0)	12:	lw \$s0, 0(\$t0) # \$s0 = x
0x0040000c	0x8d110004	lw \$s1, 4(\$t0)	13:	lw \$s1, 4(\$t0) # \$s1 = y
0x00400010	0x35290000	ori \$t1, \$t1, 0	14:	ori \$t1, \$t1, 0 # contador de bits de x
0x00400014	0x354a0000	ori \$t2, \$t2, 0	15:	ori \$t2, \$t2, 0 # contador de bits de y
0x00400018	0x00105825	or \$t3, \$0, \$s0	16:	or \$t3, \$0, \$s0 # \$t3 = x
0x0040001c	0x11600003	beq \$t3, \$0, countY	19:	beq \$t3, \$0, countY # if(\$t3 == 0){goto countY}
0x00400020	0x000b5842	srl \$t3, \$t3, 1	20:	srl \$t3, \$t3, 1 # \$t3 = \$t3 >> 1
0x00400024	0x21290001	addi \$t1, \$t1, 1	21:	addi \$t1, \$t1, 1 # \$t1 = \$t1 + 1
0x00400028	0x09100007	j countX	22:	j countX # goto countX
0x0040002c	0x00115825	or \$t3, \$0, \$s1	25:	or \$t3, \$0, \$s1 # \$t3 = y
0x00400030	0x11600003	beq \$t3, \$0, multi	28:	beq \$t3, \$0, multi # if(\$t3 == 0){goto multi}

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000004	0x00000003	0x00000006	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

0x10010000 (.data)

☒ Hexadecimal Addresses
☒ Hexadecimal Values
☐ ASCII

## Programa 20:

```

Edit Execute
mips1.asm
1  # Os valores de x devem ser lidos da primeira posição livre da memória e o
2  # valor de y deverá ser escrito na segunda posição livre.
3
4  .text
5  main:
6      ori $t0, $t0, 0x1001 # Acessando a primeira posicao da memoria
7      sll $t0, $t0, 16     # Acessando a primeira posicao da memoria
8
9      lw $s0, 0($t0)       # x = t0[0]
10     and $t1, $s0, 1      # Verificar se é impar ou par
11
12     mult $s0, $s0
13     mflo $s1             # x^2
14
15     mult $s1, $s0
16     mflo $s2             # x^3
17
18     mult $s1, $s1
19     mflo $s3             # x^4
20
21     mult $s3, $s0
22     mflo $s4             # x^5
23     bne $t1, $0, impar   # if(x != 0){goto impar}
24
25     par:                 # x^4 + x^3 - 2x^2
26         add $t2, $s3, $s2
27         add $t3, $s1, $s1
28         sub $t4, $t2, $t3
29         sw $t4, 4($t0)
30         j end
31
32     impar:               # x^5 - x^3 + 1
33         sub $t5, $s4, $s2
34         addi $t6, $t5, 1
35         sw $t6, 4($t0)
36
37     end:
38         nop
39
40     .data
41     x: .word 5

```

Text Segment	Address	Code	Basic	Source
	0x00400024	0x00009812	mflo \$t0	19: mflo \$s3 # x^4
	0x00400026	0x00270001	mult \$t0, \$t0	21: mult \$s3, \$s0
	0x00400028	0x0000a012	mflo \$t0	22: mflo \$s4 # x^5
	0x00400030	0x15200005	bne \$t0, \$0, 0x00000005	23: bne \$t1, \$0, impar # if(x != 0){goto impar}
	0x00400034	0x02725020	add \$t0, \$t0, \$t0	26: add \$t2, \$s3, \$s2
	0x00400038	0x02315820	add \$t1, \$t1, \$t1	27: add \$t3, \$s1, \$s1
	0x0040003c	0x014b6022	sub \$t2, \$t2, \$t3	28: sub \$t4, \$t2, \$t3
	0x00400040	0xad0e0004	sw \$t2, 0x00000004(\$t0)	29: sw \$t4, 4(\$t0)
	0x00400044	0x01000115	j 0x00400054	30: j end
	0x00400048	0x02926822	sub \$t3, \$t3, \$t2	33: sub \$t5, \$s4, \$s2
	0x0040004c	0x21ae0001	addi \$t4, \$t3, 0x0000...	34: addi \$t6, \$t5, 1
	0x00400050	0xad0e0004	sw \$t4, 0x00000004(\$t0)	35: sw \$t6, 4(\$t0)
	0x00400054	0x00000000	nop	38: nop

Data Segment	Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
	0x10010000	0x00000005	0x000000b9	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
	0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Registers	Coproc 1	Coproc 0	Name	Number	Value
			\$zero	0	0x00000000
			\$at	1	0x00000000
			\$v0	2	0x00000000
			\$v1	3	0x00000000
			\$a0	4	0x00000000
			\$a1	5	0x00000000
			\$a2	6	0x00000000
			\$a3	7	0x00000000
			\$t0	8	0x10010000
			\$t1	9	0x00000001
			\$t2	10	0x00000000
			\$t3	11	0x00000000
			\$t4	12	0x00000000
			\$t5	13	0x000000b9
			\$t6	14	0x000000b9
			\$t7	15	0x00000000
			\$a0	16	0x00000000
			\$a1	17	0x00000019
			\$a2	18	0x0000007d
			\$a3	19	0x00000271
			\$a4	20	0x000000c3
			\$a5	21	0x00000000
			\$a6	22	0x00000000
			\$a7	23	0x00000000
			\$t8	24	0x00000000
			\$t9	25	0x00000000
			\$t0	26	0x00000000
			\$t1	27	0x00000000
			\$t2	28	0x10000000
			\$t3	29	0x7fffffc0
			\$t4	30	0x00000000
			\$t5	31	0x00000000
			\$pc		0x00400058
			\$hi		0x00000000
			\$lo		0x000000c3

## Programa 21:

