



Basic gates




NOT-gate
Inverting input
out 1




AND-gate
Parents both
out 1


Compound gates




handout-1-
to-2-
mux.syn
1 to 2 multiplexer from handout




or-
gate.syn
OR gate as
specified in the
handout



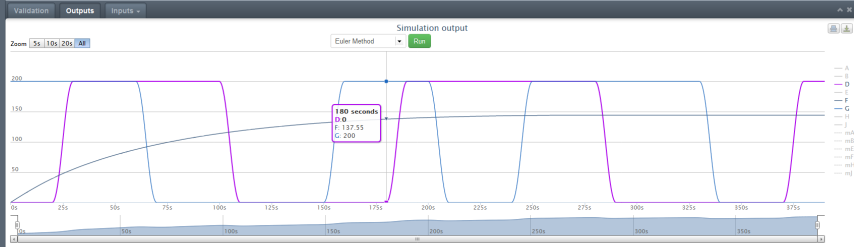
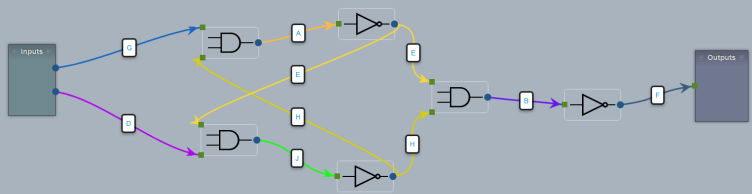
not.syn
Simple not gate
with 700 L, 700 H
File 1



handout-
xor.syn
XOR gate from
the handout



handout-
sr-
latch.syn
SR latch from the handout







Zelula's key features

- ▶ Intuitive interface:
 - ▶ Drag & Drop gates;
 - ▶ Recognizable icons;
 - ▶ Colored proteins;
- ▶ Compound gates;
- ▶ Choose from 10 different solvers;
- ▶ Exportable to SBML;
- ▶ Platform independent:
 - ▶ Client is in the browser;
 - ▶ Works on tablets/phones.

