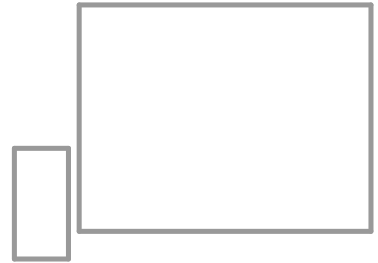


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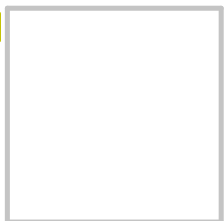


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C4

C9



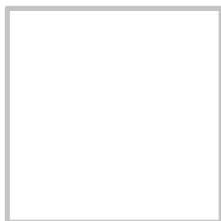
J2 L1



D2

C2

C1



J1

U1

U1

R1C6



R5

R3

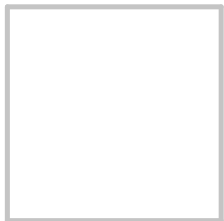
C7

U2



R4C5

C8



C3

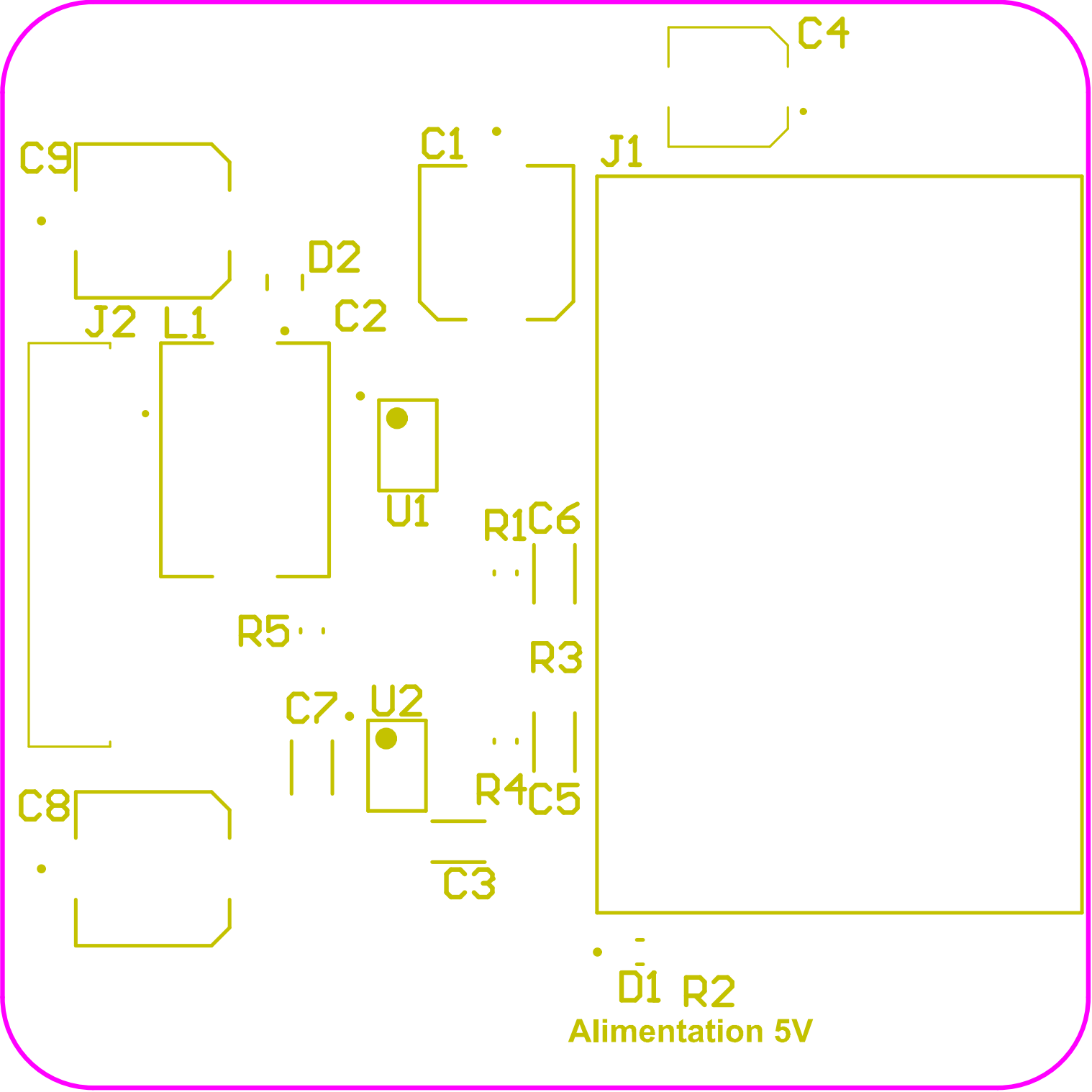


D1 R2



REV1

SER1



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REV1

PCB REV #

2

SER1

SERIAL #

