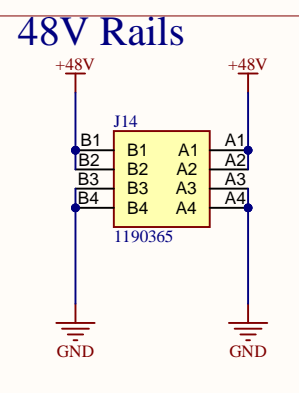
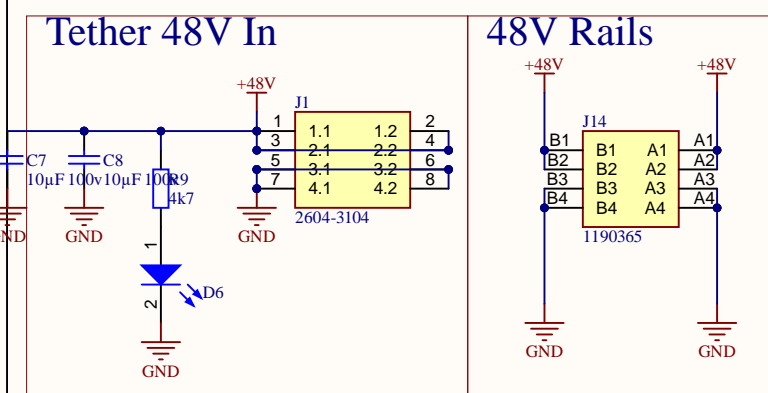
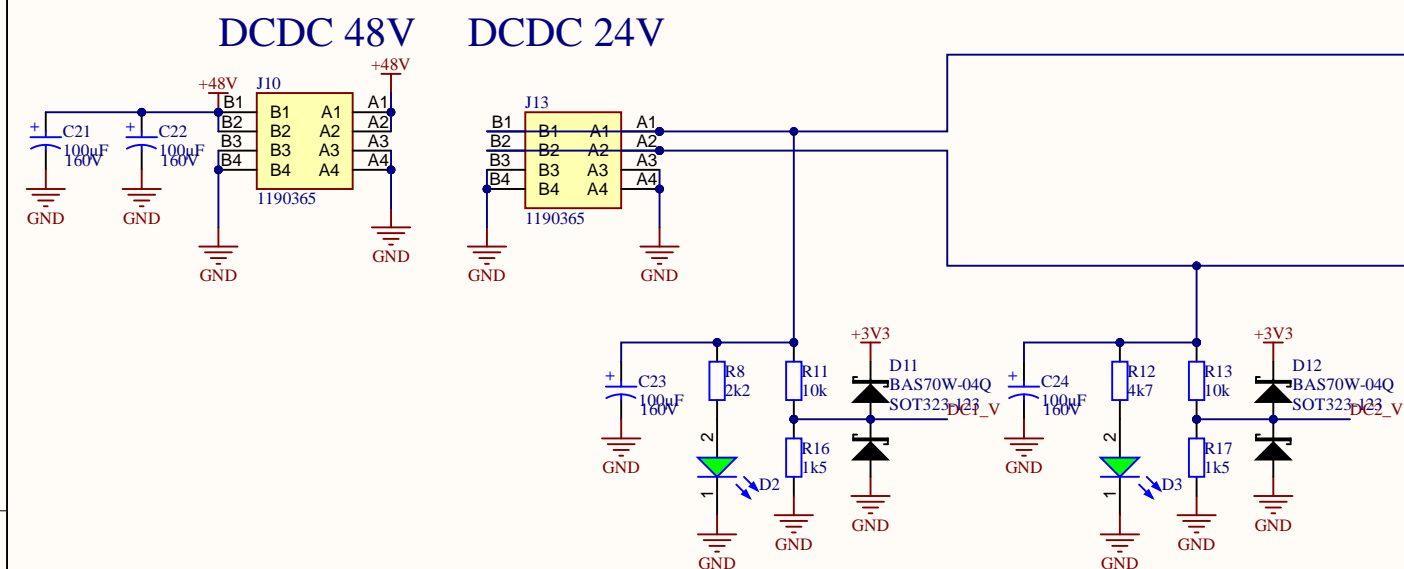
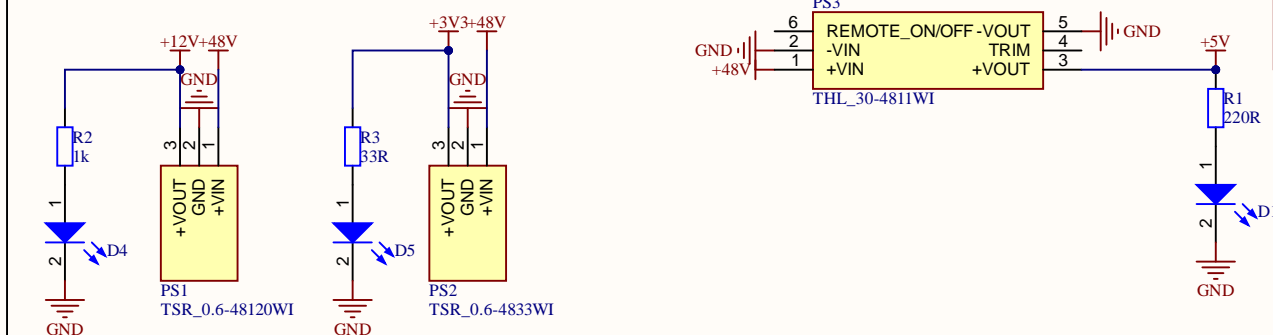
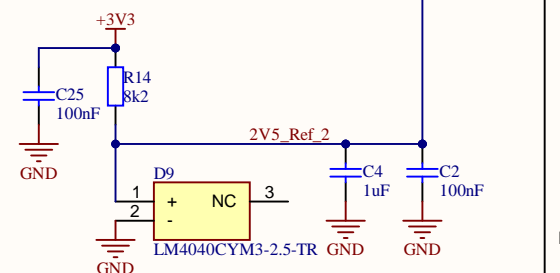
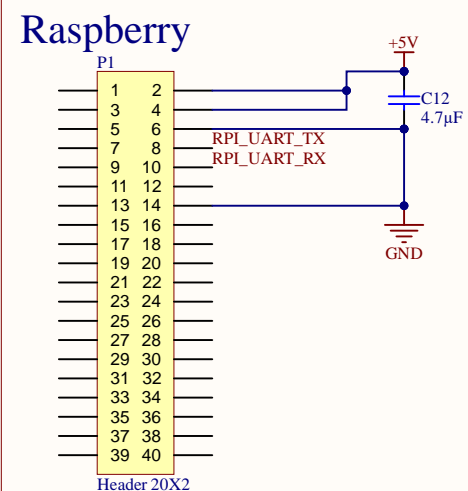
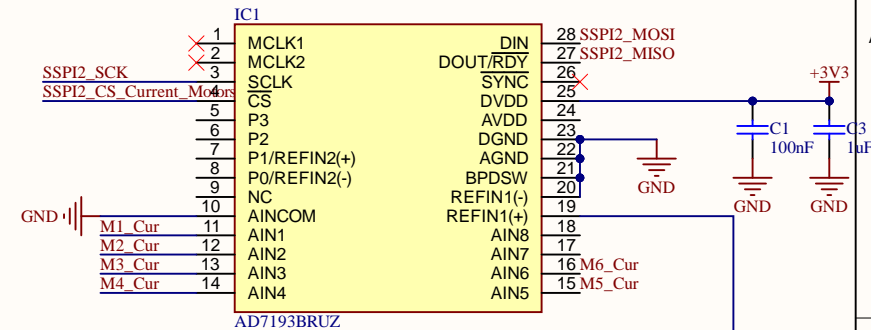
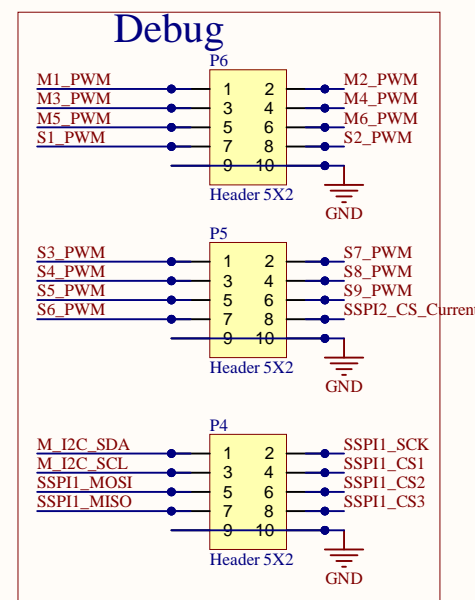
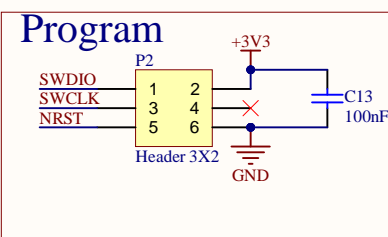
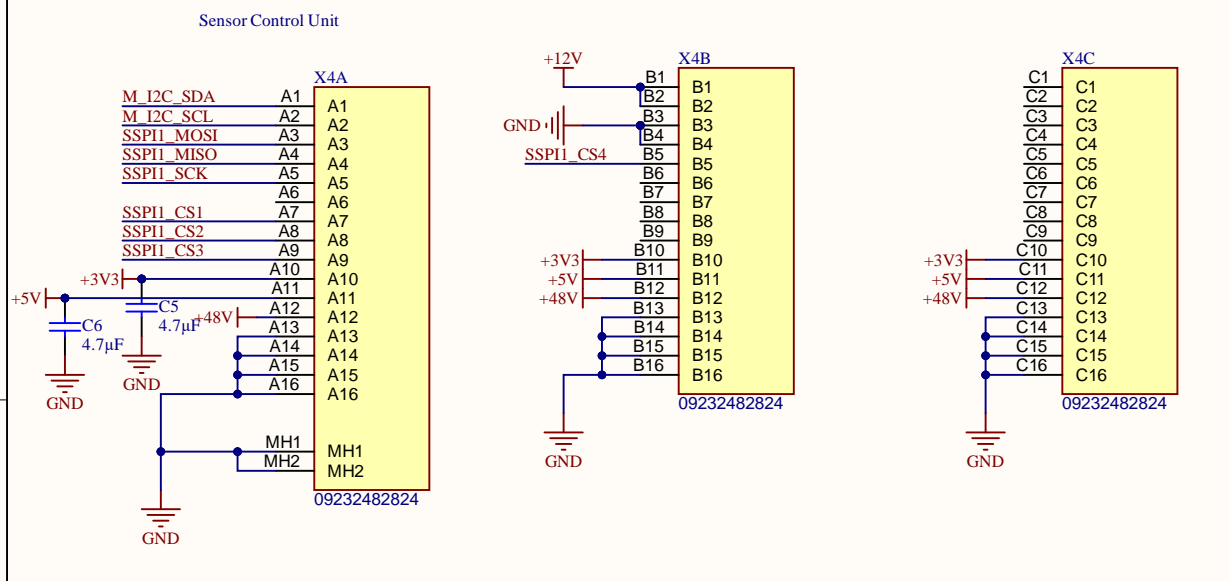
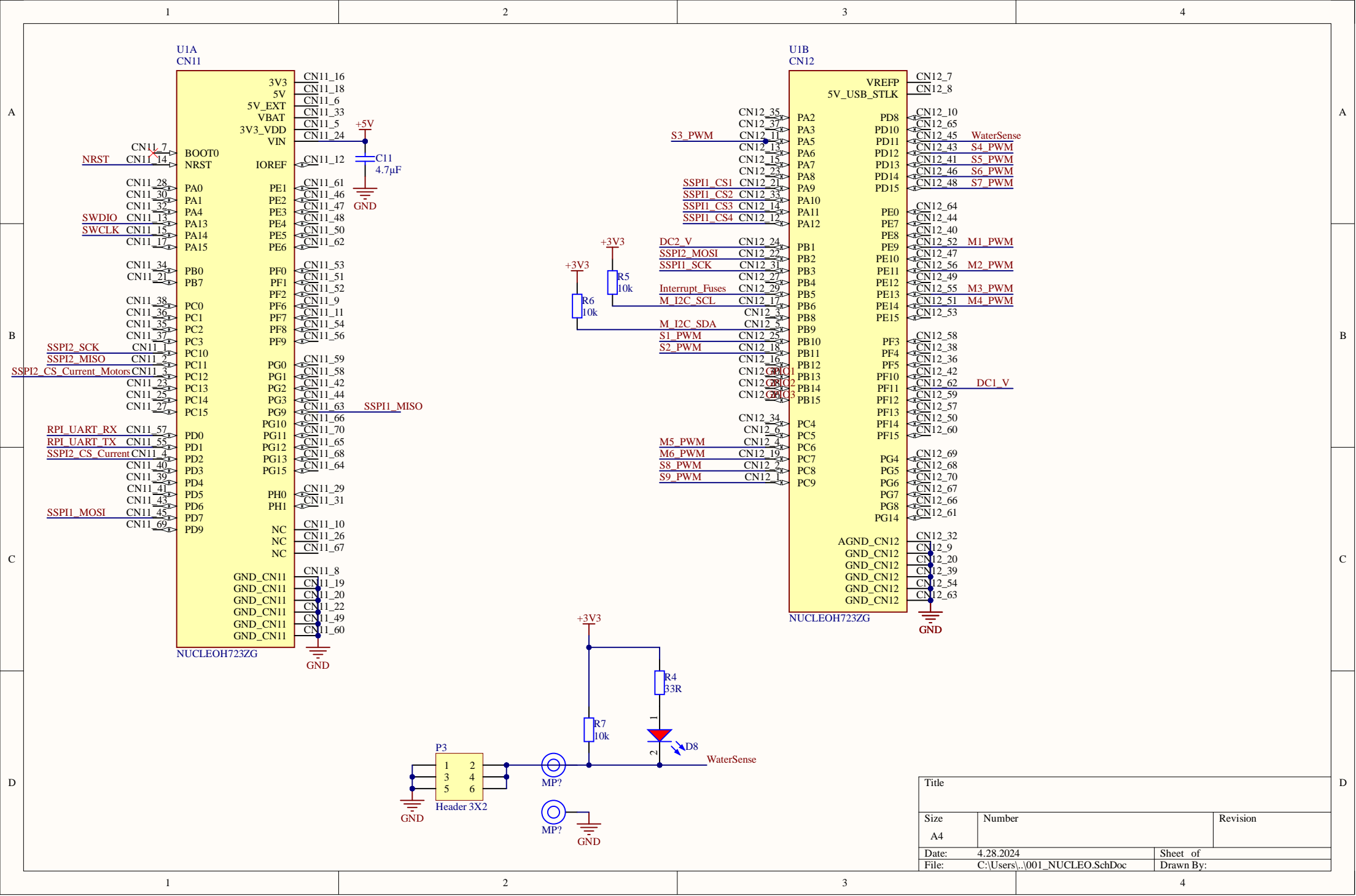


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REVISION	DESCRIPTION	DATE	APPROVED

<div><div>GND</div><div>GND</div></div>		PROJECT		<div><div>Altium</div><div>Not in version control</div><div>Not in version control</div></div>							
APPROVALS		DATE									
ENG:											
DSN:											
CHK:				PROJECT REVISION:		DOCUMENT REVISION:		DESIGN ITEM:			
REFERENCE DOCUMENTS				TITLE							
BOM:				★							
ASSY DWG:				SIZE		CAGE CODE		DWG NO.		REV	
FAB DWG:				A3							
PCB DWG:				SCALE:		FILE NAME				SHEET	
						000 Main SchDoc				1 OF 3	



Servos

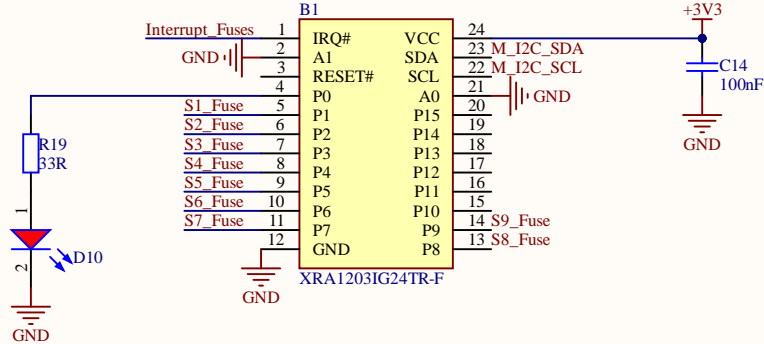
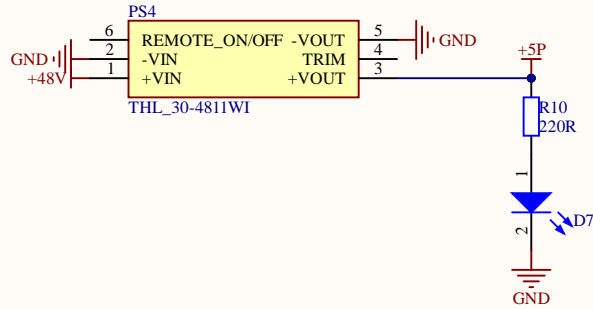
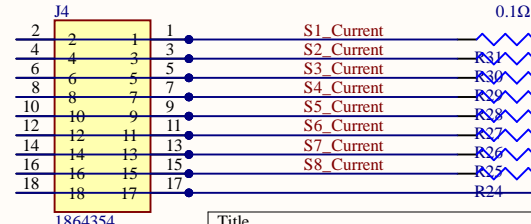
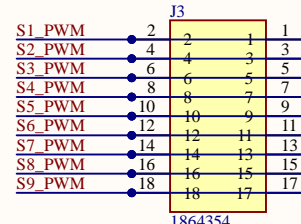
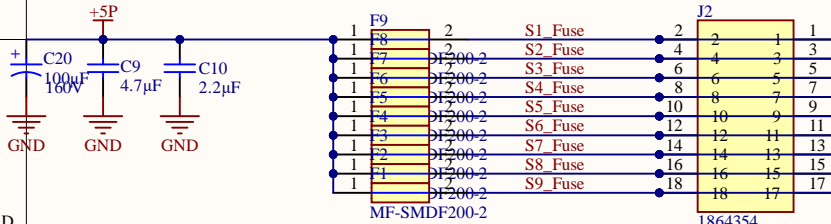
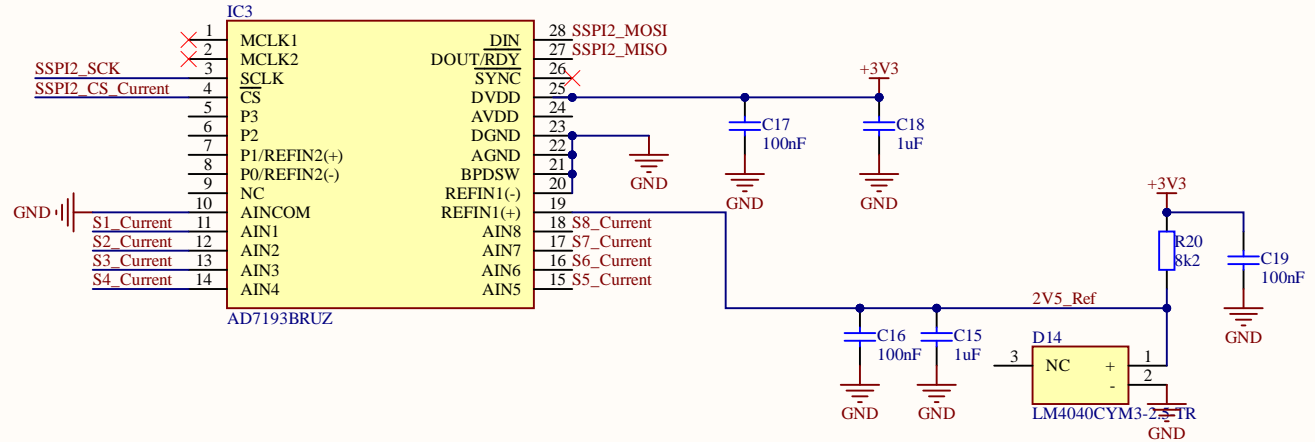
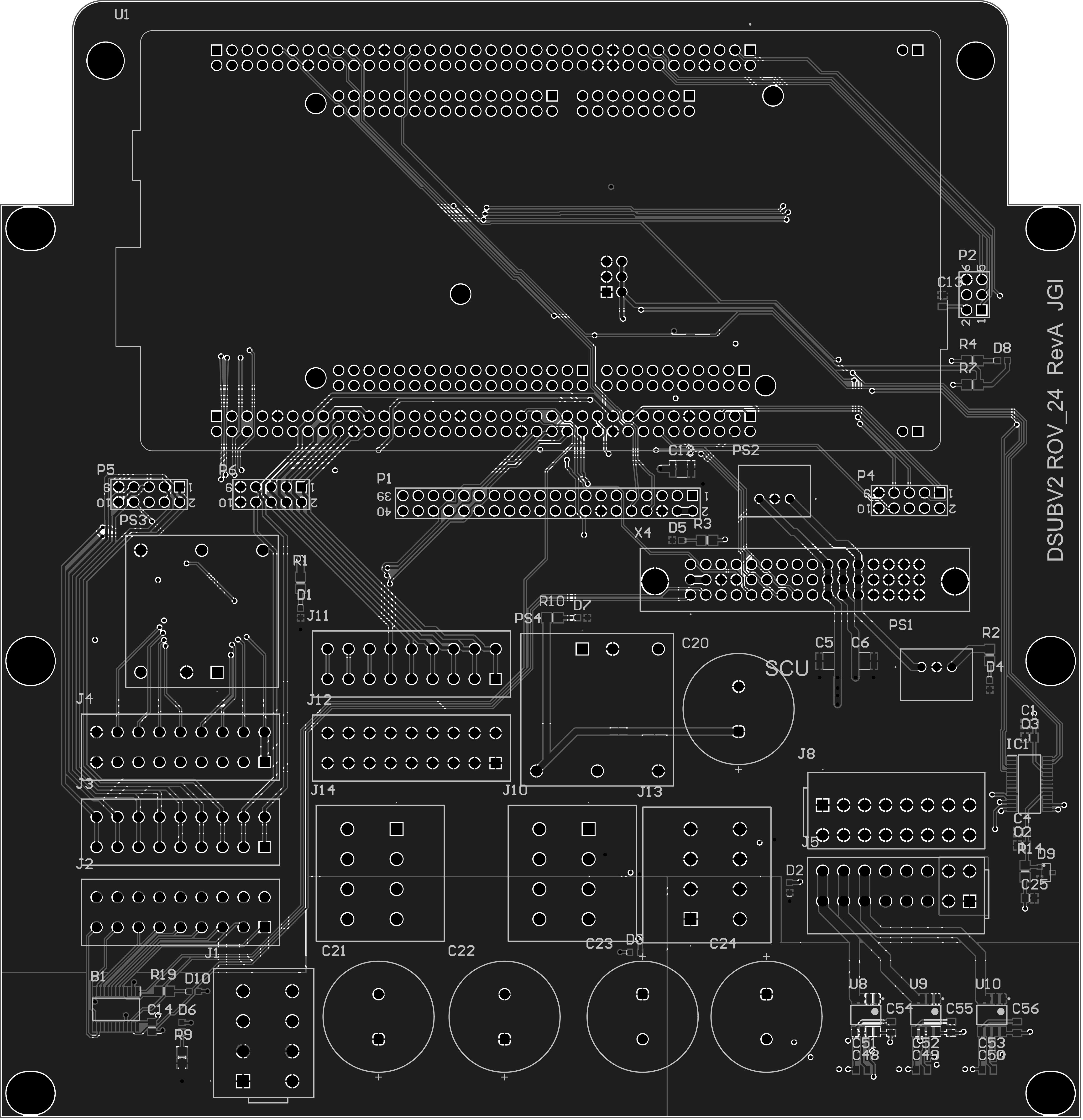


TABLE 1: PC Address MAP		
A1	A0	PC Address
SCL	GND	0x20 (0010 000X)
SCL	VCC	0x22 (0010 001X)
SDA	GND	0x24 (0010 010X)
SDA	VCC	0x26 (0010 011X)
SCL	SDA	0x30 (0011 000X)
SCL	SCL	0x32 (0011 001X)
SDA	SCL	0x34 (0011 010X)
SDA	SDA	0x36 (0011 011X)
GND	GND	0x40 (0100 000X)
GND	VCC	0x42 (0100 001X)
VCC	GND	0x44 (0100 010X)
VCC	VCC	0x46 (0100 011X)
GND	SCL	0x50 (0101 000X)
GND	SDA	0x52 (0101 001X)
VCC	SCL	0x54 (0101 010X)
VCC	SDA	0x56 (0101 011X)



Title		
Size	Number	Revision
A4		
Date:	4.28.2024	Sheet of
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