



GROUP 47 FENGYU WU

P1 PIN 1 is connected to 5V Power supply.
P1 PIN 2 is connected to Ground Plane.
P1 PIN 1-5 are connected to the STM32 Analogue In ports.
P1 PIN 6 is connected to GPIO port.

Comment	Description	Designator	Footprint	LibRef	Quantity
Debug Interface	Header, 6-Pin	P1, P2	PROGRAMMING- HEADER-MICRO	Header 6	2
Resistor (IO board)	Resistor (SFR25)	R1, R2, R3, R4, R5, RA, RB, RC, RD, RE	RESISTOR_10K_TH_ QW	Resistor(IO board)	10
TCRT5000(On PCB)	4-Pin Phototransistor Optocoupler	S1, S2, S3, S4, S5	TCRT5000 Breakout	TCRT5000(On PCB)	5
ULN2003	Darlington Transistor Array	U1	DILP-16(300) - 3D	ULN2003	1