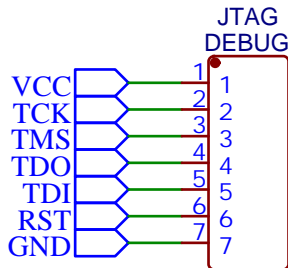


RF-BM-2652P1/P2 MODULE,LED,BUTTONS

[illegible]


DEBUG/PROG INTERFACE



The diagram illustrates the pinout for a JTAG/DEBUG interface. It shows a 7-pin connector with the following connections:

Signal	Pin
VCC	1
TCK	2
TMS	3
TDO	4
TDI	5
RST	6
GND	7

The pins are numbered 1 through 7, and the signals are labeled VCC, TCK, TMS, TDO, TDI, RST, and GND. The JTAG/DEBUG label is positioned above the pin numbers.

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	Company: DYI	Sheet: 1/1
	Date: 2020-09-23	Drawn By: mercenaruss