

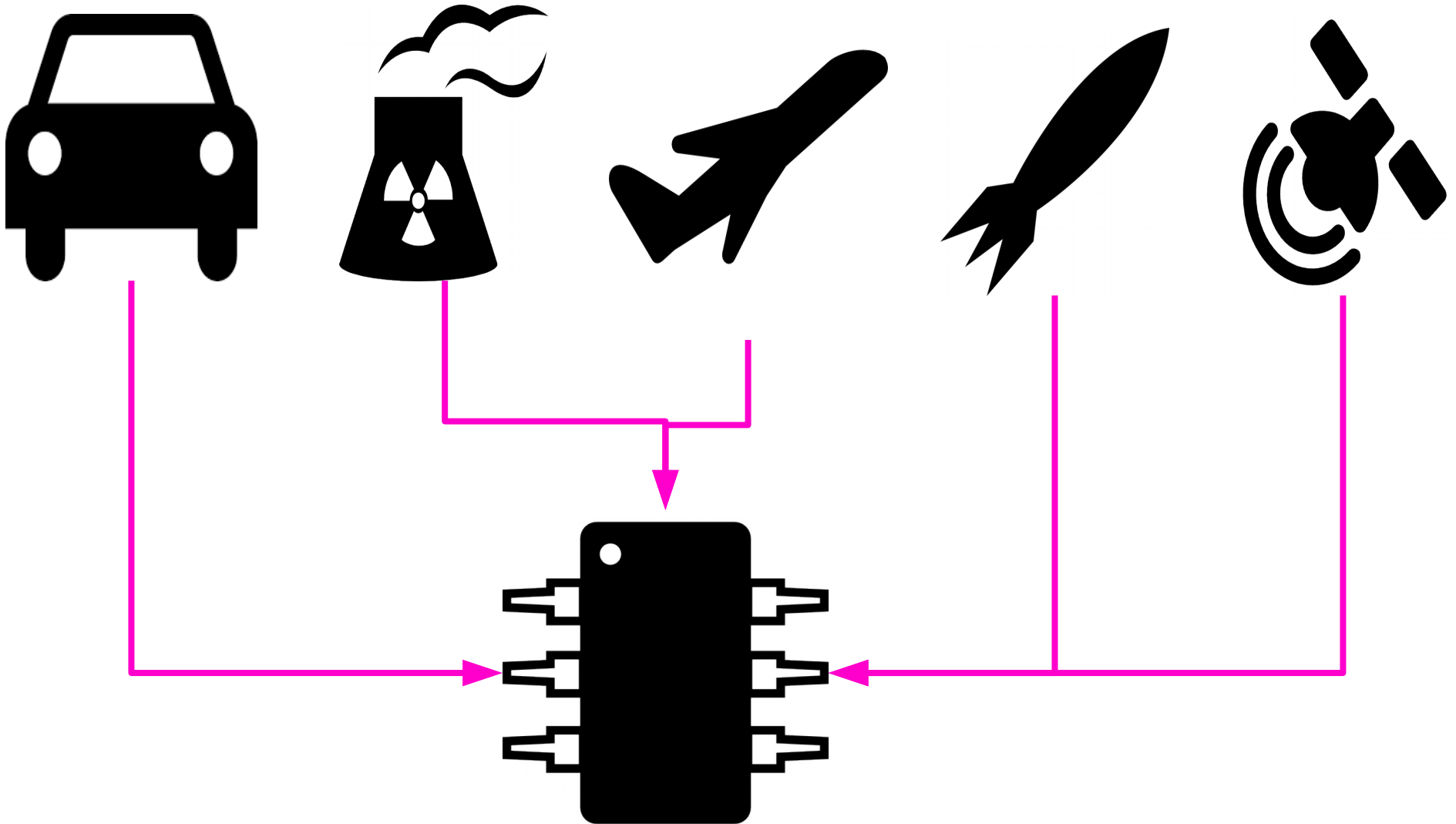
A Generalized Reduction of Ordered Binary Decision Diagram (GroBdd)

Joan Thibault

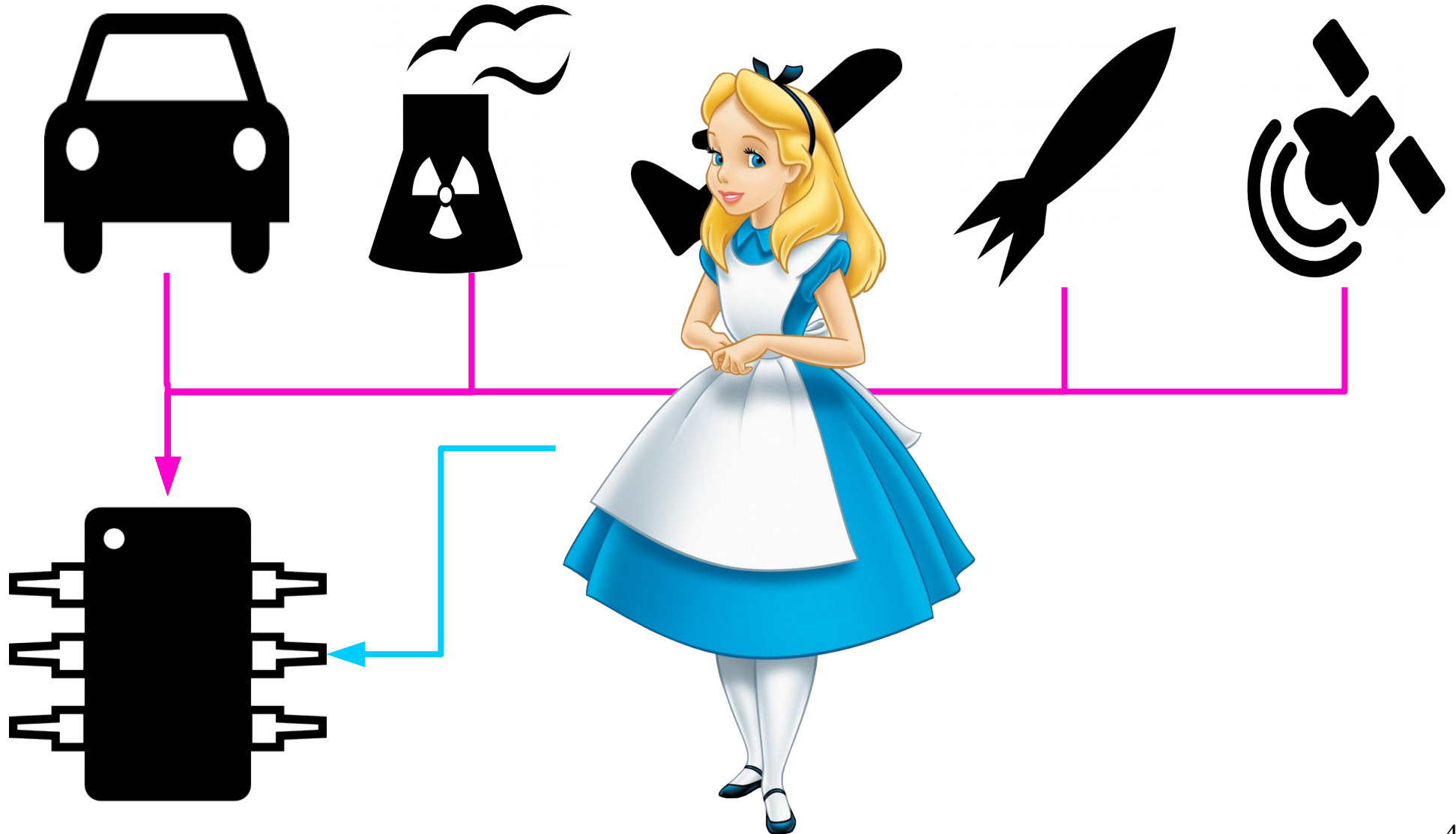
Most critical systems ...



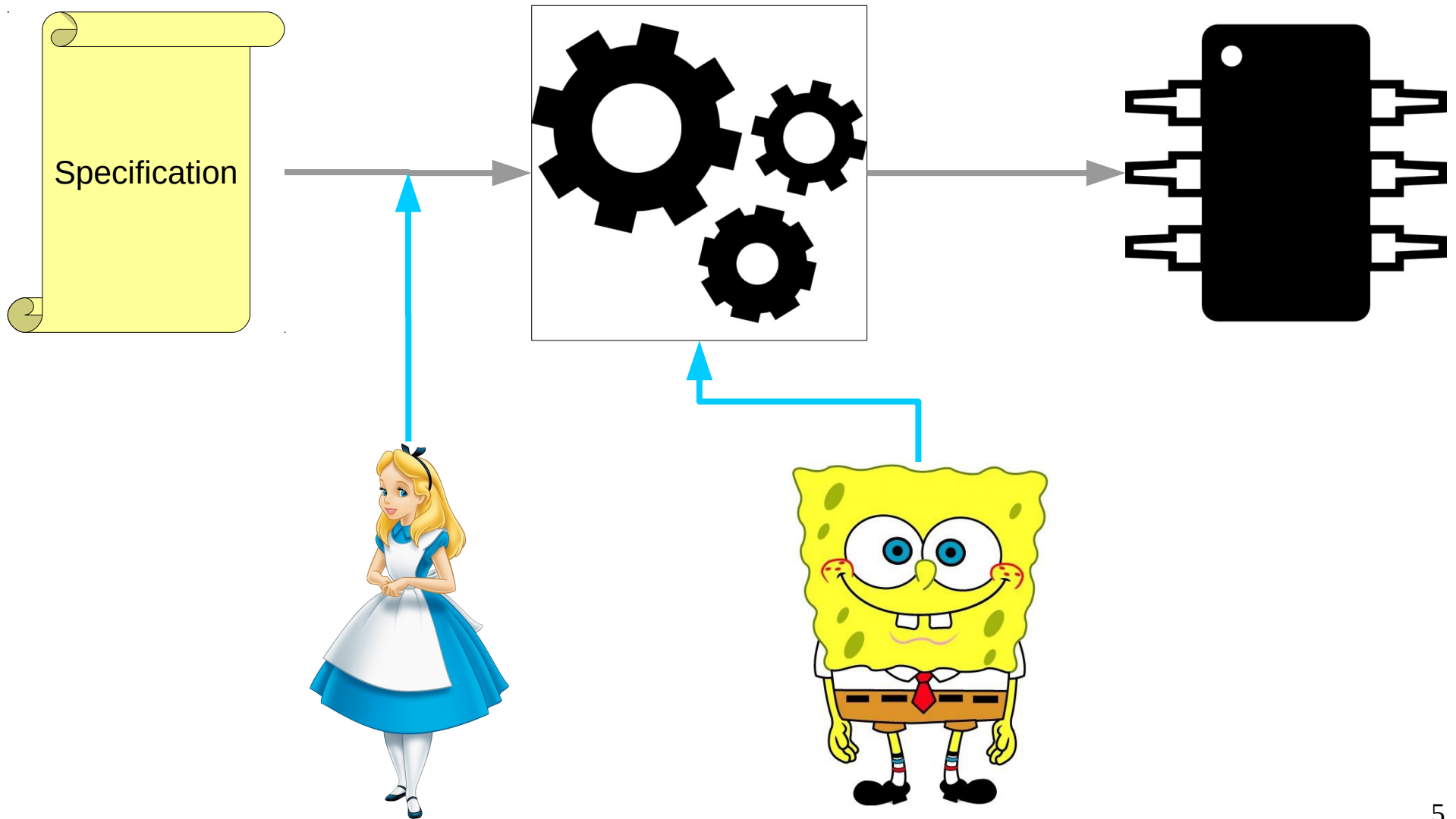
... relies on chips ...



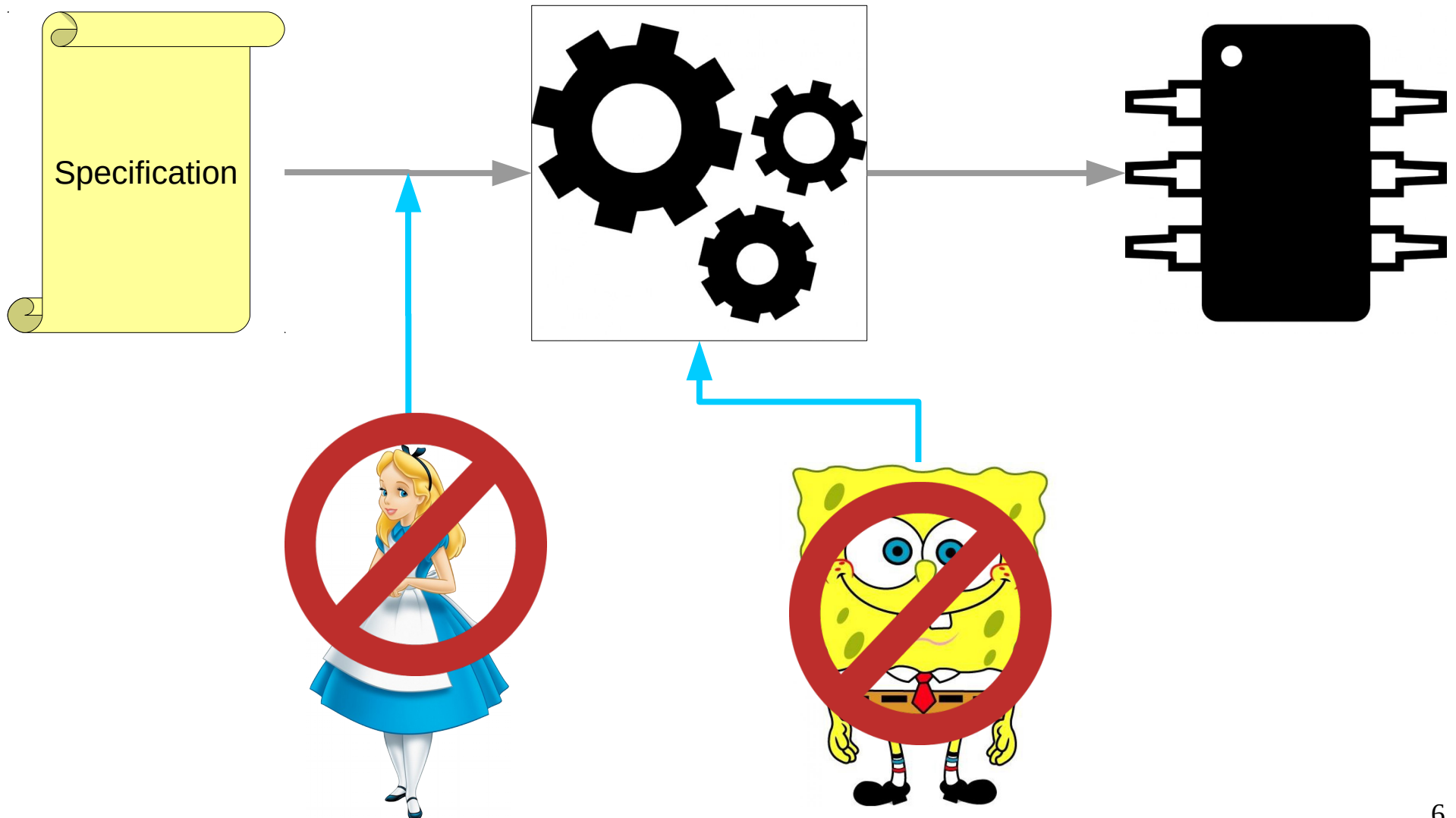
... designed by Alice ...



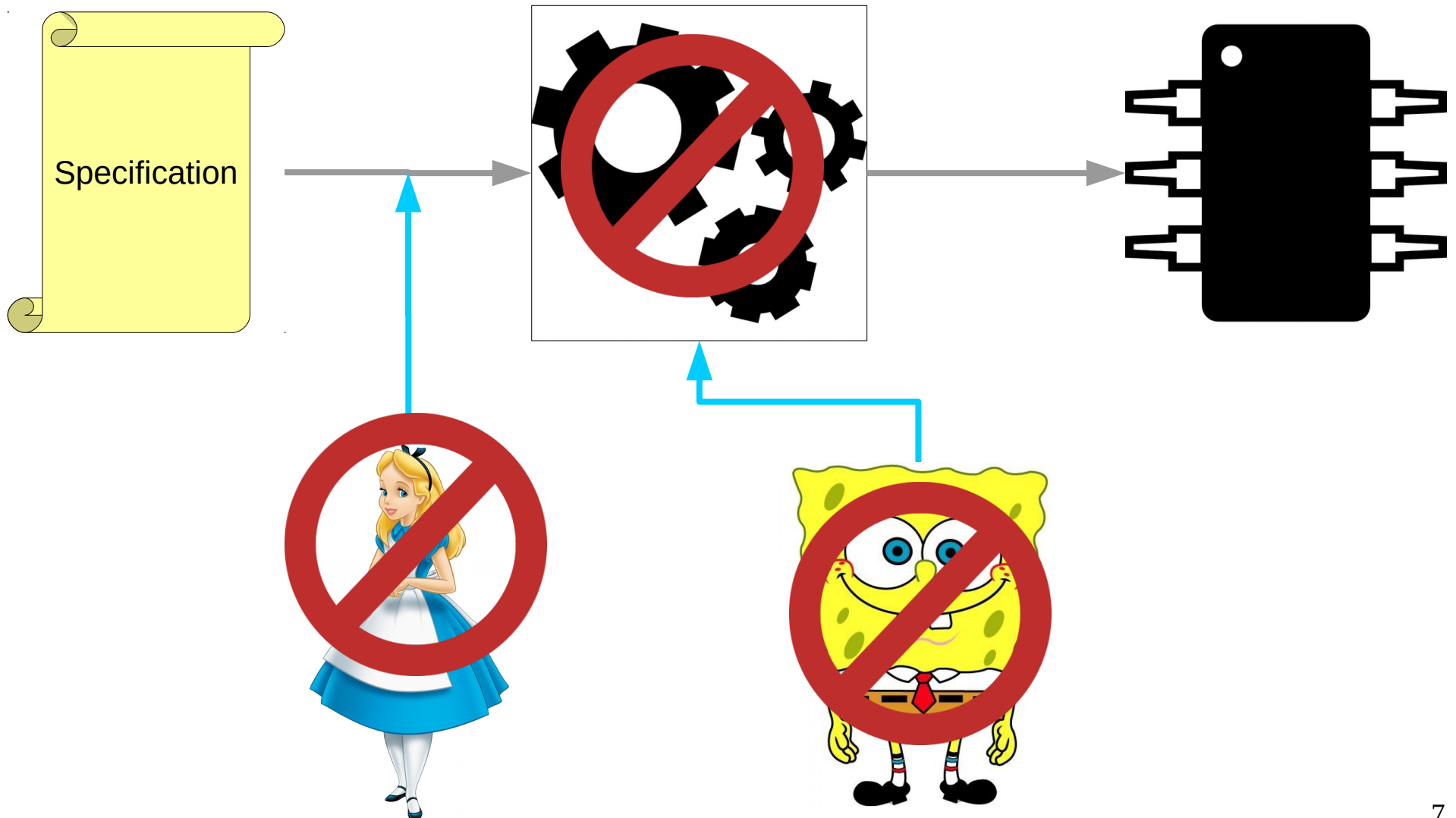
... using Bob's software.



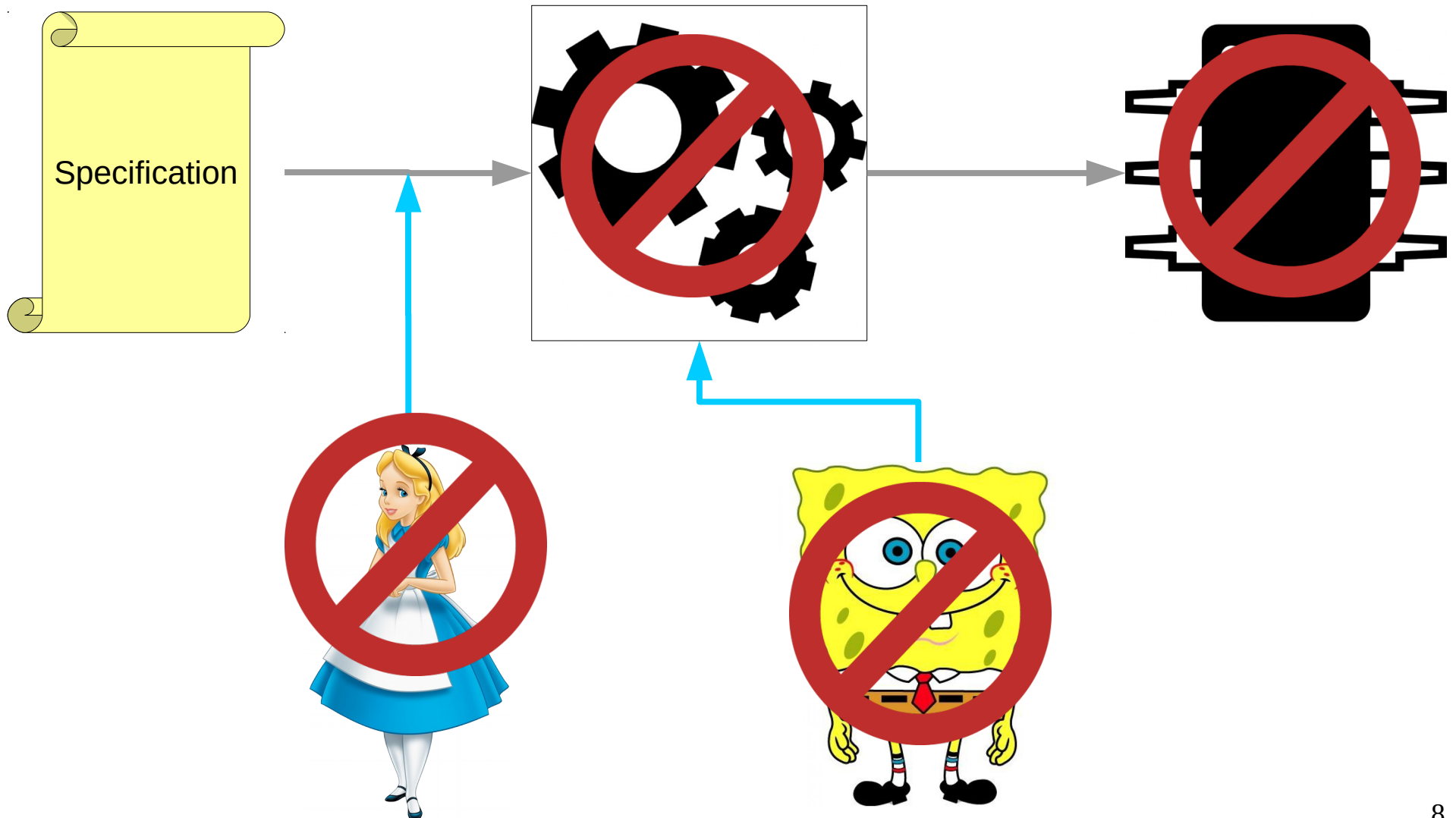
Lack of reliability



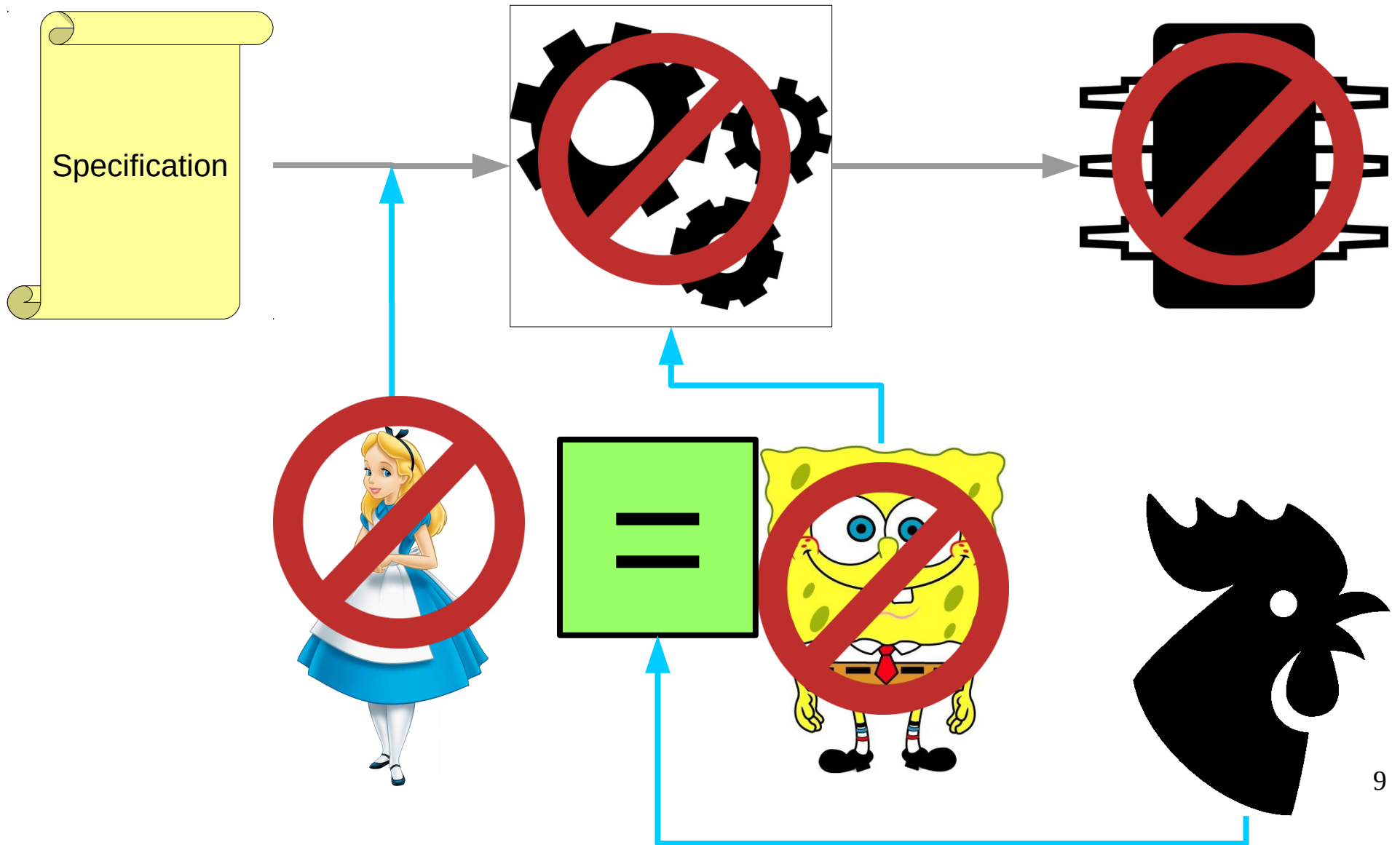
Lack of reliability



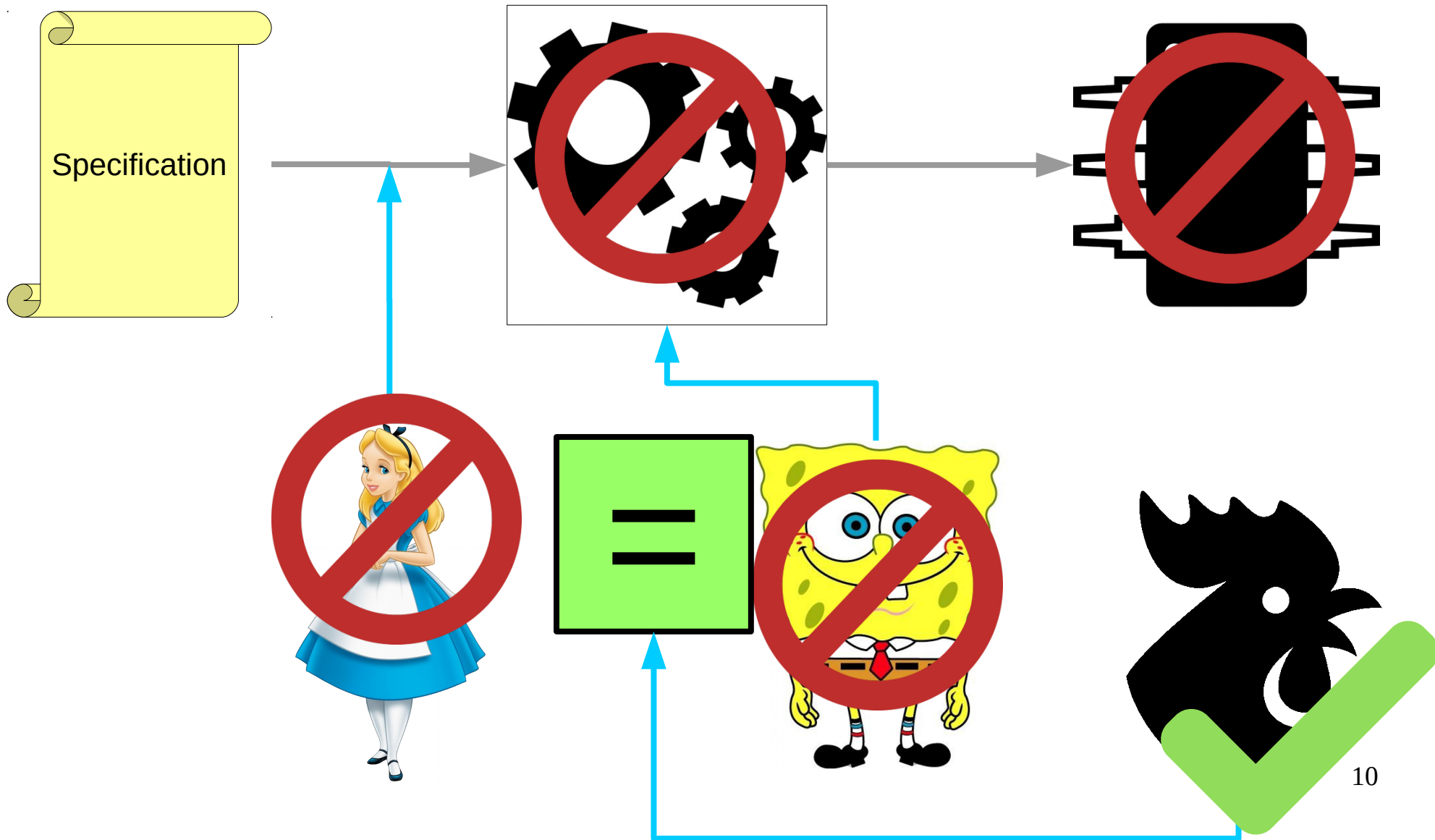
Lack of reliability



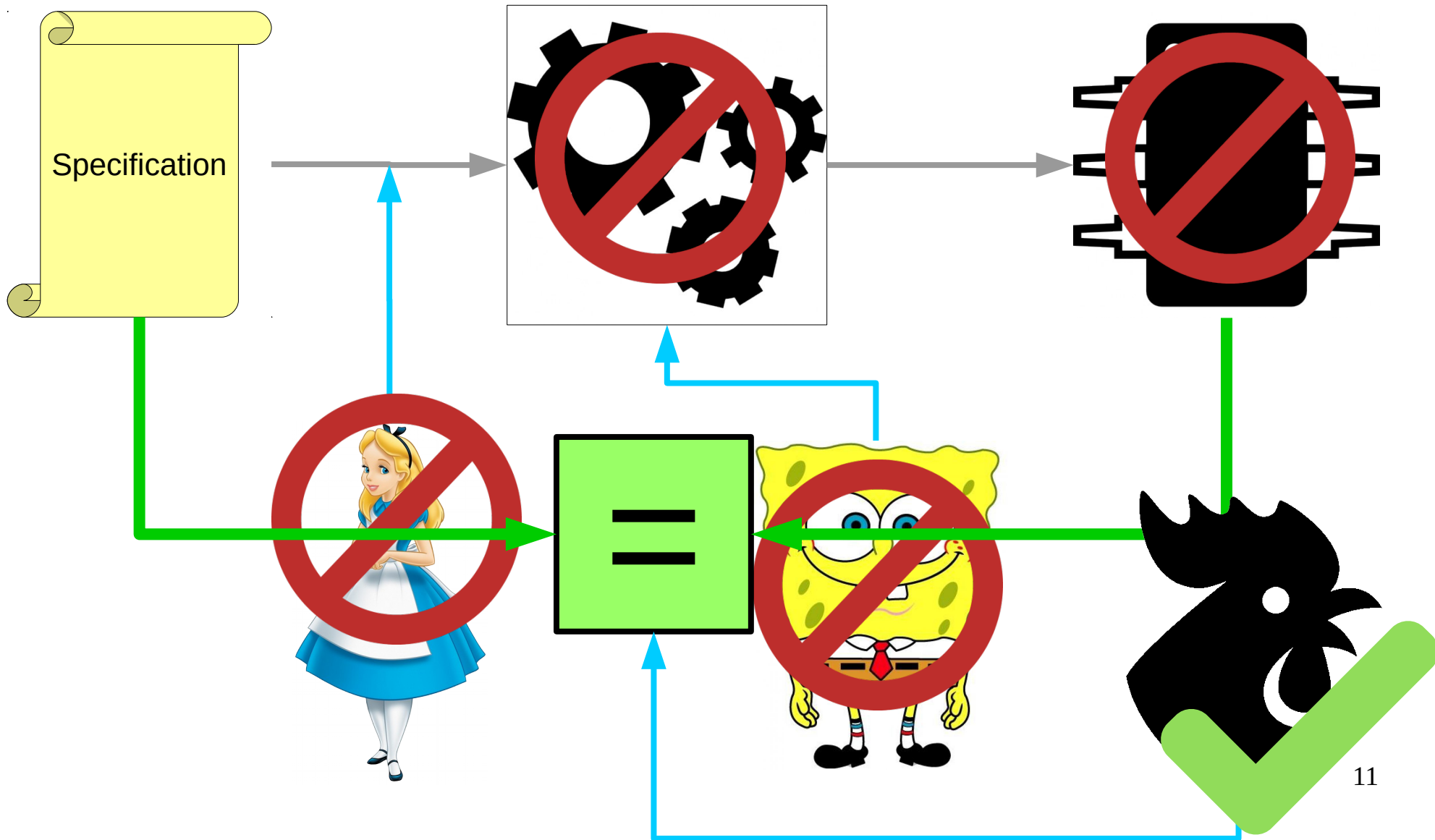
Circuit Equivalence Checking



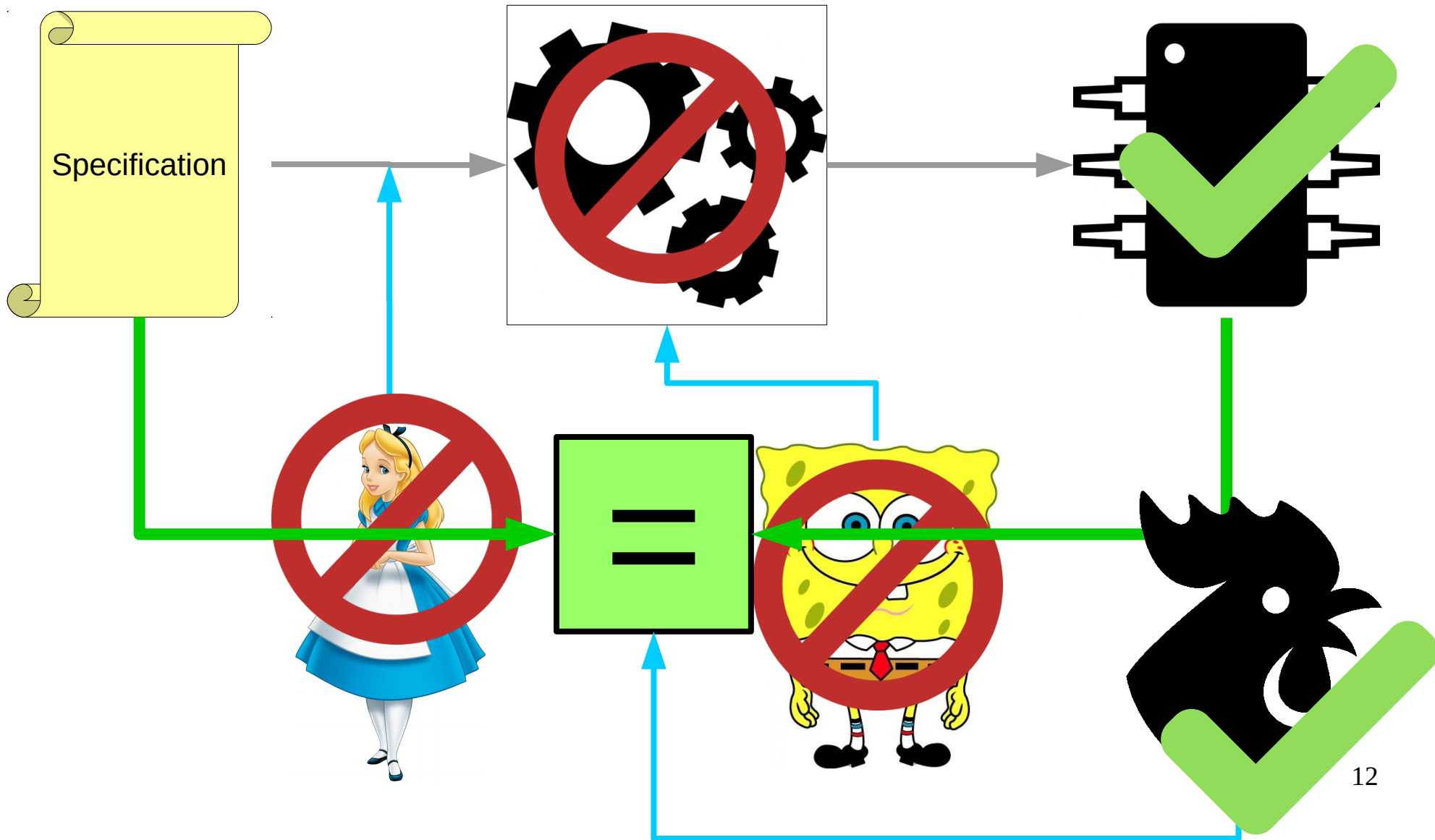
Circuit Equivalence Checking



Circuit Equivalence Checking



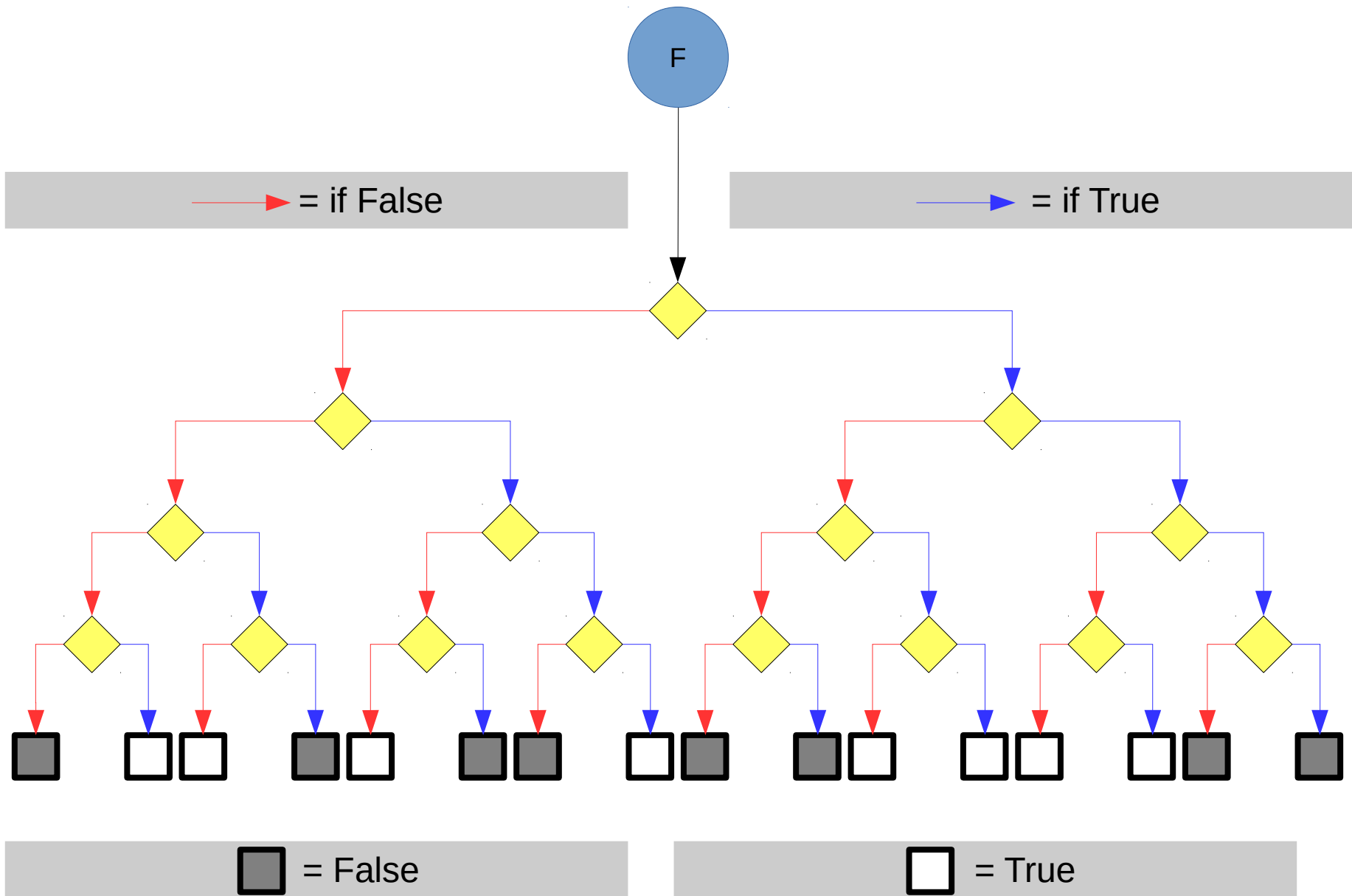
Design matches specification



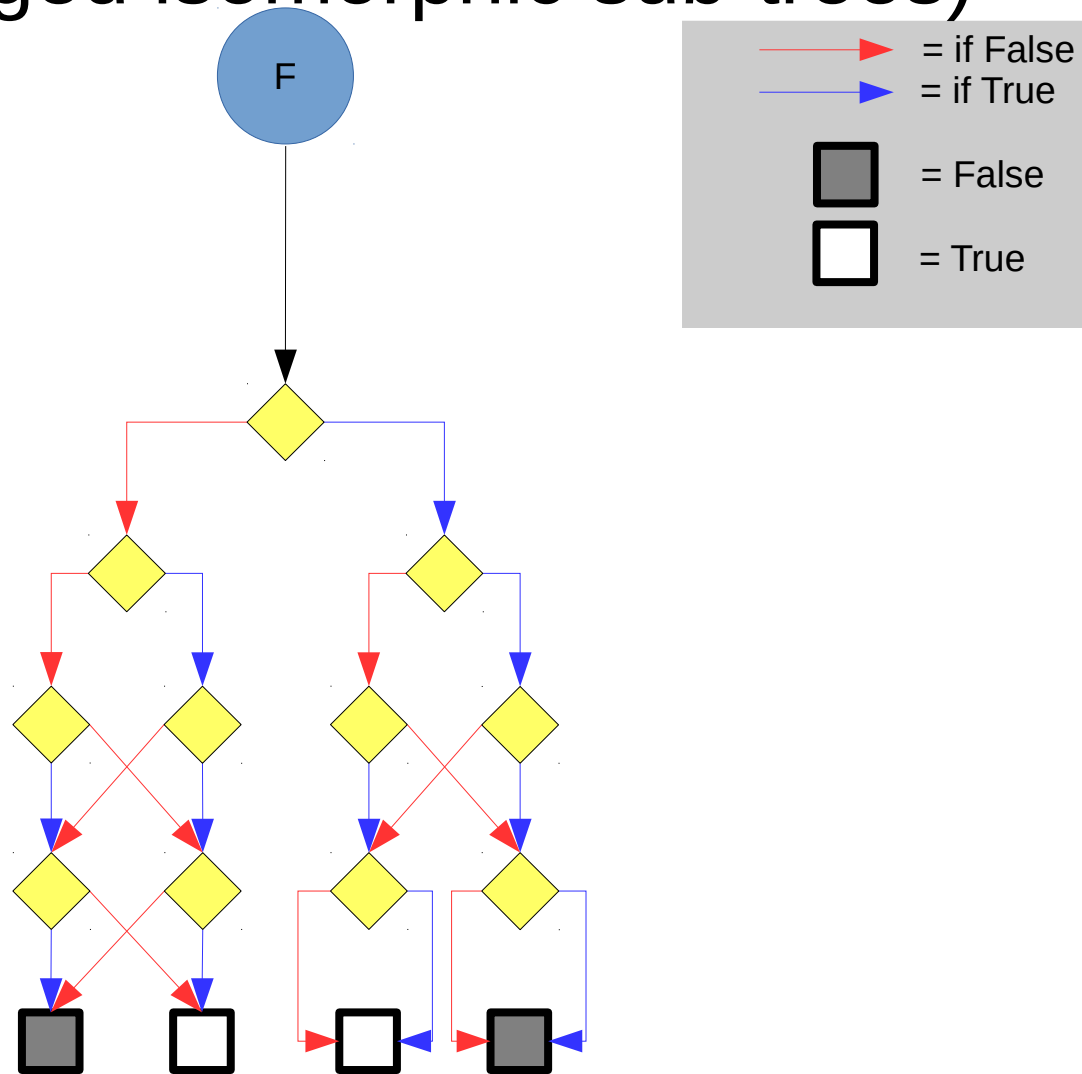
Reduced Ordered Binary Decision Diagram (ROBDD)

- Applications
 - Computer Aided Design (e.g. equivalence checking)
 - Knowledge Representation (e.g. Artificial Intelligence)
 - Combinatorial Problems (e.g. CNF SAT problem)
- What are required operation ?
 - Compact representation
 - Operations (e.g. composing, concatenating, evaluation)
 - Operators (e.g. AND, XOR, ITE, NOT)
 - Reductions (e.g. quantification, partial evaluation, SAT)

Shanon's Binary Decision Tree

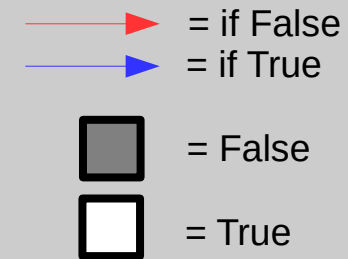
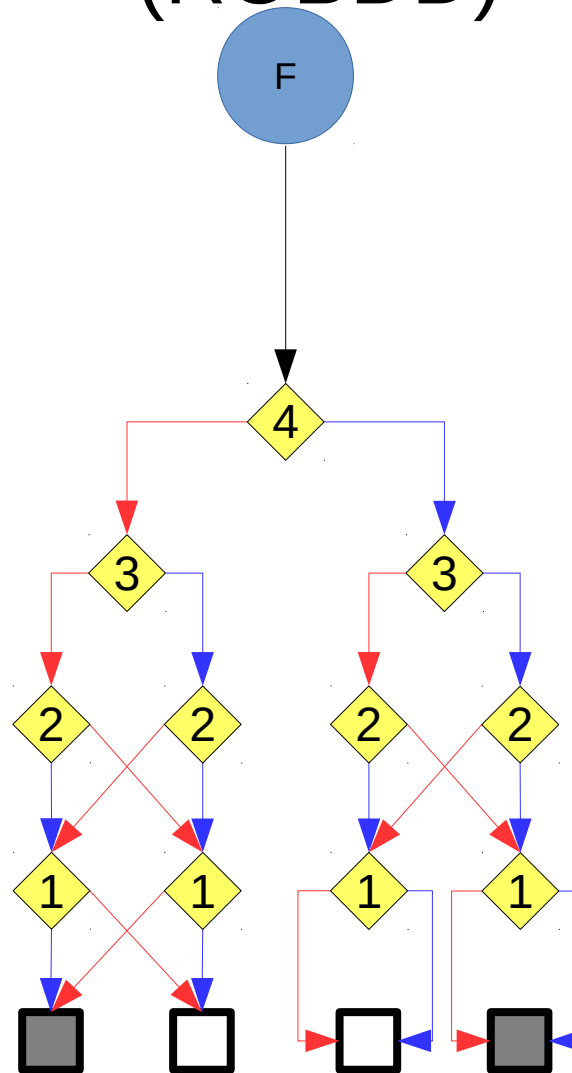


Shannon's Decision Diagram (We merged isomorphic sub-trees)



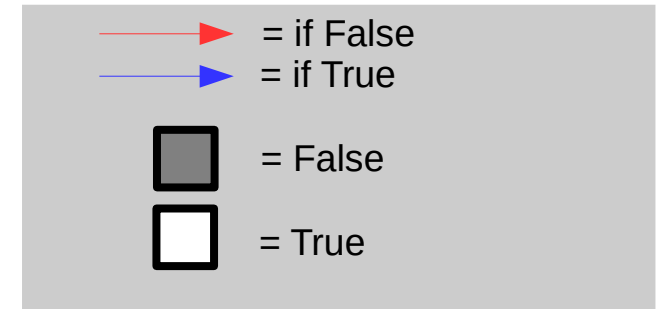
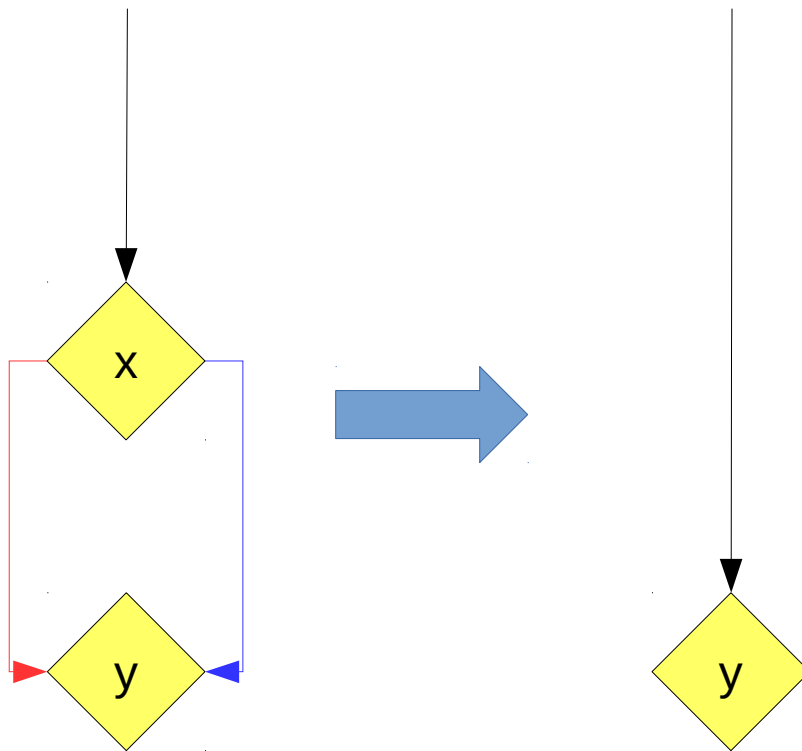
For aesthetic reasons, we do not merge all terminals

Reduced Ordered Binary Decision Diagram (ROBDD)



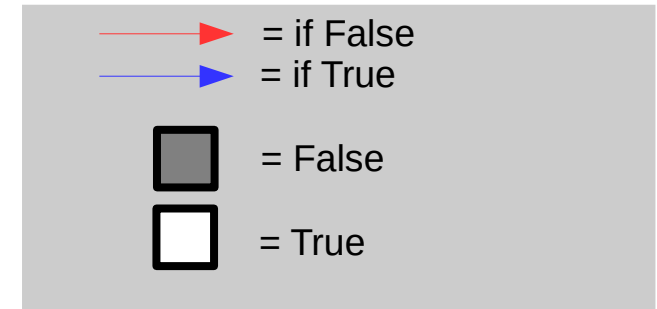
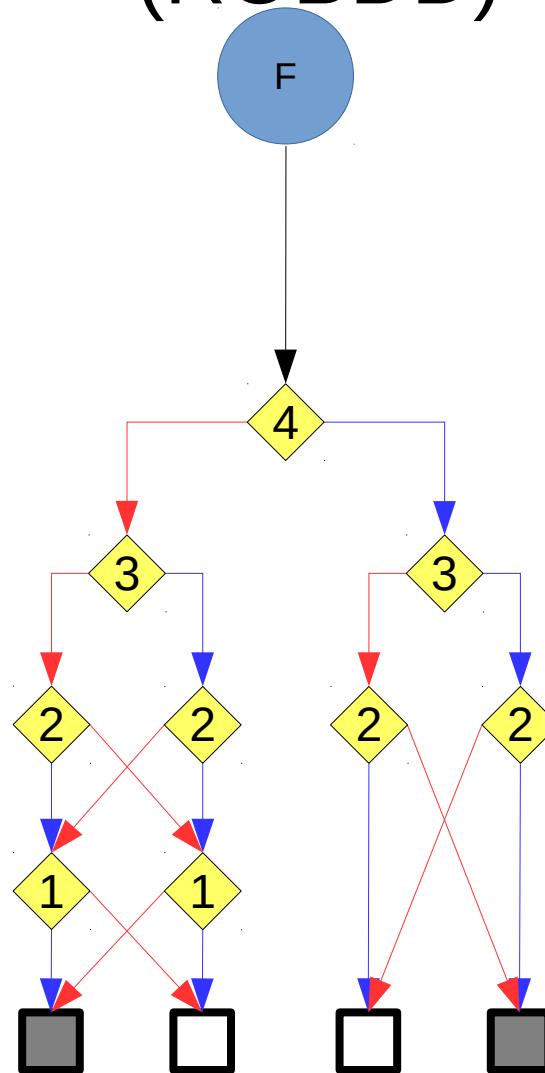
For aesthetic reasons, we do not merge all terminals

ROBDD : reduction rule

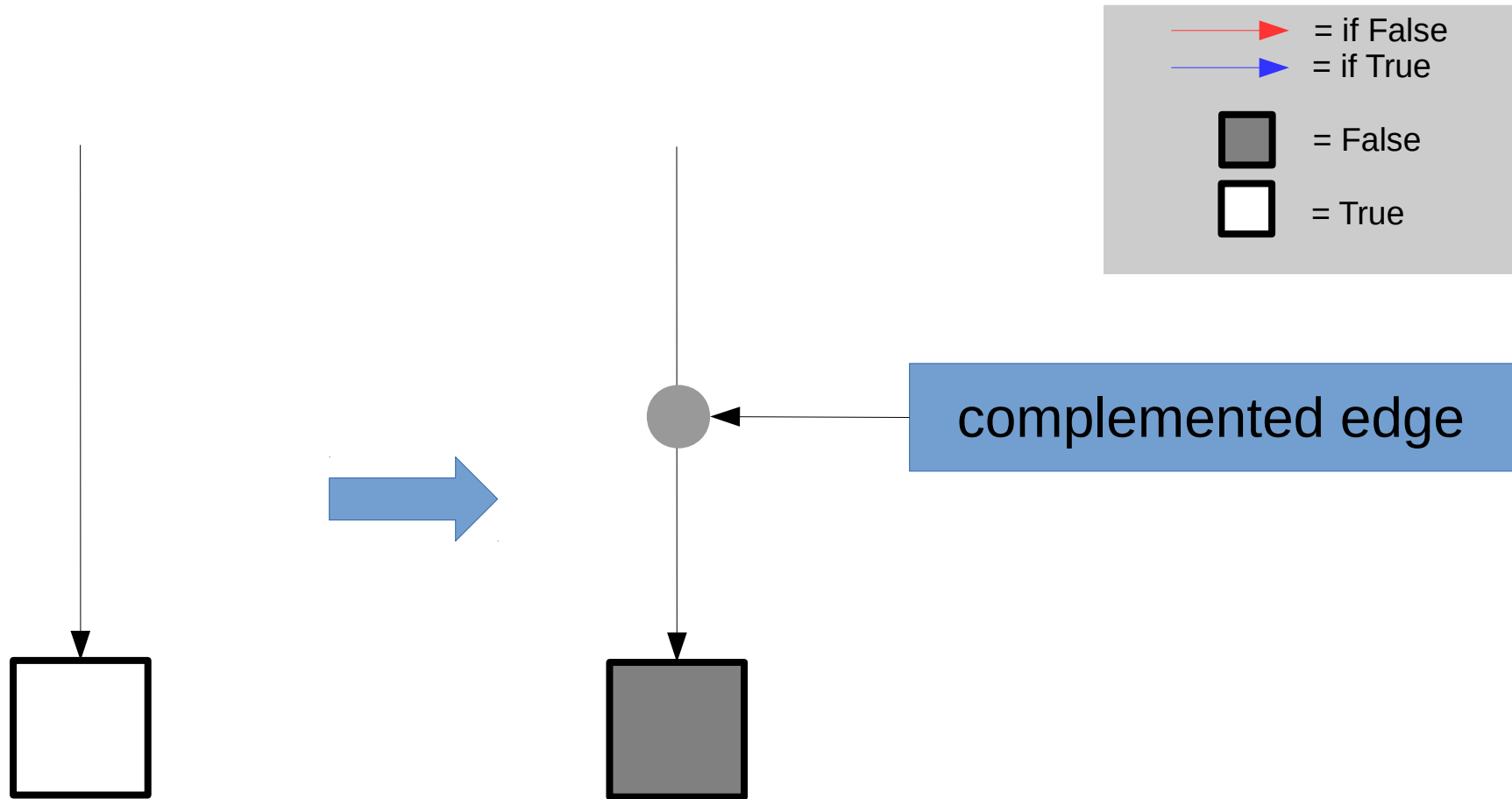


For aesthetic reasons, we do not merge all terminals

Reduced Ordered Binary Decision Diagram (ROBDD)

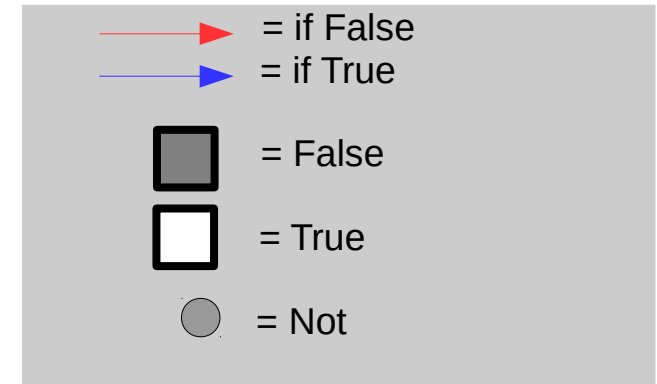
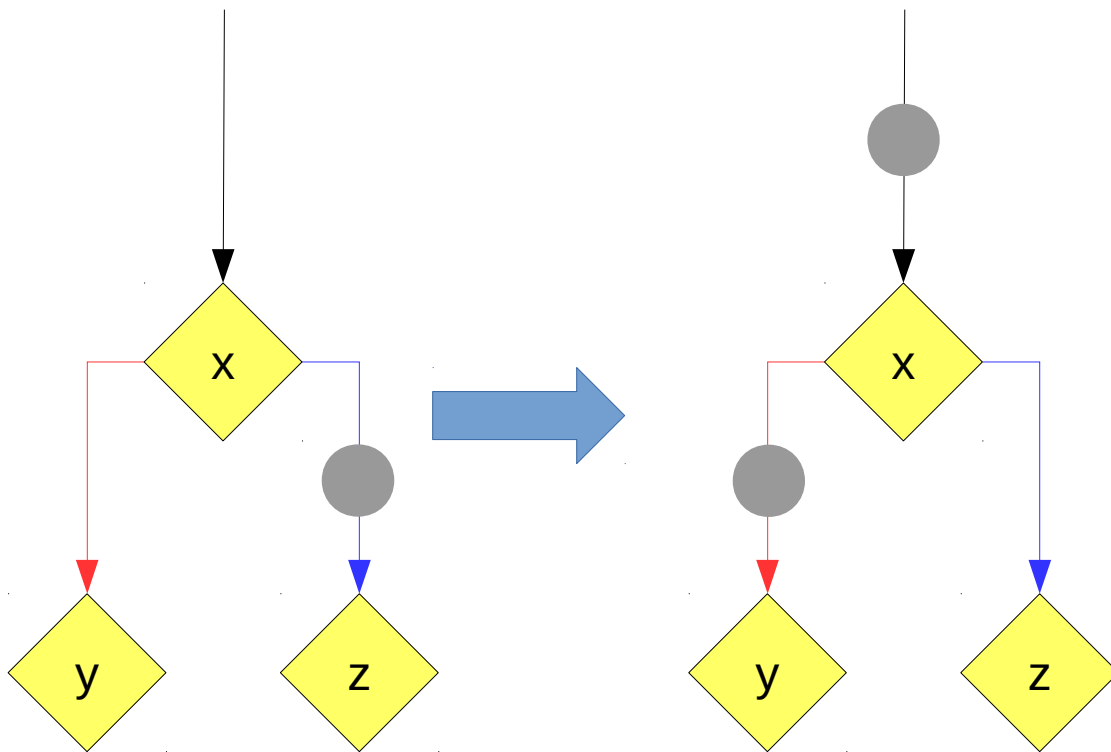


“output negation” : reduction rule (N1)



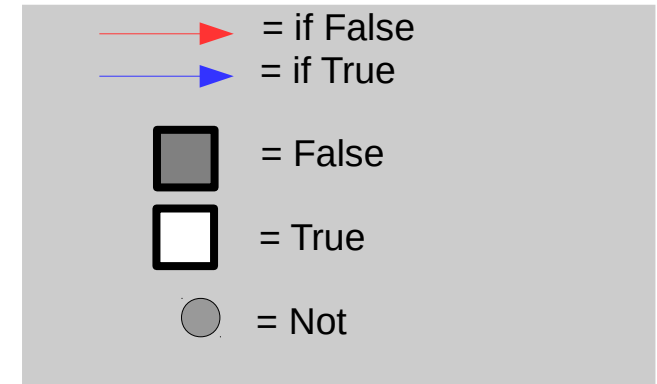
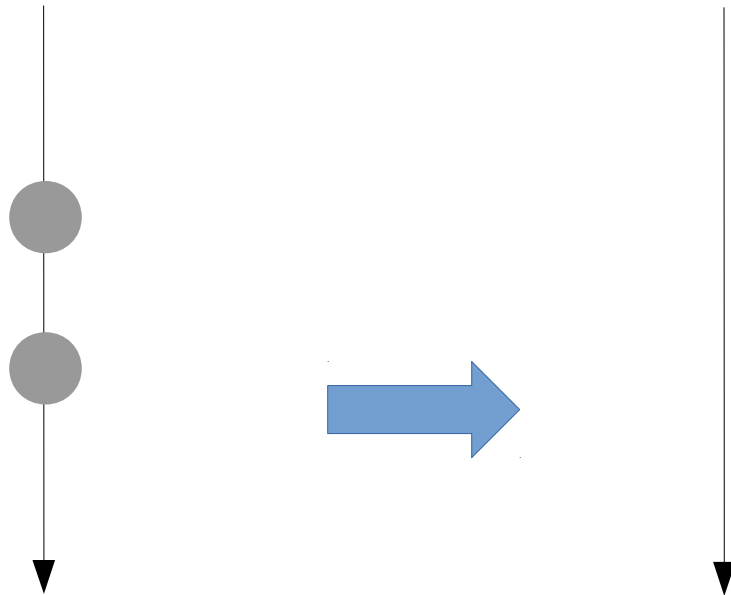
For aesthetic reasons, we do not merge all terminals

“output negation” : reduction rule (N2)



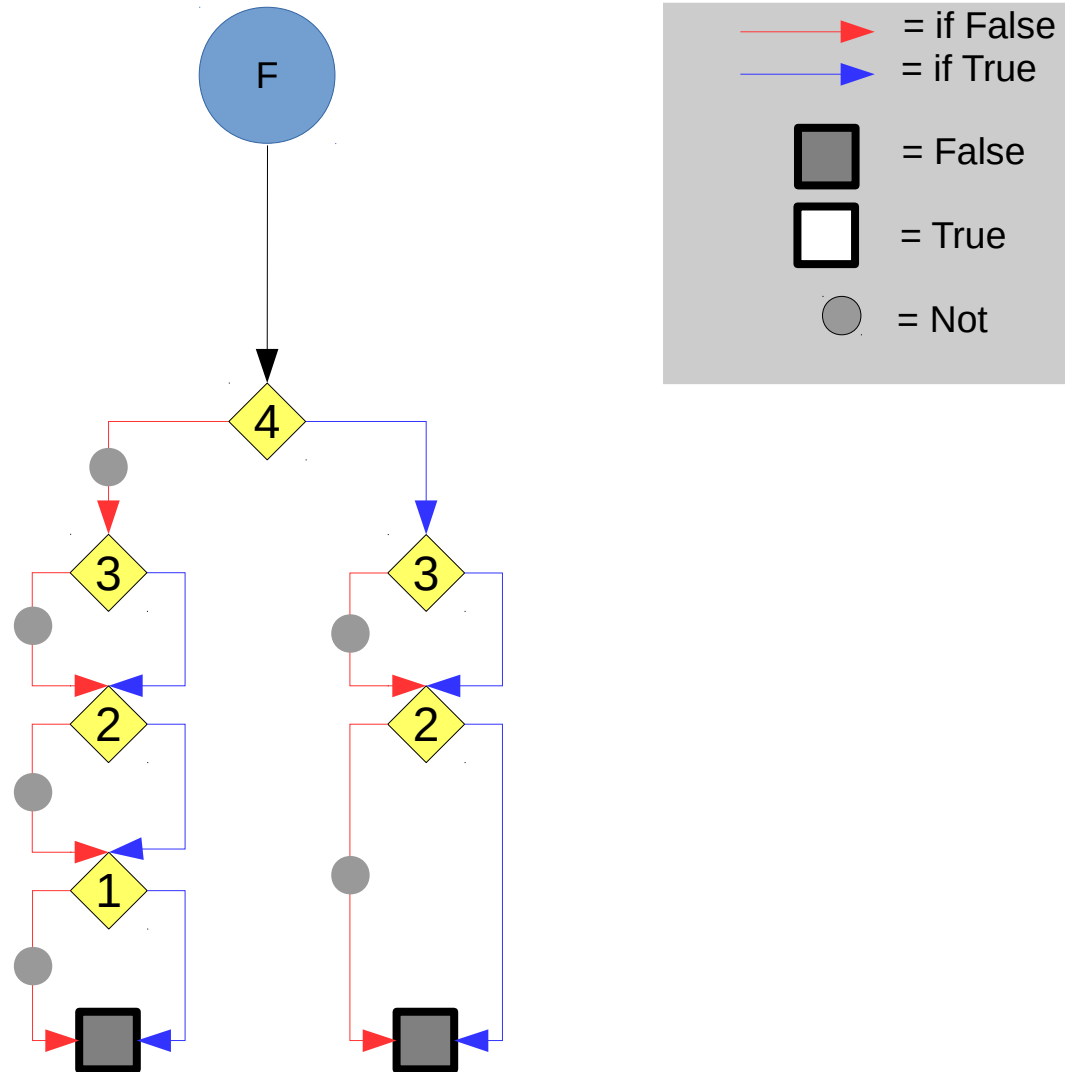
For aesthetic reasons, we do not merge all terminals

“output negation” : reduction rule (N3)

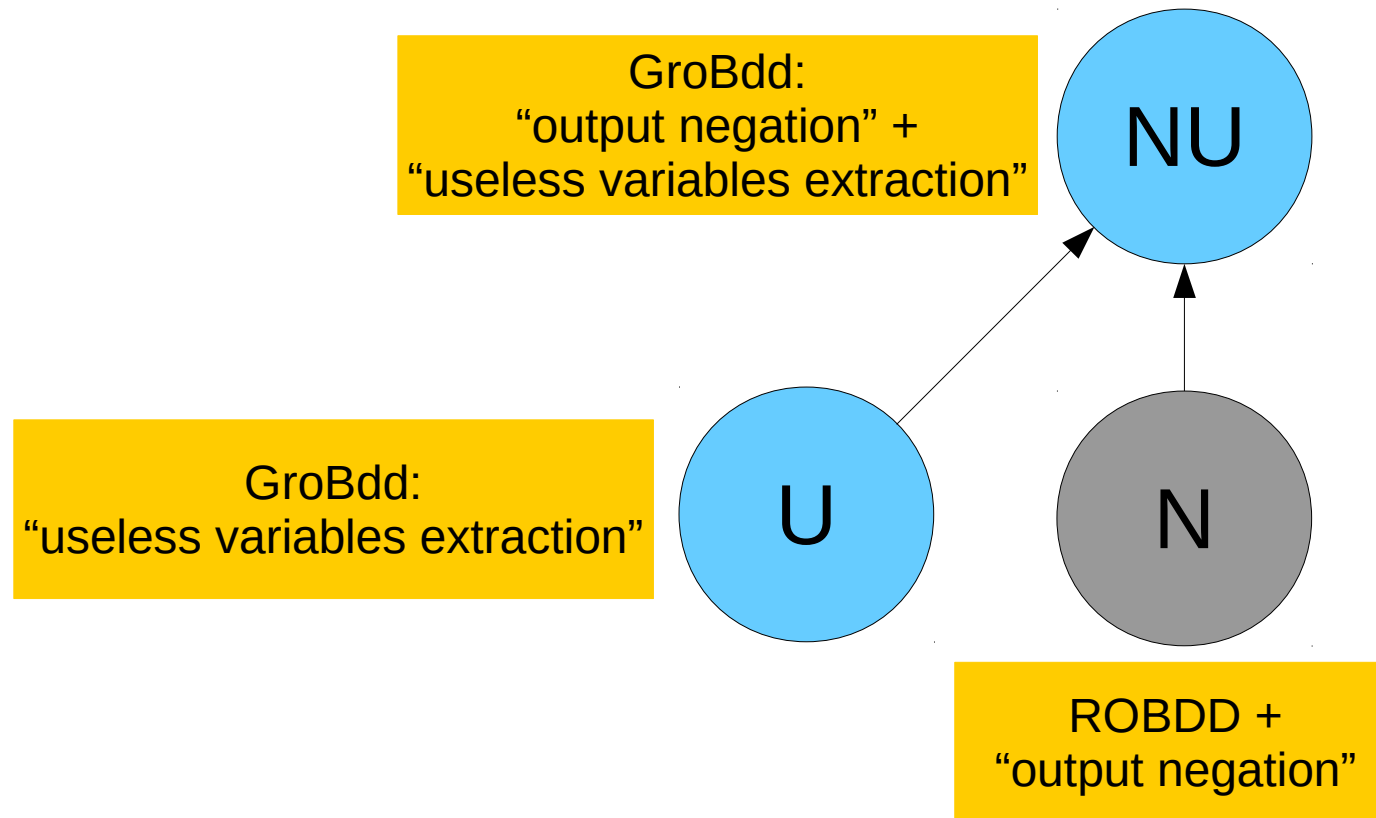


For aesthetic reasons, we do not merge all terminals

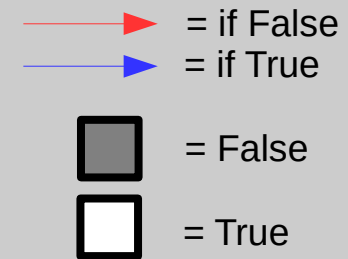
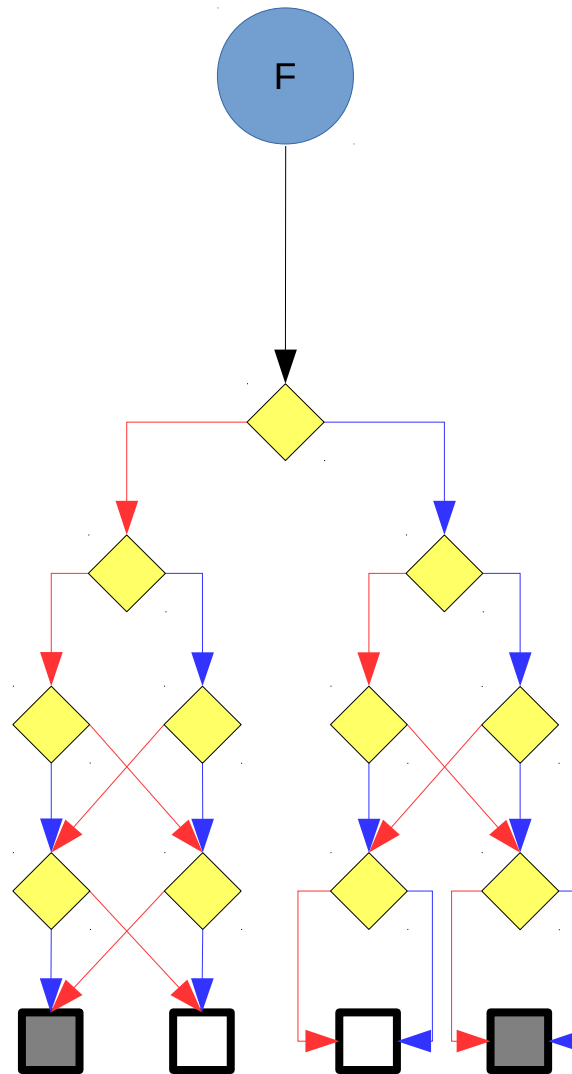
ROBDD + “output negation”



Generalized Reduction of Ordered Binary Decision Diagram (GroBdd)



Shannon's Decision Diagram



For aesthetic reasons, we do not merge all terminals

Section 1 : model U

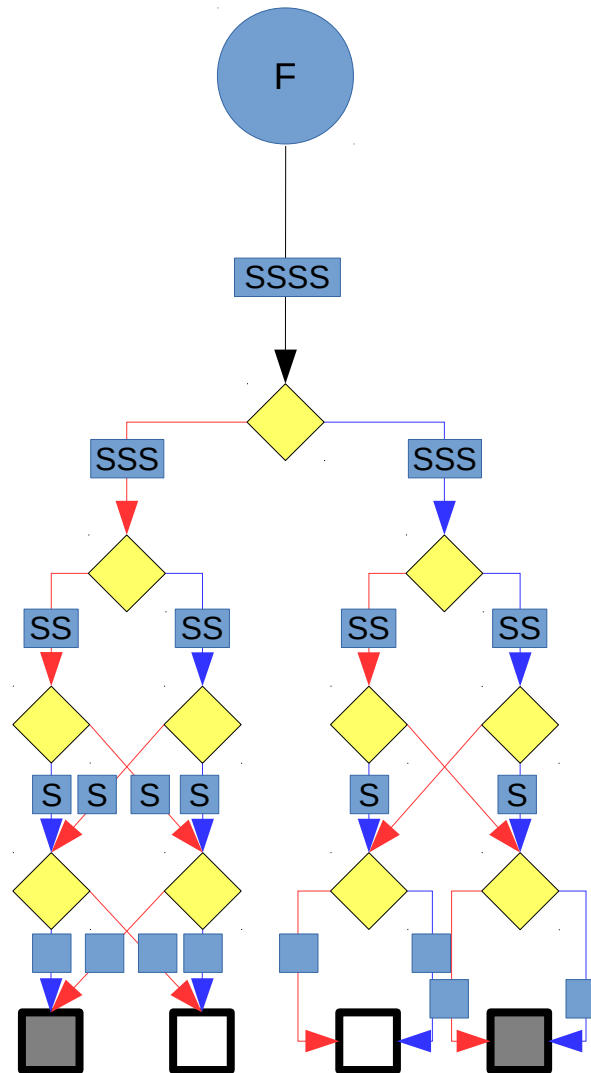
Functions of arity 4

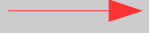
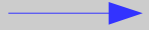
Functions of arity 3



Functions of arity 2

Functions of arity 1

Functions of arity 0



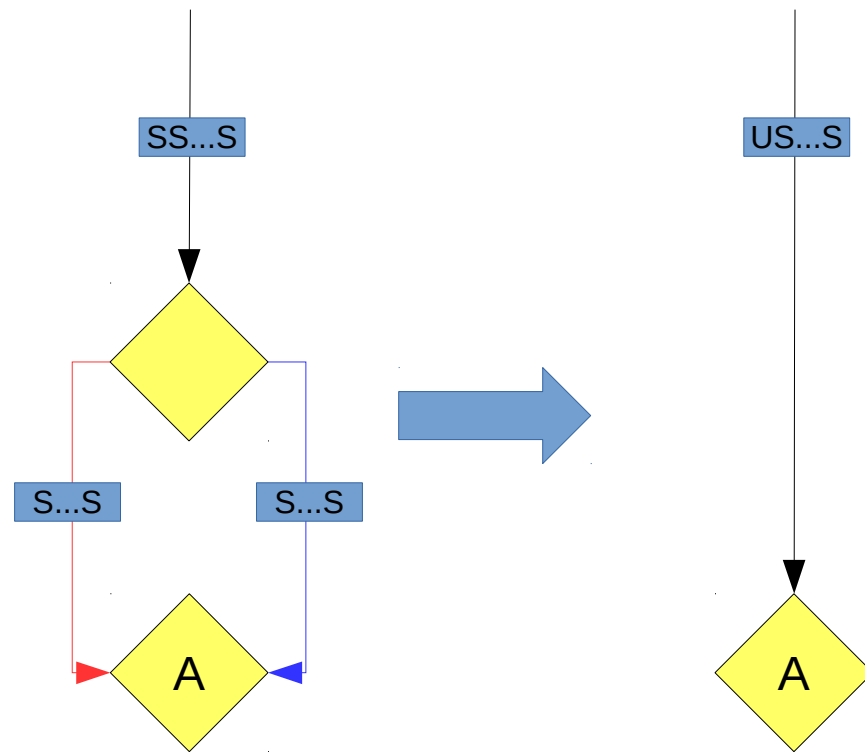
 = if False
 = if True

 = False
 = True

S = significant

For aesthetic reasons, we do not merge all terminals

“Useless variables extraction” : reduction rule (U1)



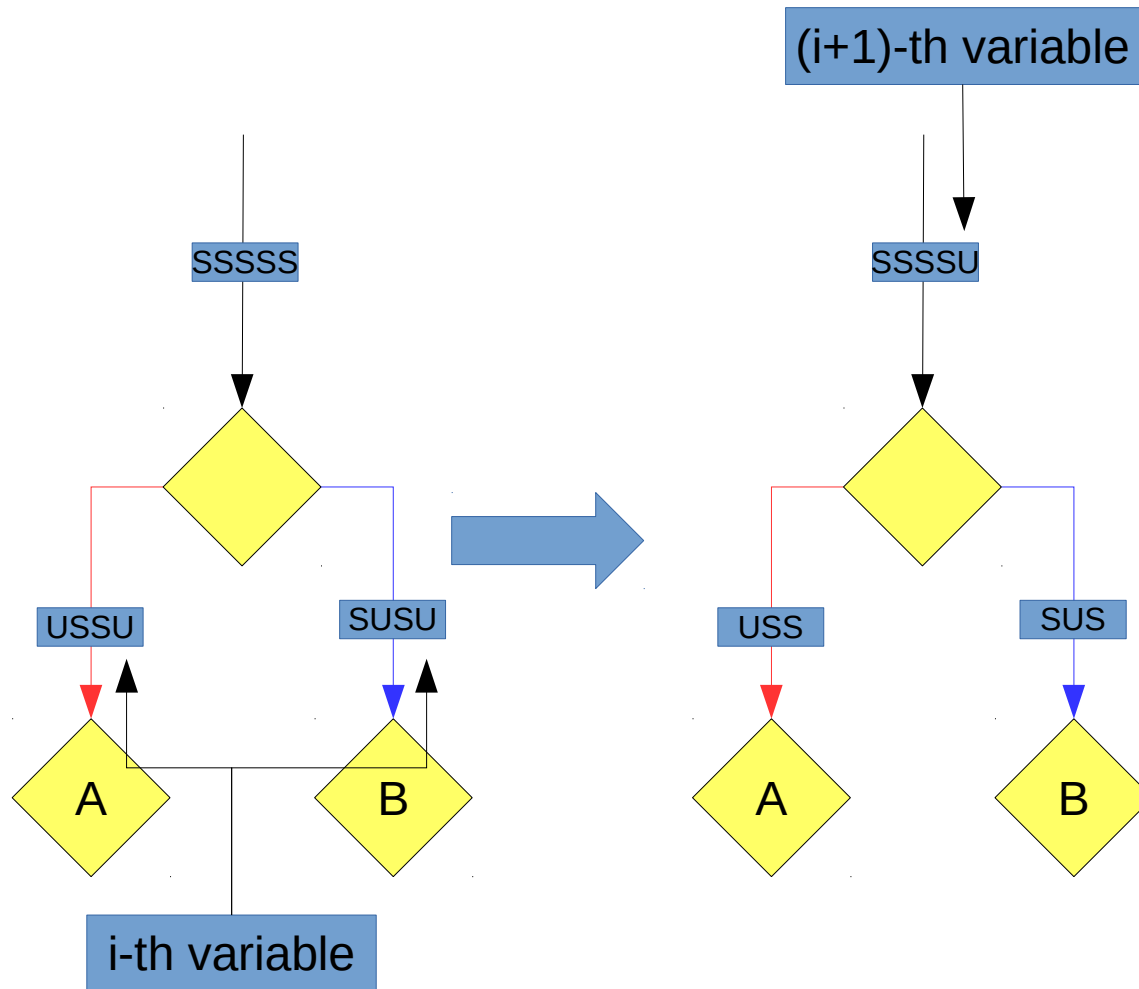
—▶ = if False
—▶ = if True

■ = False
□ = True

S = significant
U = useless

For aesthetic reasons, we do not merge all terminals

“Useless variables extraction” : reduction rule (U2)



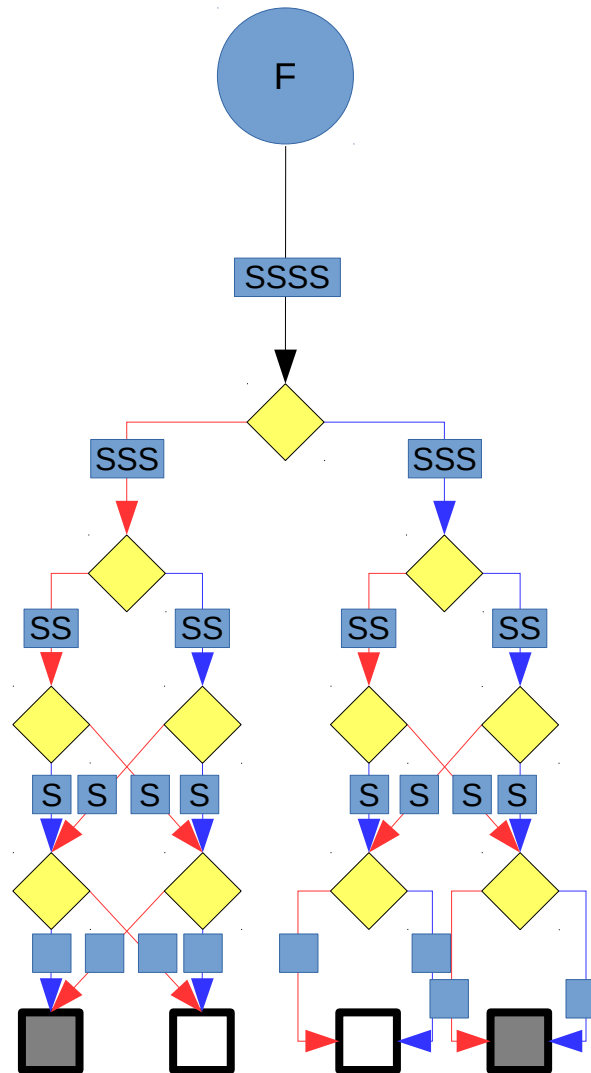
—▶ = if False
—▶ = if True

■ = False
□ = True

S = significant
U = useless

For aesthetic reasons, we do not merge all terminals

Section 1 : model U



→ = if False
→ = if True

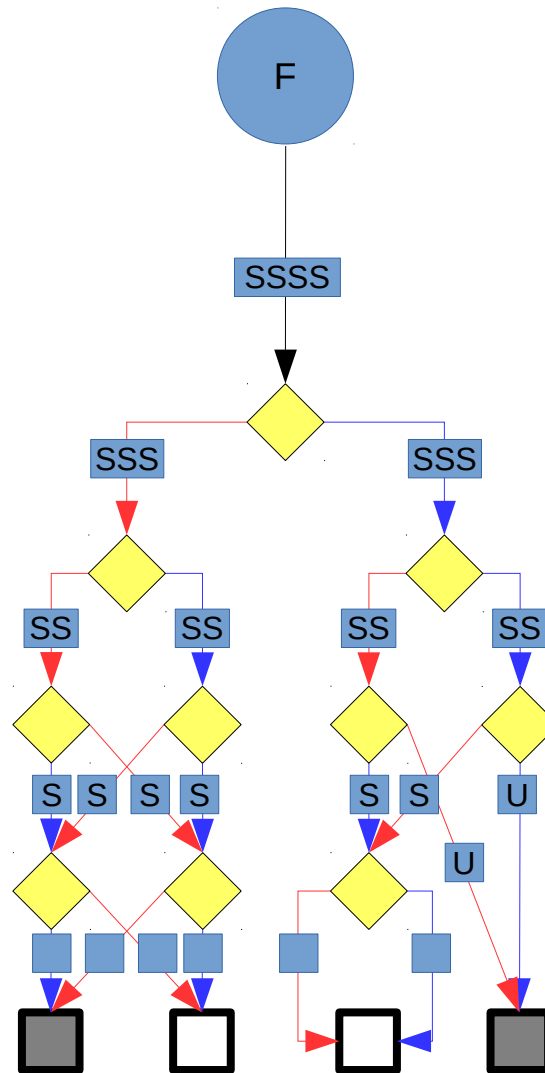
■ = False
□ = True



S = significant
U = useless


Apply U1


For aesthetic reasons, we do not merge all terminals

Section 1 : model U



 = if False
 = if True

 = False

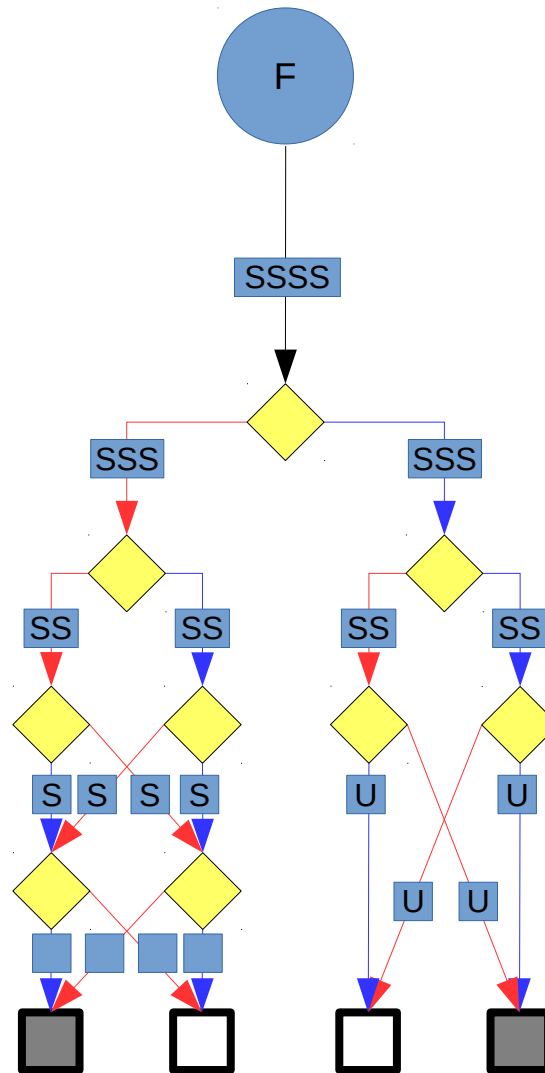
 = True



S = significant
U = useless


Apply U1


For aesthetic reasons, we do not merge all terminals

Section 1 : model U



 = if False
 = if True

 = False

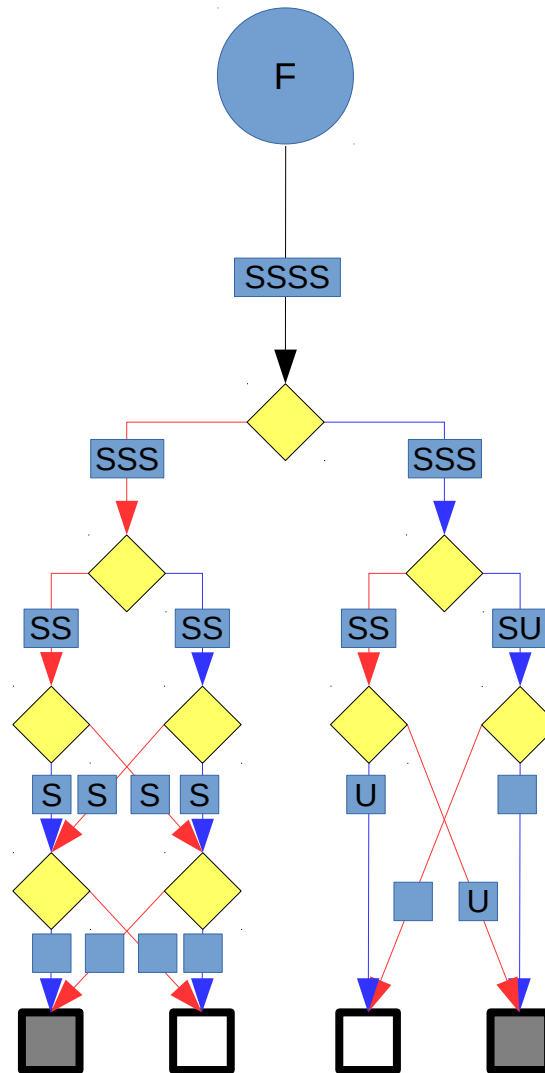
 = True

S = significant
U = useless

Apply U2

For aesthetic reasons, we do not merge all terminals

Section 1 : model U



→ = if False
→ = if True

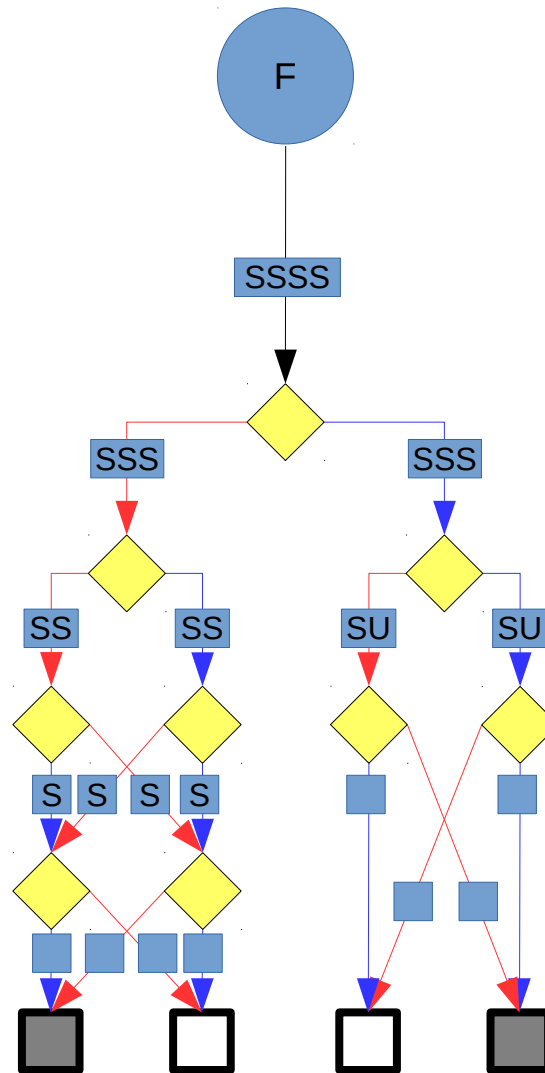
= False
 = True

S = significant
 U = useless

Apply U2

For aesthetic reasons, we do not merge all terminals

Section 1 : model U



→ = if False
→ = if True

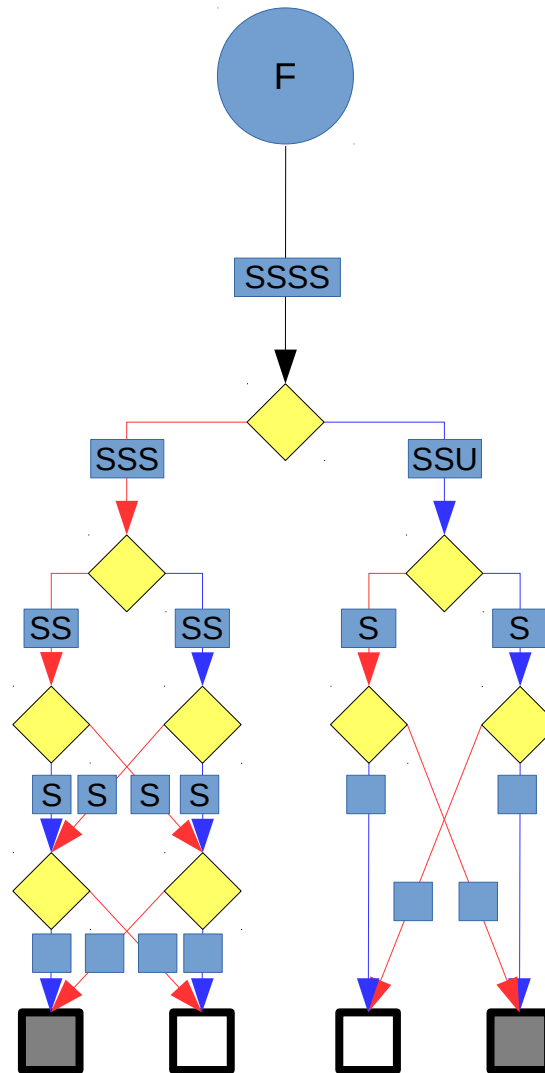
= False
 = True

S = significant
 U = useless

Apply U2

For aesthetic reasons, we do not merge all terminals

Section 1 : model U



→ = if False
→ = if True

= False
 = True

S = significant
 U = useless

For aesthetic reasons, we do not merge all terminals

Section 1 : model U

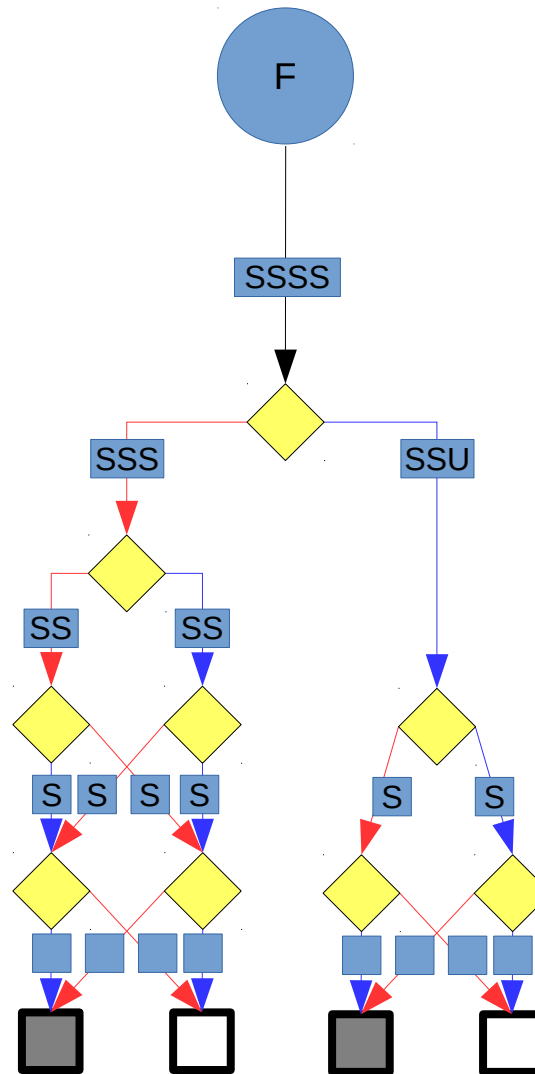
Functions of arity 4

Functions of arity 3

Functions of arity 2

Functions of arity 1

Functions of arity 0



→ = if False
→ = if True

■ = False
□ = True

S = significant
U = useless

Merge

For aesthetic reasons, we do not merge all terminals

Section 1 : model U

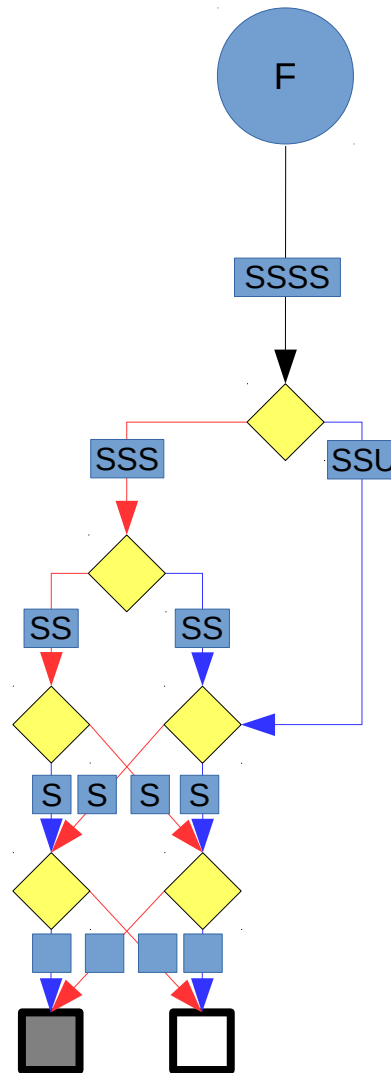
Functions of arity 4

Functions of arity 3

Functions of arity 2

Functions of arity 1

Functions of arity 0



= if False
 = if True

= False
 = True

S = significant
 U = useless

For aesthetic reasons, we do not merge all terminals

Section 1 : model U

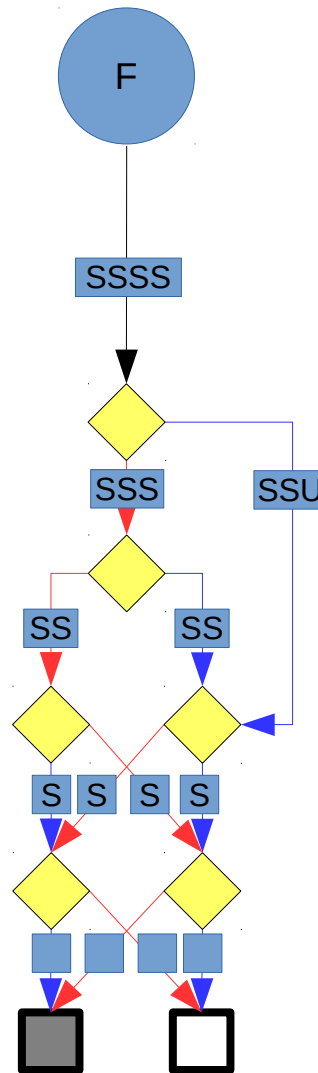
Functions of arity 4

Functions of arity 3

Functions of arity 2

Functions of arity 1

Functions of arity 0



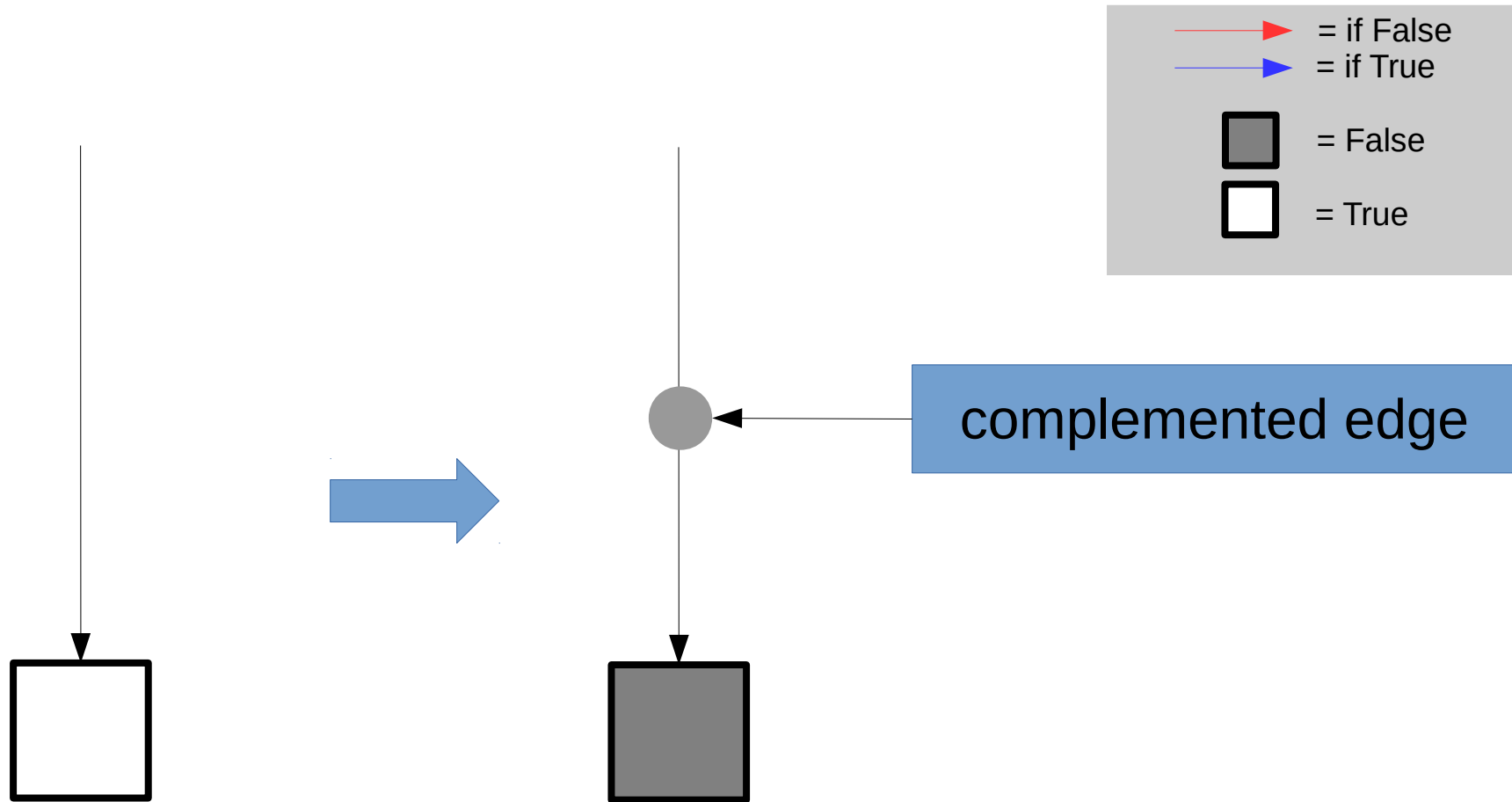
= if False
 = if True

= False
 = True

S = significant
 U = useless

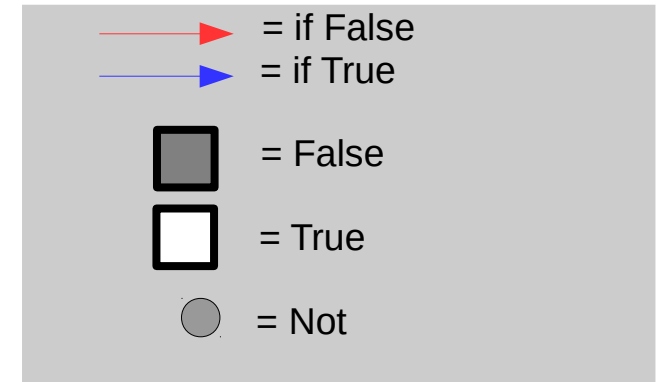
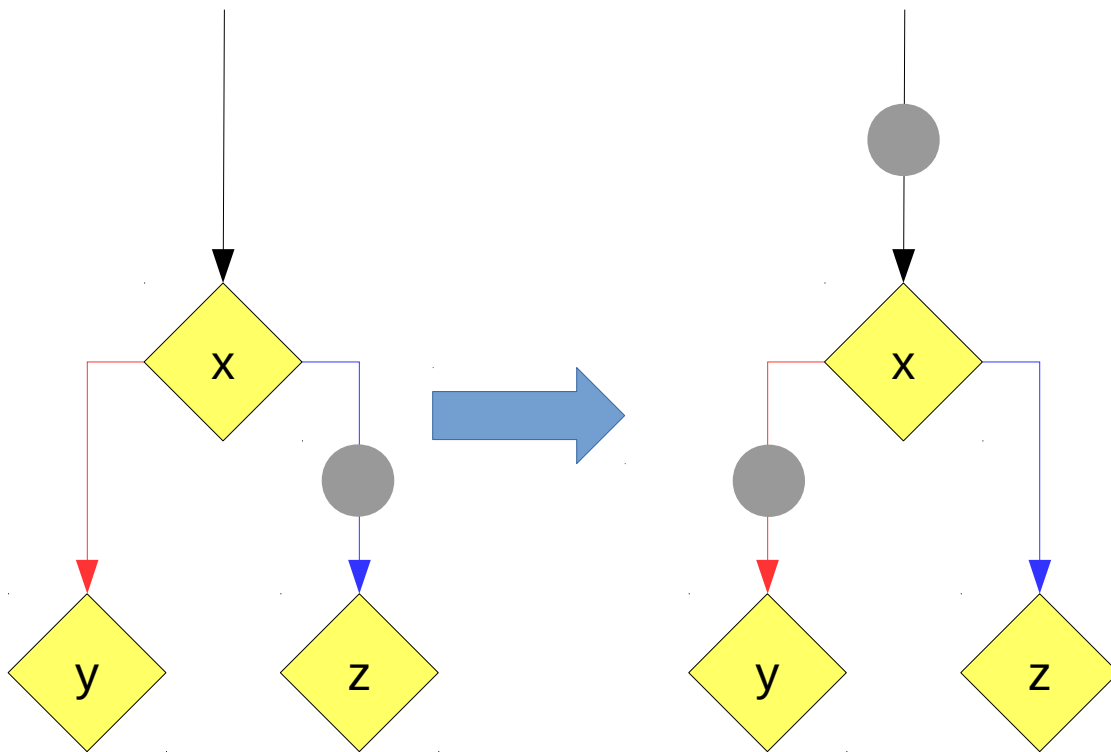
For aesthetic reasons, we do not merge all terminals

“output negation” : reduction rule (N1)



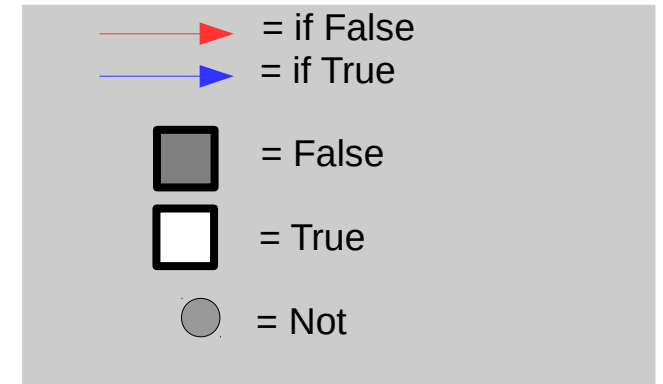
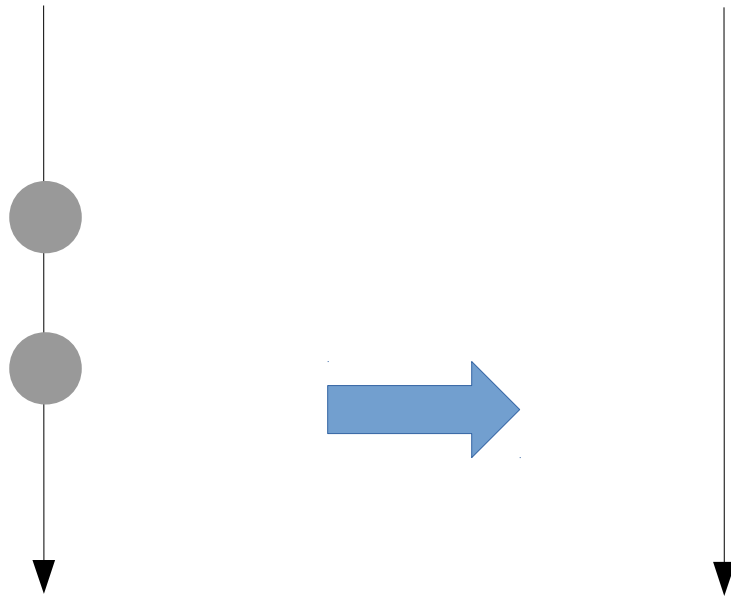
For aesthetic reasons, we do not merge all terminals

“output negation” : reduction rule (N2)



For aesthetic reasons, we do not merge all terminals

“output negation” : reduction rule (N3)



For aesthetic reasons, we do not merge all terminals

Section 2 : model NU

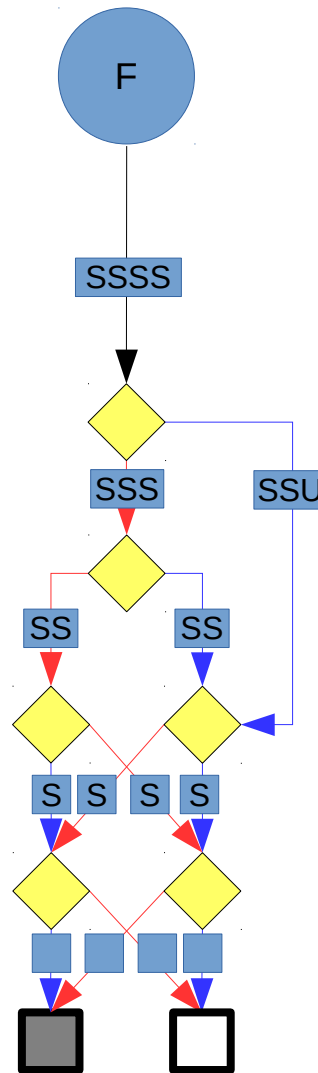
Functions of arity 4

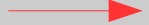
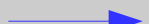
Functions of arity 3



Functions of arity 2


Functions of arity 1

Functions of arity 0



 = if False
 = if True

 = False
 = True

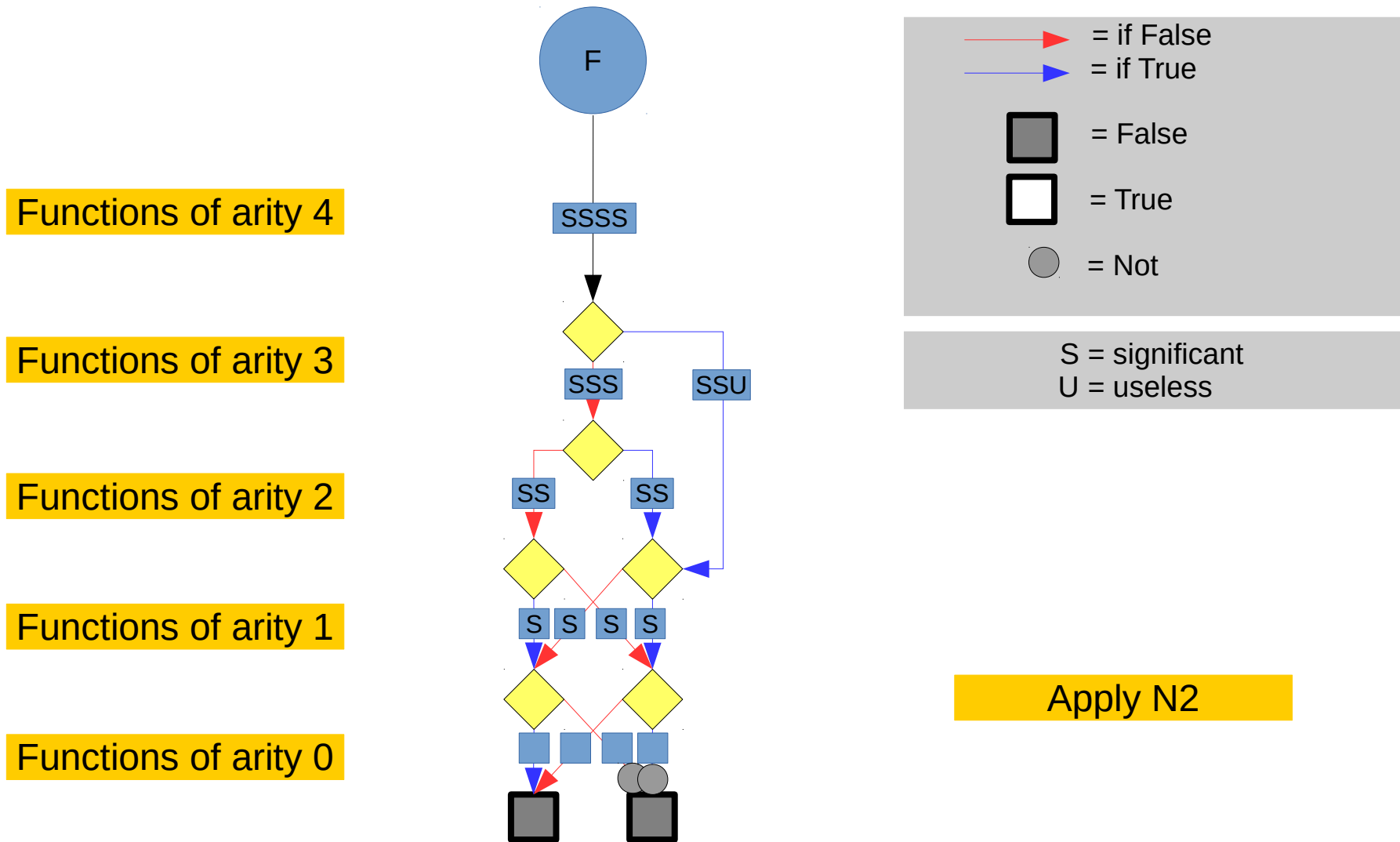
 = Not

S = significant
 U = useless

Apply N1

For aesthetic reasons, we do not merge all terminals

Section 2 : model NU



For aesthetic reasons, we do not merge all terminals

Section 2 : model NU

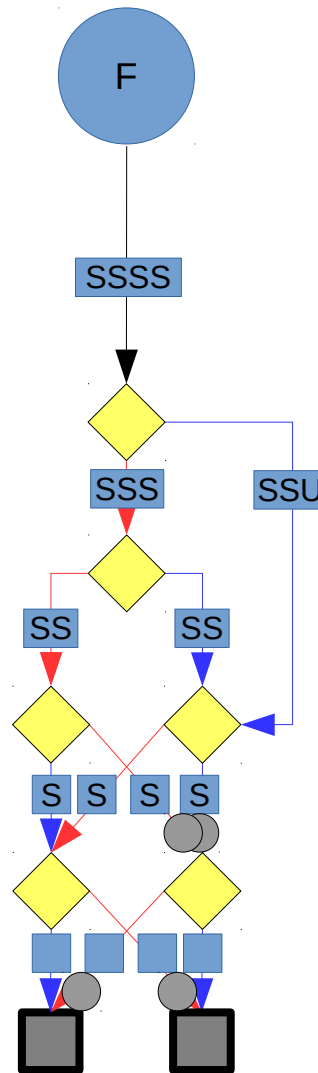
Functions of arity 4

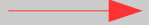
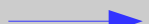
Functions of arity 3



Functions of arity 2


Functions of arity 1

Functions of arity 0



 = if False
 = if True

 = False
 = True

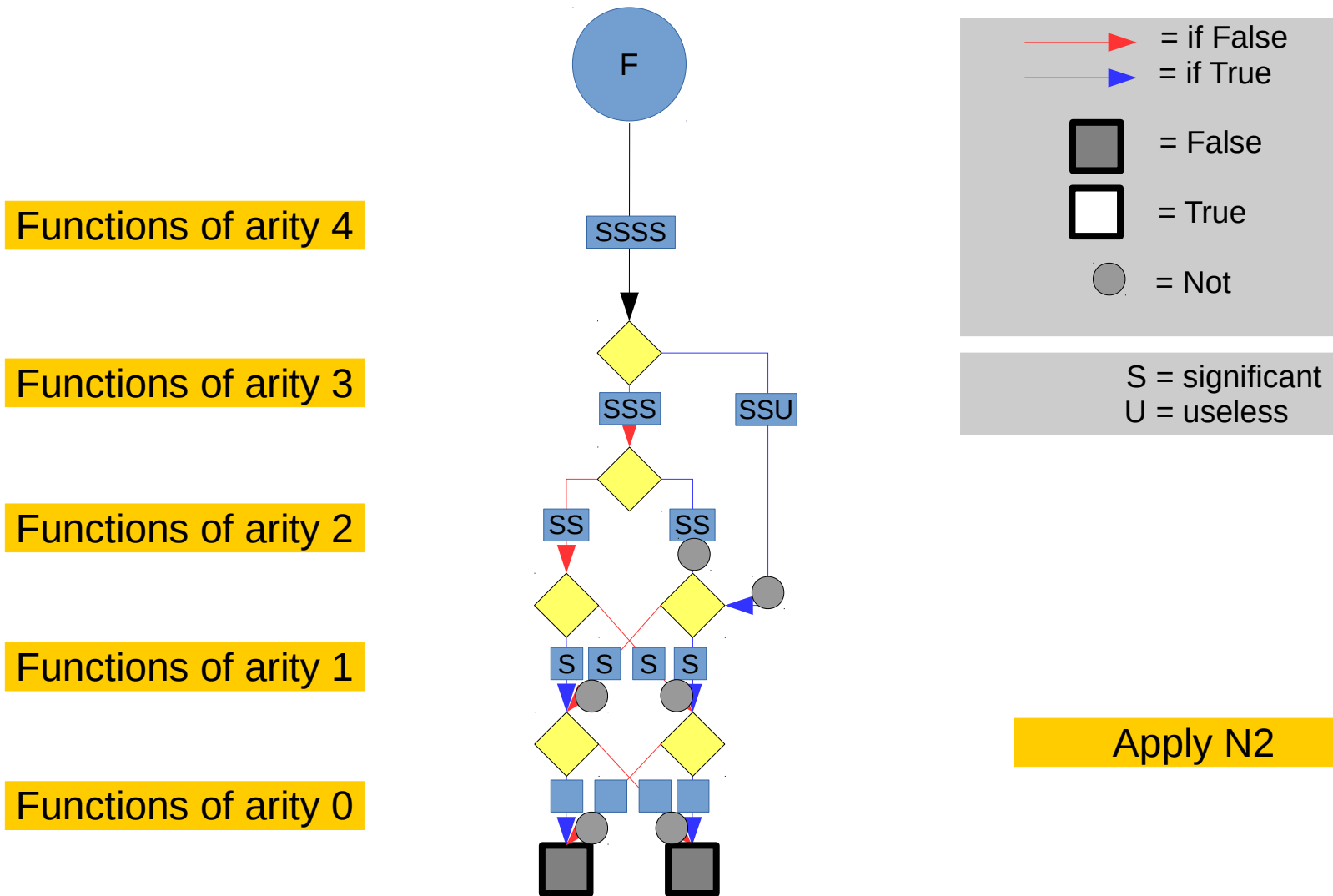
 = Not

S = significant
 U = useless

Apply N2

For aesthetic reasons, we do not merge all terminals

Section 2 : model NU



Apply N2

For aesthetic reasons, we do not merge all terminals

Section 2 : model NU

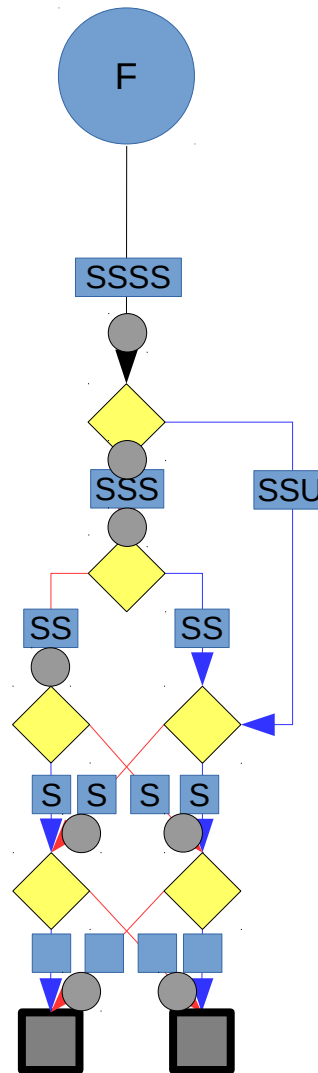
Functions of arity 4

Functions of arity 3

Functions of arity 2

Functions of arity 1

Functions of arity 0



→ = if False
→ = if True

■ = False
□ = True

○ = Not

S = significant
U = useless

Apply N3

For aesthetic reasons, we do not merge all terminals

Section 2 : model NU

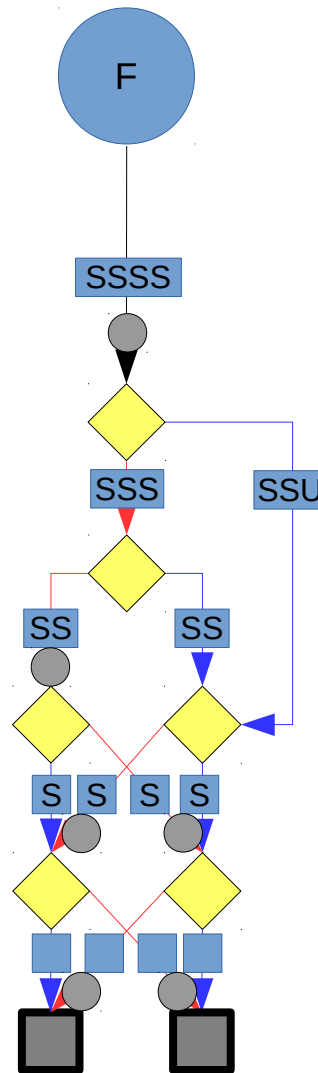
Functions of arity 4

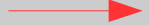
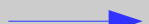
Functions of arity 3



Functions of arity 2


Functions of arity 1

Functions of arity 0



 = if False
 = if True

 = False
 = True

 = Not

S = significant
 U = useless

Merge

For aesthetic reasons, we do not merge all terminals

Section 2 : model NU

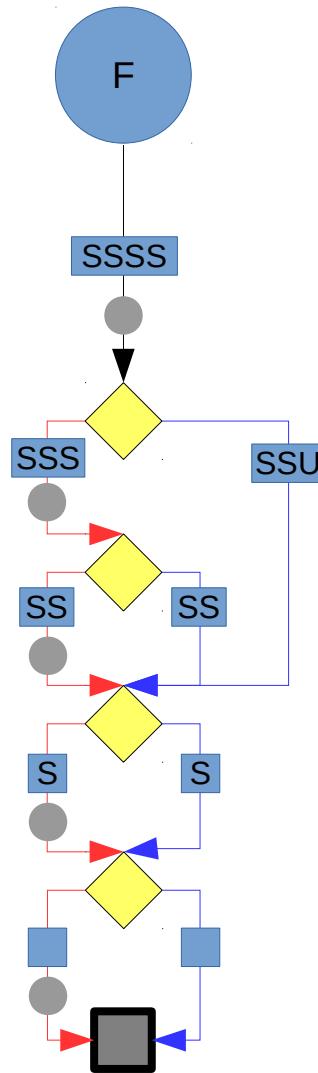
Functions of arity 4

Functions of arity 3

Functions of arity 2

Functions of arity 1

Functions of arity 0



→ = if False
→ = if True

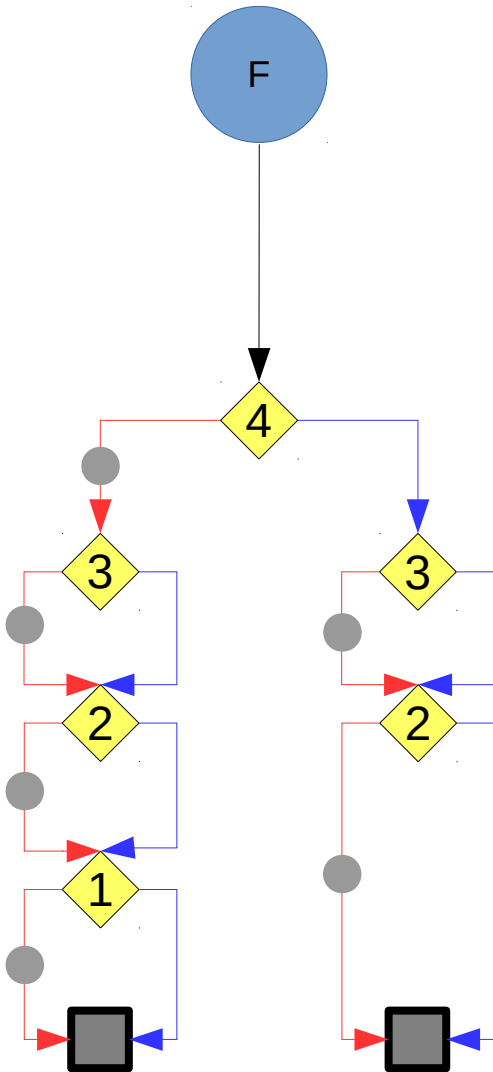
■ = False
□ = True

● = Not

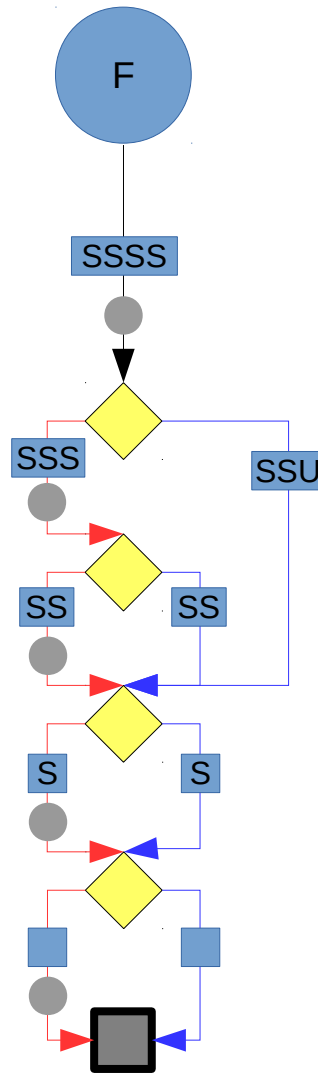
S = significant
U = useless

For aesthetic reasons, we do not merge all terminals

N vs NU



N =
ROBDD + "output negation"



Model NU

→ = if False
→ = if True

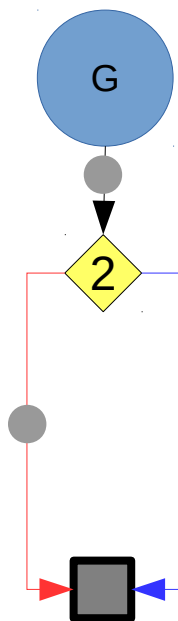
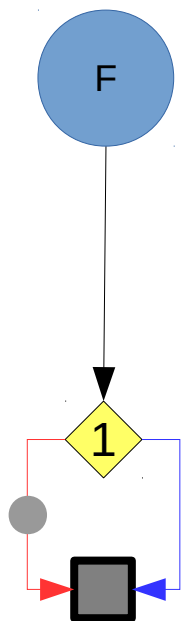
■ = False

□ = True

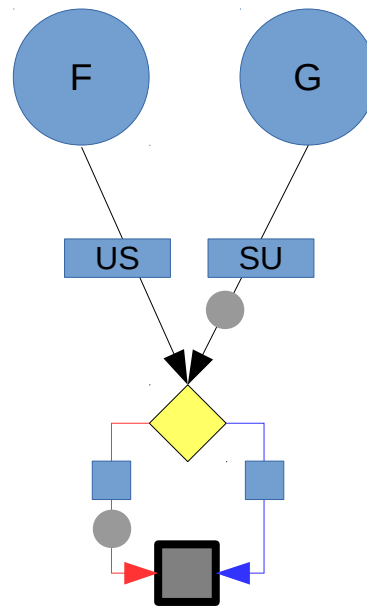
● = Not

S = significant
U = useless

N vs NU: Example 2



N =
ROBDD + "output negation"



Model NU

→ = if False
→ = if True

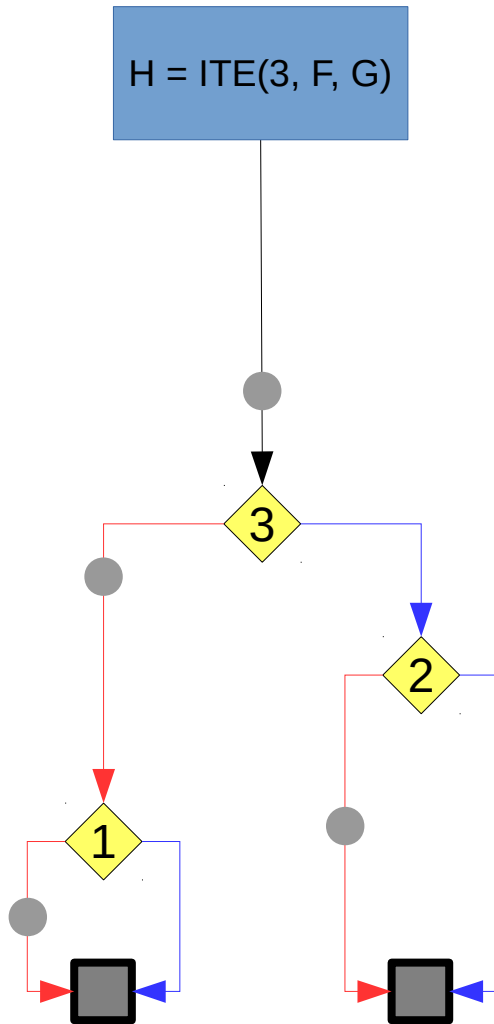
■ = False

□ = True

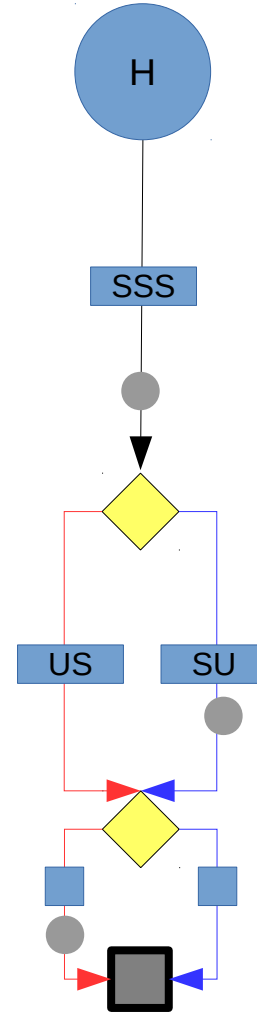
● = Not

S = significant
U = useless

N vs NU: Example 3



N =
ROBDD + "output negation"



Model NU

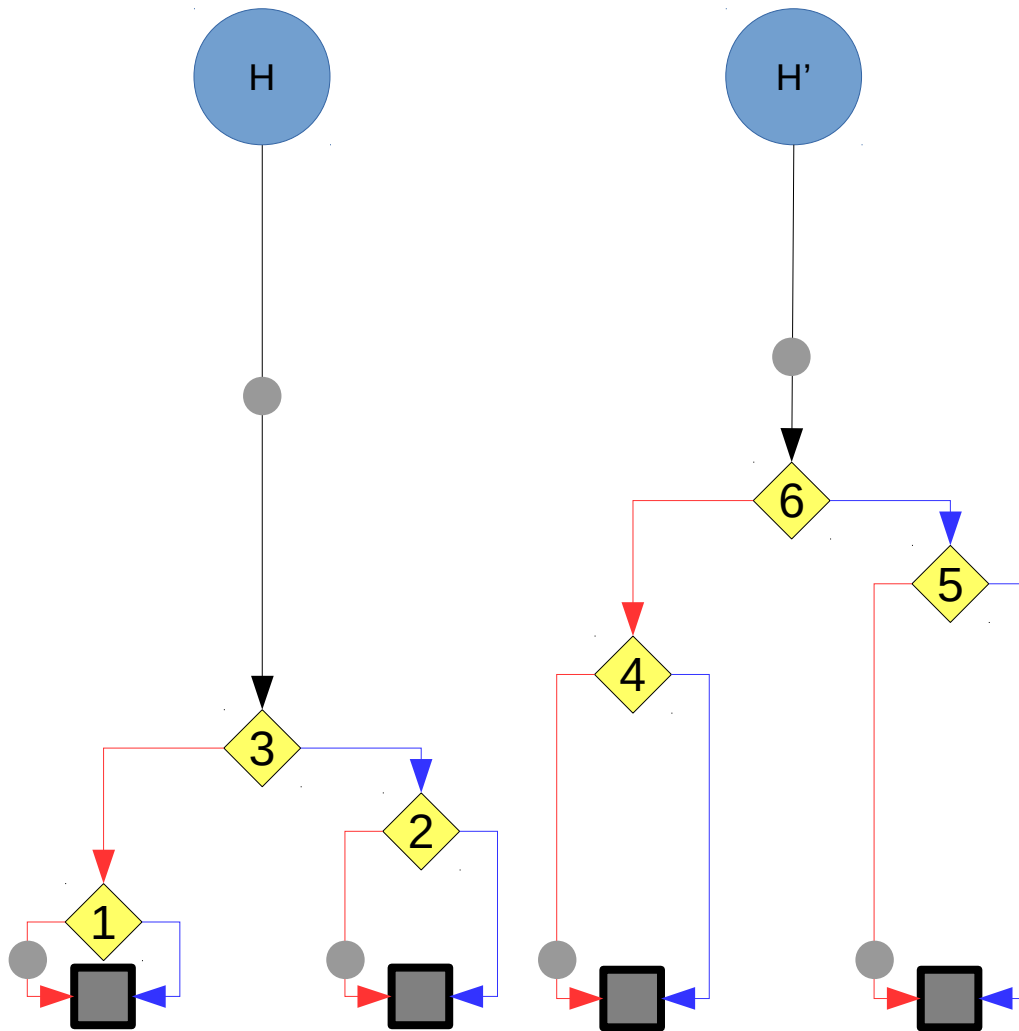
→ = if False
→ = if True

■ = False
□ = True

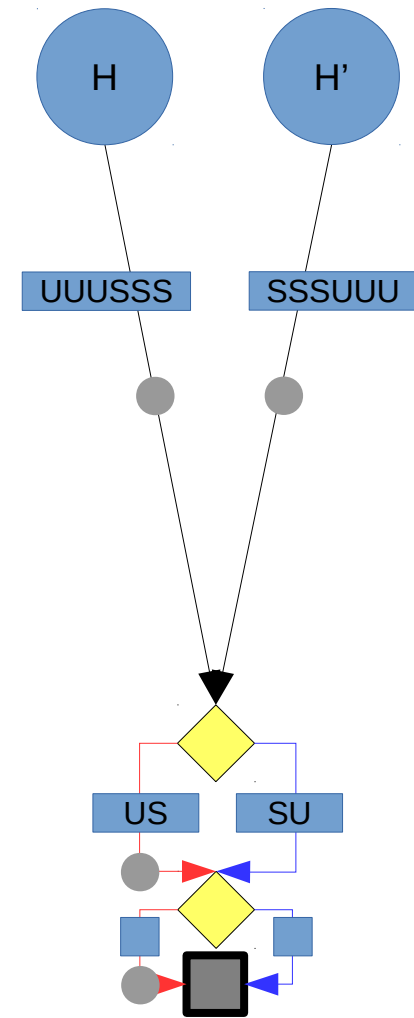
● = Not

S = significant
U = useless

N vs NU: Example 4



N =
ROBDD + "output negation"



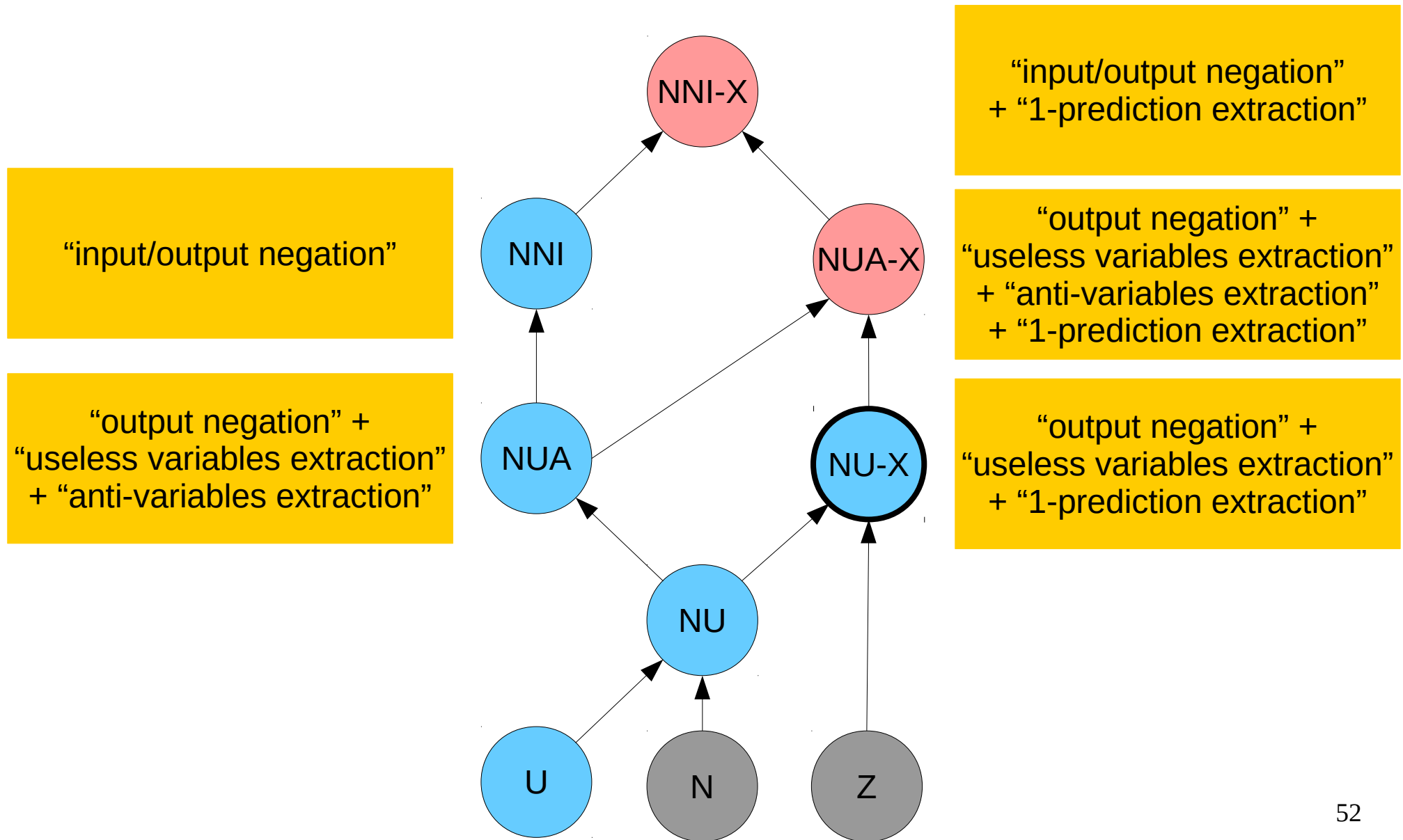
Model NU

Results

Average reduction of the {number of nodes / **estimated** memory cost} on four benchmarks

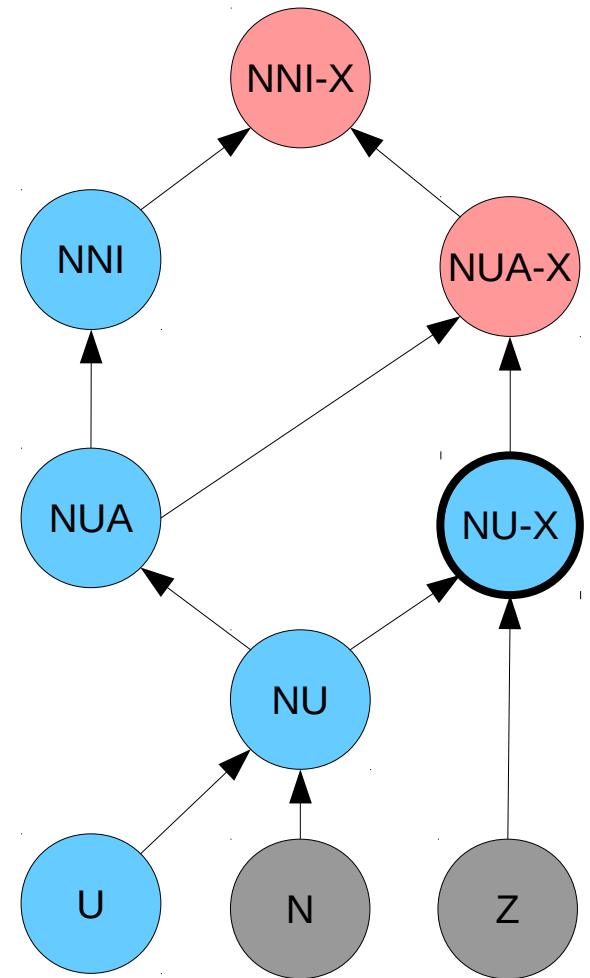
	Circuits (dense functions)				CNF formulas (sparse functions)			
	lgsynth91		iscas99		uf20-91		uf50-218	
variants	#node	mem	#node	mem	#node	mem	#node	mem
NU	-26%	-21%	-25%	-20%	-3%	<u>+7%</u>	-3%	<u>+22%</u>

Can we go further ?



Conclusion

- Software implemented in OCaml:
 - <https://github.com/JoanThibault/DAGaml/tree/grobdd-dev>
 - ~ 12 000 lines of OCaml
- Fewer nodes & Less memory
- Future Work
 - Quantify the dependency between variables' order and #node
 - Solve & Implement NUA-X and NNI-X versions
- TO DO
 - Quantification Operators
 - Variable Reordering
 - Parallelism & hardware acceleration
- Other Applications
 - Apply similar strategies to compress other DAG
 - DAG / Graph isomorphism

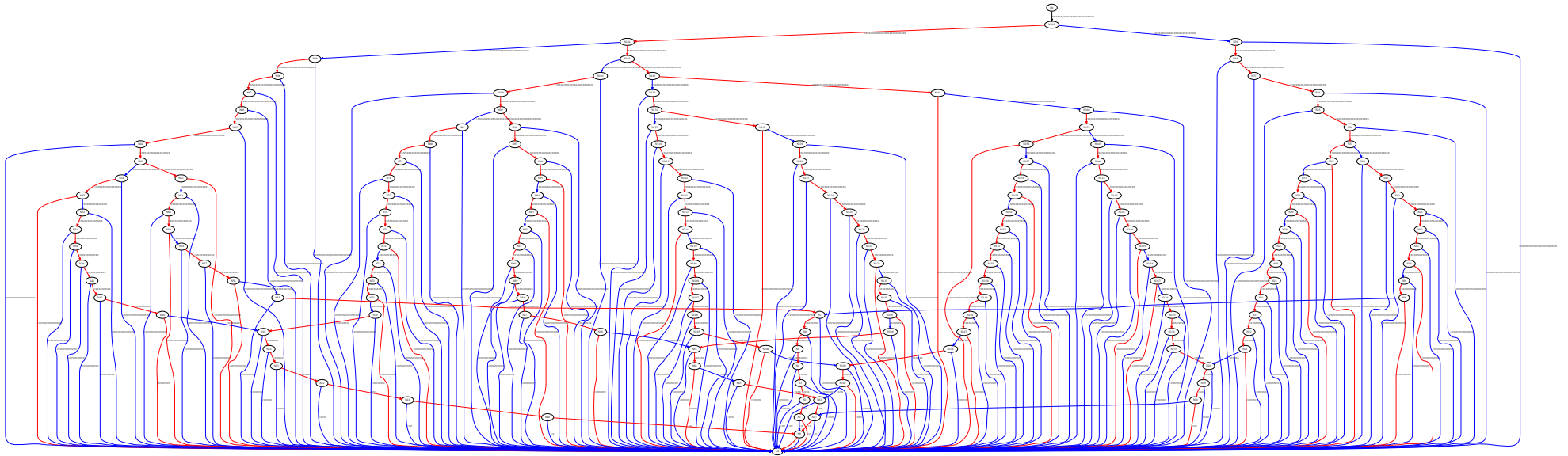


Results

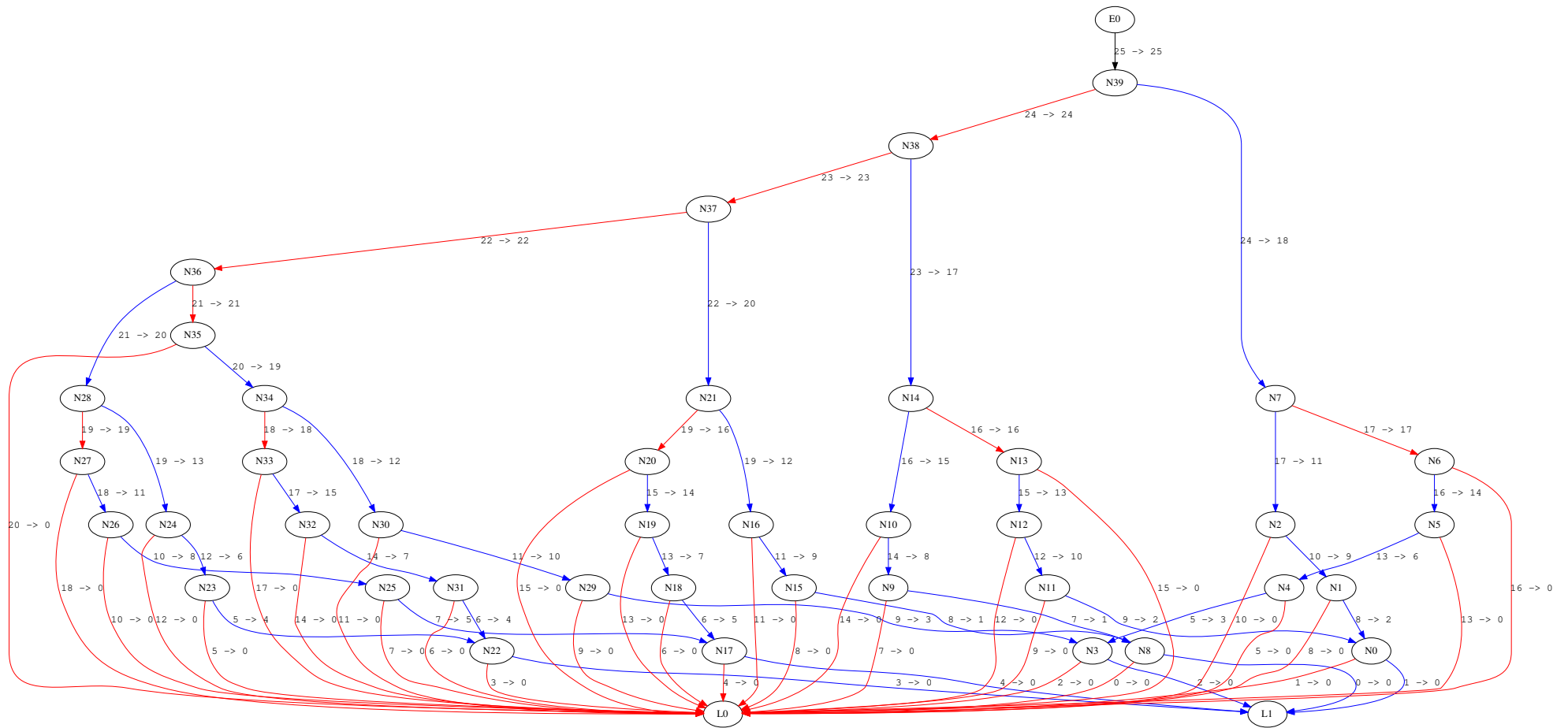
Average reduction of the {number of nodes / **estimated** memory cost} on four benchmarks

	Circuits (dense functions)				CNF formulas (sparse functions)			
	lgsynth91		iscas99		uf20-91		uf50-218	
variants	#node	mem	#node	mem	#node	mem	#node	mem
Z	<u>+233%</u>	<u>+233%</u>	<u>+162%</u>	<u>+162%</u>	-41%	-41%	-42%	-42%
NU	-26%	-21%	-25%	-20%	-3%	<u>+7%</u>	-3%	<u>+22%</u>
NNI	-60%	-53%	-56%	-49%	-30%	-10%	-39%	<u>+5%</u>
NU-X	-64%	-58%	-55%	-46%	-96%	-95%	-97%	-96%

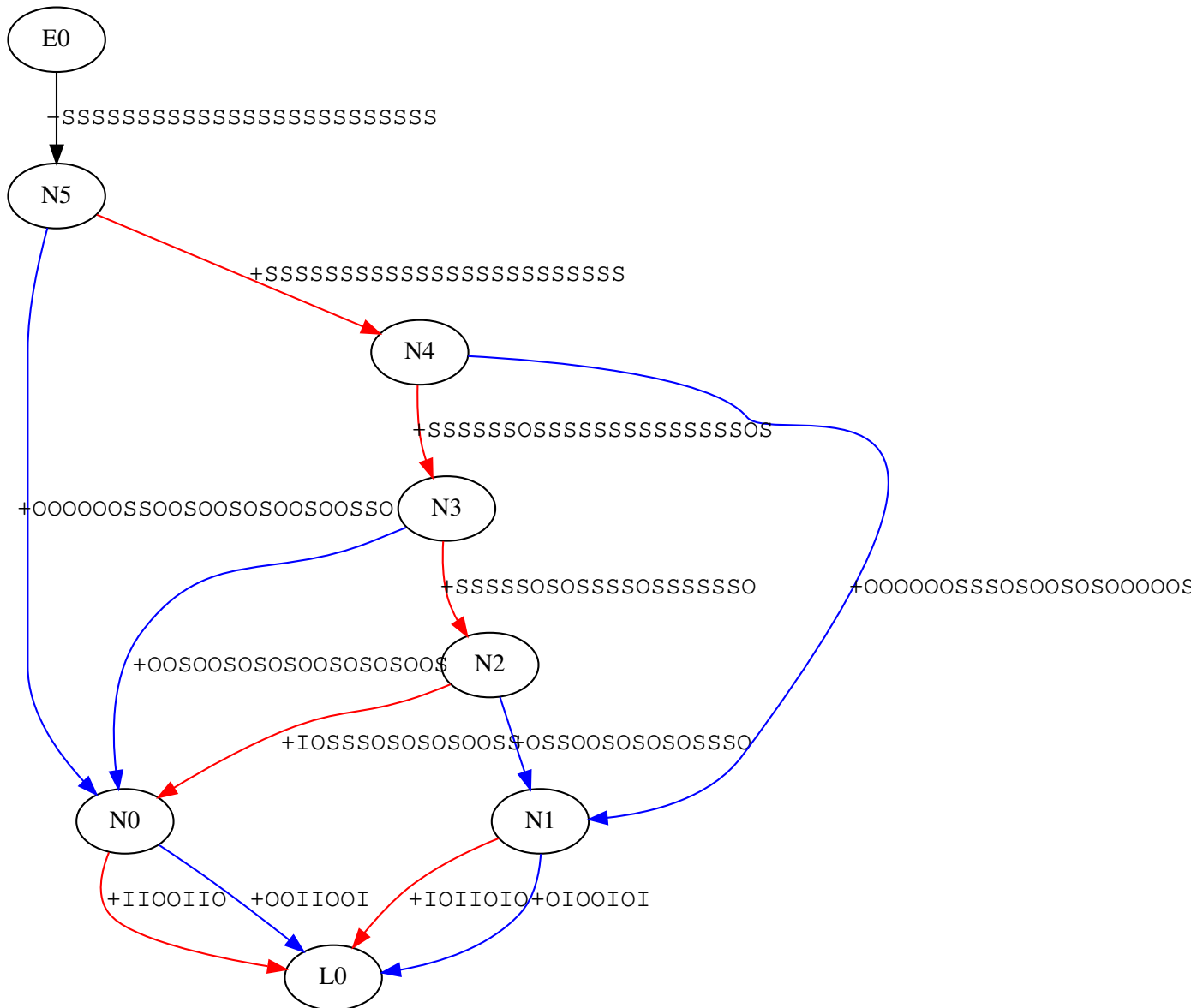
5-Queens: N or NU



5-Queens: Z



5-Queens: NU-X



5-Queens: NNI

