

Project Contributions:

1. Mohamad Olid Afzal

ID- 2011831042

Work: 20% (4bit Magnitude Comparator, integration of pattern checker)

2. Ratul Dey

ID- 2014323642

Work: 15% (Decimal to BCD encoder and right shift register)

3. Mohamad Ashraful Islam Chowdhury

ID- 2014318642

Work: 15% (7 Segment Display)

4. Ferdous Reza Niloy

ID- 2021281642

Work: 30% (Main circuit, pattern checker, clock multiplier, Enabler, 4 bit memory block)

5. Ratul Bhattacharjee

ID- 2012996642

Work: 20% (Decimal to excess-1, BCD to excess-11 , 7 segment display)