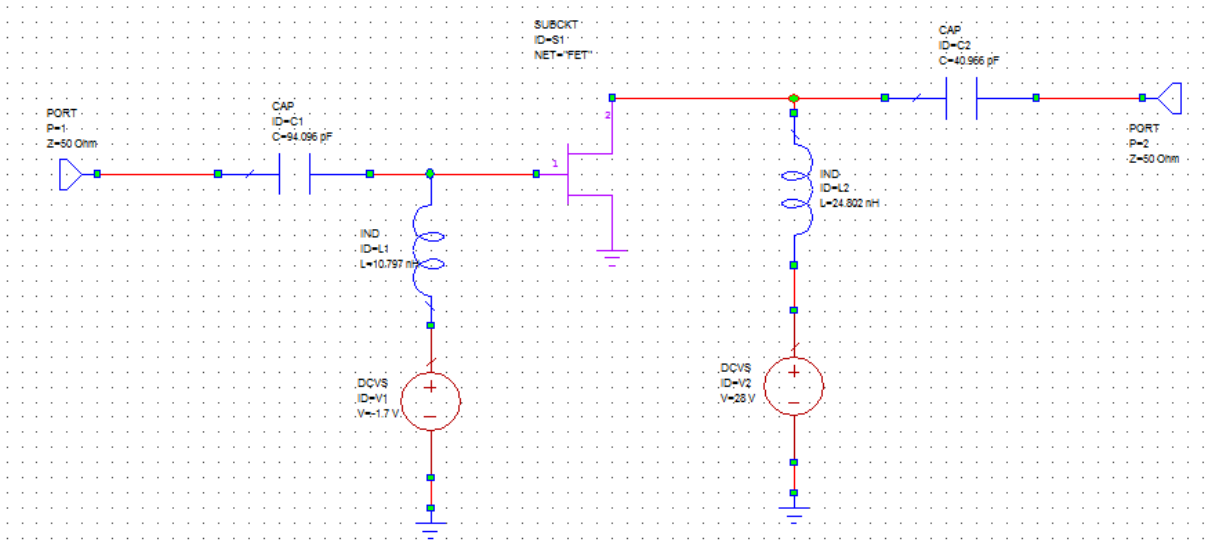
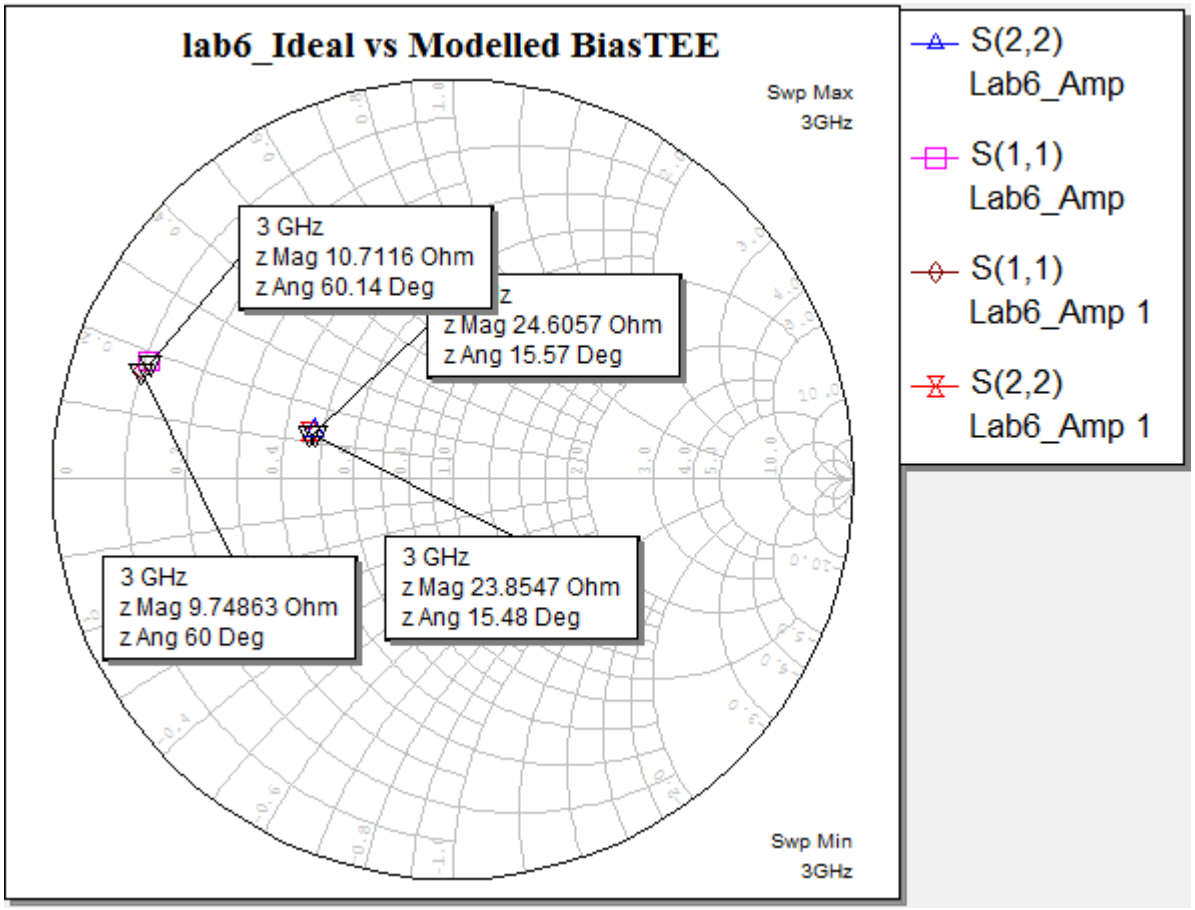
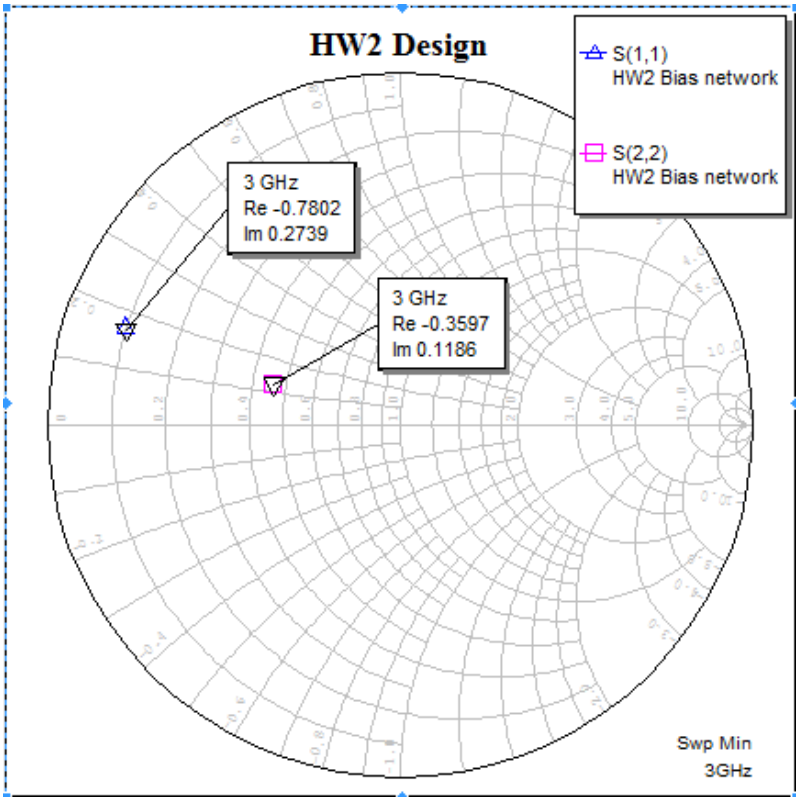
Biased network



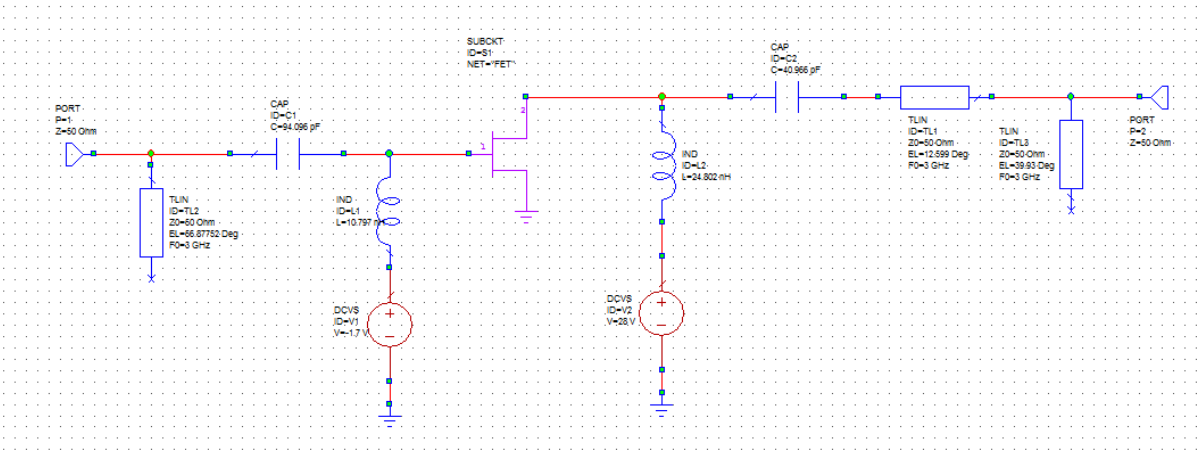


We see that our circuit is biased correctly.

Matched and Biased



Frequency (GHz)	|S(2,1)| HW2 Bias network	Ang(S(2,1)) (Deg) HW2 Bias network
3	4.8216	13.024



DB(GT()) (GHz) HW2 Matched Amplifier Frequency	DB(GT()) HW2 Matched Amplifier
3	16.043

We see that our final gain is 16.043dB which is very close to our desired value of 16 so I'm happy my design satisfies the solution.