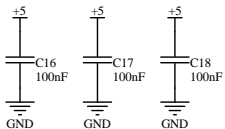
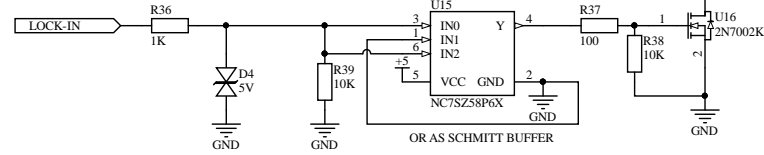


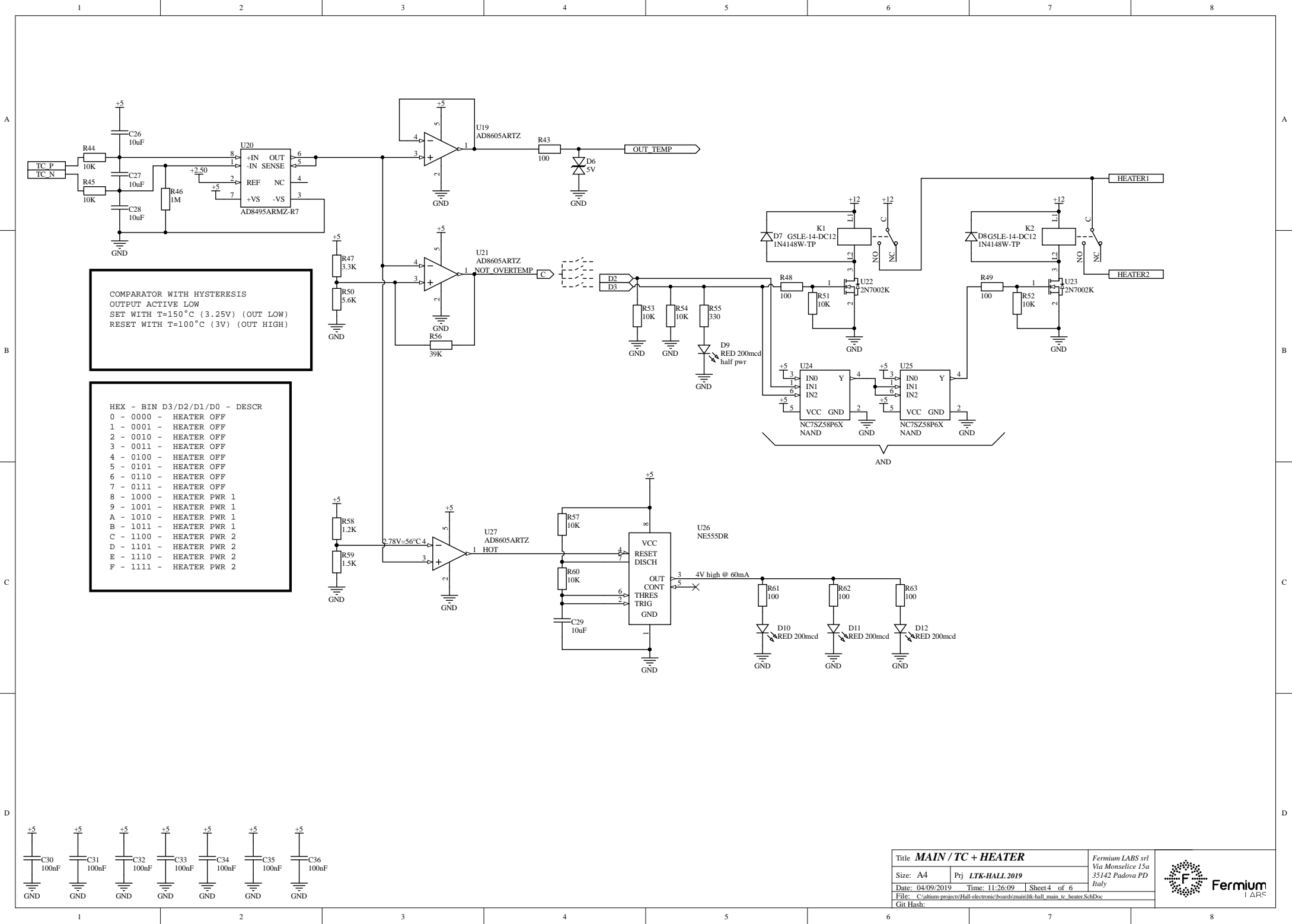
CONNECTOR	PIN	HALL	RDT
1	RED	VH	R1 (VH1)
1	GREEN	VR	R2 (VH2)
1	BLUE	TEMP	TEMP
1	WHITE	CURRENT	CURRENT
1	BLACK	GAUSS	R3 (VR)
2	RED	2.50V	2.50V
2	GREEN	VH1	NC
2	BLUE	VH2	NC
2	WHITE	LOCK-IN	LOCK-IN
2	BLACK	5V	5V





Title MAIN / CURRENT GENERATION			<i>Fermium LABS srl</i> <i>Via Monselice 15a</i> <i>35142 Padova PD</i> <i>Italy</i>
Size: A4	Prj LTk-HALL 2019		
Date: 04/09/2019	Time: 11:26:09	Sheet 3 of 6	
File: <i>C:\alium-projets\Hall-electronic\boards\main\lkt-hall_main_igen.SchDoc</i> Git Hash: <i>b1fe1c950d59a97f8f5889da5a33d474412410b2b7</i>			





COMPARATOR WITH HYSTERESIS
OUTPUT ACTIVE LOW
SET WITH T=150°C (3.25V) (OUT LOW)
RESET WITH T=100°C (3V) (OUT HIGH)

HEX	-	BIN	D3/D2/D1/D0	-	DESCR
0	-	0000	-	-	HEATER OFF
1	-	0001	-	-	HEATER OFF
2	-	0010	-	-	HEATER OFF
3	-	0011	-	-	HEATER OFF
4	-	0100	-	-	HEATER OFF
5	-	0101	-	-	HEATER OFF
6	-	0110	-	-	HEATER OFF
7	-	0111	-	-	HEATER OFF
8	-	1000	-	-	HEATER PWR 1
9	-	1001	-	-	HEATER PWR 1
A	-	1010	-	-	HEATER PWR 1
B	-	1011	-	-	HEATER PWR 1
C	-	1100	-	-	HEATER PWR 2
D	-	1101	-	-	HEATER PWR 2
E	-	1110	-	-	HEATER PWR 2
F	-	1111	-	-	HEATER PWR 2

