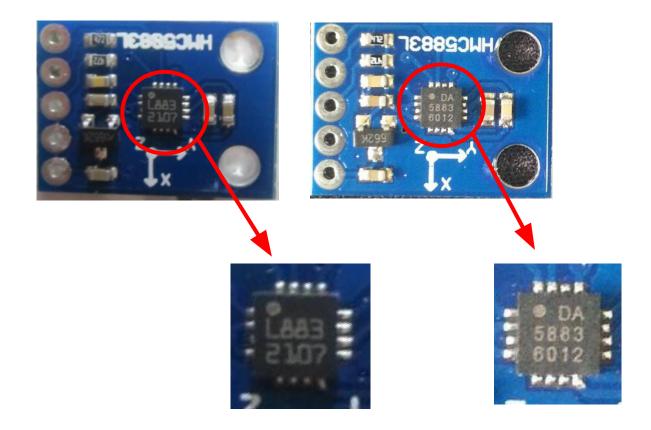
## **CI/N** Magnetómetros



Si bien los módulos tienen la misma serigrafía "HCM5883L" hay variantes del integrado tal como se puede apreciar. A la izquierda, el denominado L883 de honeywell (HMC5883L) y a la derecha, el DA5883, un integrado alternativo (QMC5883L).



La diferencia fundamental es en los registros internos.

| Address Location | Name                       | Access<br>Read/Write |  |  |
|------------------|----------------------------|----------------------|--|--|
| 00               | Configuration Register A   |                      |  |  |
| 01               | Configuration Register B   | Read/Write           |  |  |
| 02               | Mode Register              | Read/Write           |  |  |
| 03               | Data Output X MSB Register | Read                 |  |  |
| 04               | Data Output X LSB Register | Read                 |  |  |
| 05               | Data Output Z MSB Register | Read                 |  |  |
| 06               | Data Output Z LSB Register | Read                 |  |  |
| 07               | Data Output Y MSB Register | Read                 |  |  |
| 08               | Data Output Y LSB Register | Read                 |  |  |
| 09               | Status Register            | Read                 |  |  |
| 10               | Identification Register A  | Read                 |  |  |
| 11               | Identification Register B  | Read                 |  |  |
| 12               | Identification Register C  |                      |  |  |

HMC5883L

| Addu | 7                                     | 6           | 5        | 4 | 2          | 2   | 1    | 0           | Access                         |  |
|------|---------------------------------------|-------------|----------|---|------------|-----|------|-------------|--------------------------------|--|
| 00H  | Data Output X LSB Register XOUT[7:0]  |             |          |   |            |     |      |             |                                |  |
| 01H  | Data Output X MSB Register XOUT[15:8] |             |          |   |            |     |      |             |                                |  |
| 02H  | Data Output Y LSB Register YOUT[7:0]  |             |          |   |            |     |      |             |                                |  |
| 03H  | Data Output Y MSB Register YOUT[15:8] |             |          |   |            |     |      |             |                                |  |
| 04H  | Data Output Z LSB Register ZOUT[7:0]  |             |          |   |            |     |      |             |                                |  |
| 05H  | Data Output Z MSB Register ZOUT[15:8] |             |          |   |            |     |      |             | Read only                      |  |
| 06H  |                                       |             |          |   |            | DOR | OVL  | DRDY        | Read only                      |  |
| 07H  | TOUT[7:0]                             |             |          |   |            |     |      |             |                                |  |
| 08H  | TOUT[15:8]                            |             |          |   |            |     |      |             |                                |  |
| 09H  | OSR[1:0]                              |             | RNG[1:0] |   | ODR[1:0] N |     | MODE | [1:0]       | Read/Write                     |  |
| 0AH  | SOFT_<br>RST                          | ROL_P<br>NT |          |   |            |     |      | INT_E<br>NB | R/W, Read<br>only on<br>blanks |  |
| 0BH  | SET/RESET Period FBR [7:0]            |             |          |   |            |     |      |             |                                |  |
| 0CH  | Reserved                              |             |          |   |            |     |      |             |                                |  |
| 0DH  | Chip ID                               |             |          |   |            |     |      |             |                                |  |

QMC5883L