LINUXPRUEBA

Intel Core i5-8350U testing with a LENOVO 20L7002CUS (N22ET82W 1.59 BIOS) and Intel UHD 620 on Arch Linux via the Phoronix Test Suite.

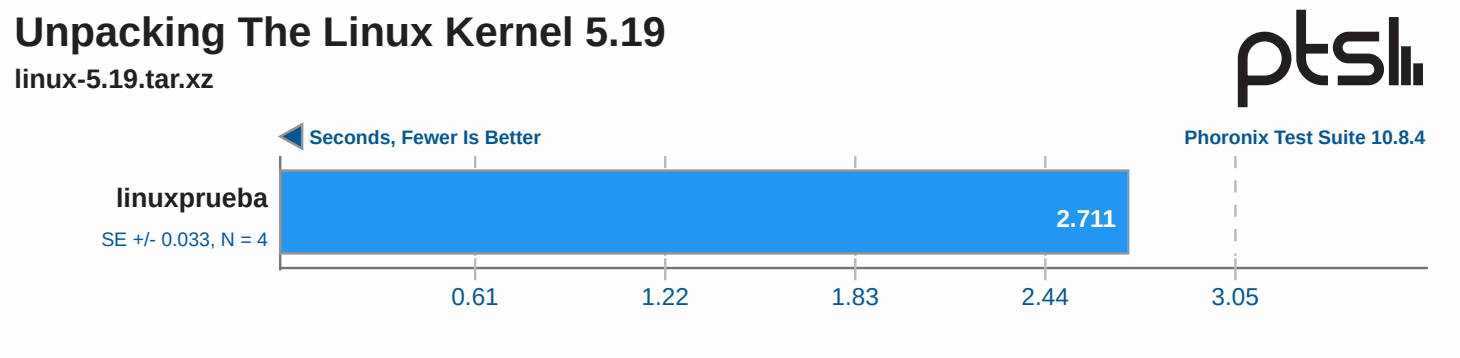
linuxprueba

Processor: Intel Core i5-8350U @ 3.60GHz (4 Cores / 8 Threads), **Motherboard:** LENOVO 20L7002CUS (N22ET82W 1.59 BIOS), **Chipset:** Intel Xeon E3-1200 v6/7th, **Memory:** 16GB, **Disk:** SK hynix PC611 NVMe 512GB + 0GB SD/MMC, **Graphics:** Intel UHD 620, **Audio:** Realtek ALC257, **Network:** Intel I219-LM + Intel 8265 / 8275

OS: Arch Linux, **Kernel:** 6.16.1-arch1-1 (x86_64), Display **Server:** X Server 1.21.1.18, **Compiler:** GCC 15.2.1 20250813, **File-System:** ext4, Screen **Resolution:** 1920x1080

Kernel Notes: *Transparent Huge Pages: always*
Disk Notes: *NONE / relatime,rw / Block Size: 4096*
Processor Notes: *Scaling Governor: intel_pstate powersave (EPP: balance_performance) - CPU Microcode: 0xf6*
Security Notes: *gather_data_sampling: Mitigation of Microcode + ghostwrite: Not affected + indirect_target_selection: Not affected + itlb_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT vulnerable + mds: Mitigation of Clear buffers; SMT vulnerable + meltdown: Mitigation of PTI + mmio_stale_data: Mitigation of Clear buffers; SMT vulnerable + old_microcode: Not affected + reg_file_data_sampling: Not affected + retbleed: Mitigation of IBRS + spec_rstack_overflow: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of IBRS; IBPB: conditional; STIBP: conditional; RSB filling; PBRSB-eIBRS: Not affected; BHI: Not affected + srbds: Mitigation of Microcode + tsa: Not affected + tsx_async_abort: Mitigation of TSX disabled*

Unpacking The Linux Kernel



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Testing initiated at 22 August 2025 22:40 by user Fernando.

Phoronix Test Suite 10.8.4 - Generated 22 August 2025 22:41:05