

BLOGPRUEBA

Intel Core i5-8350U testing with a LENOVO 20L7002CUS (N22ET82W 1.59 BIOS) and Intel UHD 620 on Arch Linux via the Phoronix Test Suite.

blogprueba

Processor: Intel Core i5-8350U @ 3.60GHz (4 Cores / 8 Threads), **Motherboard:** LENOVO 20L7002CUS (N22ET82W 1.59 BIOS), **Chipset:** Intel Xeon E3-1200 v6/7th, **Memory:** 16GB, **Disk:** SK hynix PC611 NVMe 512GB + 0GB SD/MMC, **Graphics:** Intel UHD 620, **Audio:** Realtek ALC257, **Network:** Intel I219-LM + Intel 8265 / 8275

OS: Arch Linux, **Kernel:** 6.16.1-arch1-1 (x86_64), **Display Server:** X Server 1.21.1.18, **Compiler:** GCC 15.2.1 20250813, **File-System:** ext4, **Screen Resolution:** 1920x1080

Kernel Notes: Transparent Huge Pages: always

Compiler Notes: `--disable-libssp --disable-libstdcxx-pch --disable-werror --enable-__cxa_atexit --enable-bootstrap --enable-cet=auto --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-default-ssp --enable-gnu-indirect-function --enable-gnu-unique-object --enable-languages=ada,c,c++,d,fortran,go,lto,m2,objc,obj-c++,rust,cobol --enable-libstdcxx-backtrace --enable-link-serialization=1 --enable-lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-build-config=bootstrap-lto --with-linker-hash-style=gnu`

Disk Notes: NONE / relatime,rw / Block Size: 4096

Processor Notes: Scaling Governor: intel_pstate powersave (EPP: balance_performance) - CPU Microcode: 0xf6

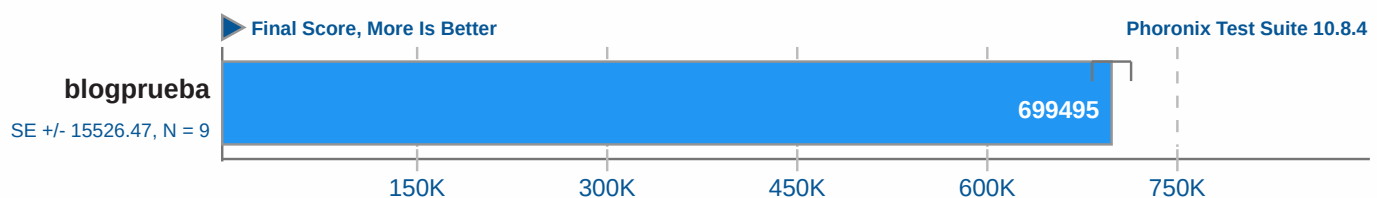
Security Notes: gather_data_sampling: Mitigation of Microcode + ghostwrite: Not affected + indirect_target_selection: Not affected + itlb_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT vulnerable + mds: Mitigation of Clear buffers; SMT vulnerable + meltdown: Mitigation of PTI + mmio_stale_data: Mitigation of Clear buffers; SMT vulnerable + old_microcode: Not affected + reg_file_data_sampling: Not affected + retbleed: Mitigation of IBRS + spec_rstack_overflow: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl + spectre_v1: Mitigation of usercopy/swaps barriers and __user pointer sanitization + spectre_v2: Mitigation of IBRS; IBPB: conditional; STIBP: conditional; RSB filling; PBRSE-IBRS: Not affected; BHI: Not affected + srbds: Mitigation of Microcode + tsa: Not affected + tsx_async_abort: Mitigation of TSX disabled

BlogBench

BlogBench is designed to replicate the load of a real-world busy file server by stressing the file-system with multiple threads of random reads, writes, and rewrites. The behavior is mimicked of that of a blog by creating blogs with content and pictures, modifying blog posts, adding comments to these blogs, and then reading the content of the blogs. All of these blogs generated are created locally with fake content and pictures.

BlogBench 1.1

Test: Read

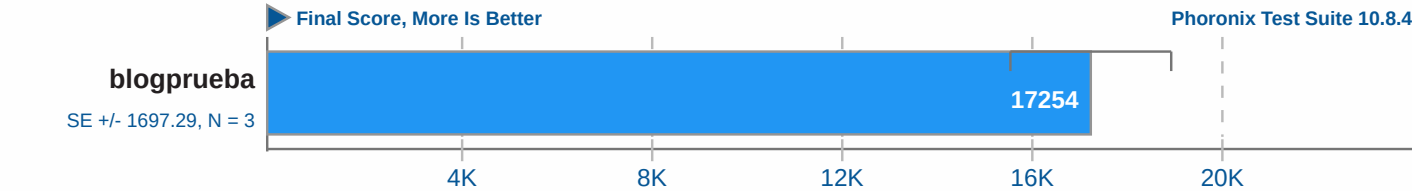


1. (CC) gcc options: -O2

BlogBench 1.1

Test: Write

pts



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Testing initiated at 22 August 2025 22:55 by user Fernando.