X11PRUEBA

Intel Core i5-6500 testing with a Dell 09WH54 (2.33.0 BIOS) and Intel HD 530 on Arch Linux via the Phoronix Test Suite.

x11prueba

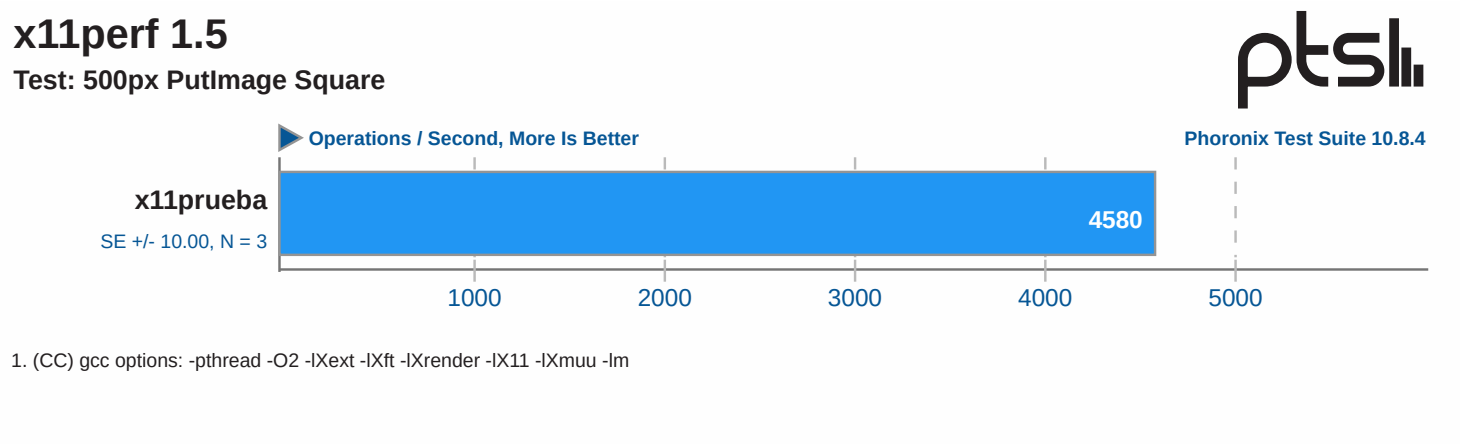
Processor: Intel Core i5-6500 @ 3.60GHz (4 Cores), **Motherboard:** Dell 09WH54 (2.33.0 BIOS), **Chipset:** Intel Xeon E3-1200 v5/E3-1500, **Memory:** 16GB, **Disk:** 1024GB ADATA SU650 + 500GB TOSHIBA MQ01ABF0 + 0GB Compact Flash + 0GB SM/xD-Picture + 0GB SD/MMC + 0GB M.S./M.S.Pro/HG, **Graphics:** Intel HD 530, **Audio:** Realtek ALC3861, **Monitor:** Acer K272HUL, **Network:** Intel I219-LM + Intel 7260

OS: Arch Linux, **Kernel:** 6.16.3-arch1-1 (x86_64), Display **Server:** X Server 1.21.1.18, **OpenGL:** 4.6 Mesa 25.2.1-arch1.1, **Compiler:** GCC 15.2.1 20250813, **File-System:** ext4, Screen **Resolution:** 2560x1440

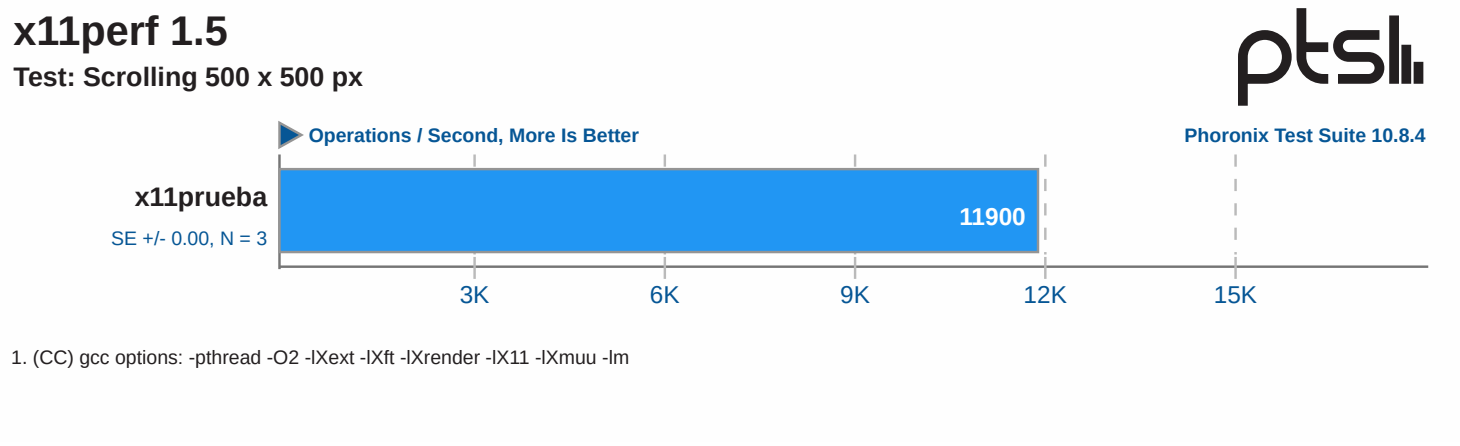
Kernel Notes: *Transparent Huge Pages: always*
Compiler Notes: *--disable-libssp --disable-libstdcxx-pch --disable-werror --enable-__cxa_atexit --enable-bootstrap --enable-cet=auto --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-default-ssp --enable-gnu-indirect-function --enable-gnu-unique-object --enable-languages=ada,c,c++,d,fortran,go,lto,m2,objc,obj-c++,rust,cobol --enable-libstdcxx-backtrace --enable-link-serialization=1 --enable-lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-build-config=bootstrap-lto --with-linker-hash-style=gnu*
Processor Notes: *Scaling Governor: intel_pstate powersave (EPP: balance_performance) - CPU Microcode: 0xf0*
Graphics Notes: *SNA*
Security Notes: *gather_data_sampling: Vulnerable: No microcode + ghostwrite: Not affected + indirect_target_selection: Not affected + itlb_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT disabled + mds: Mitigation of Clear buffers; SMT disabled + meltdown: Mitigation of PTI + mmio_stale_data: Mitigation of Clear buffers; SMT disabled + old_microcode: Not affected + reg_file_data_sampling: Not affected + retbleed: Mitigation of IBRS + spec_rstack_overflow: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of IBRS; IBPB: conditional; STIBP: disabled; RSB filling; PBRSB-eIBRS: Not affected; BHI: Not affected + srbds: Mitigation of Microcode + tsa: Not affected + tsx_async_abort: Mitigation of TSX disabled*

x11perf

x11perf is a very basic performance/regression test for X.Org.



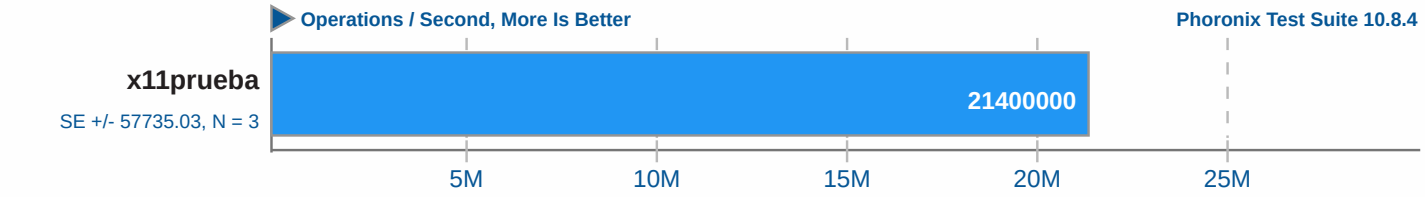
1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

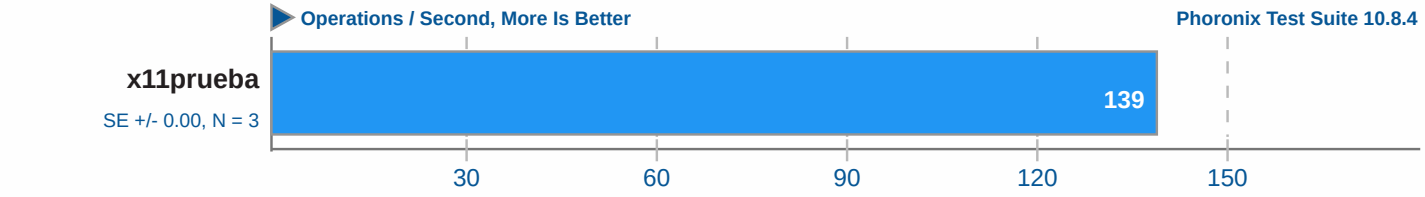
Test: Char in 80-char aa line



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

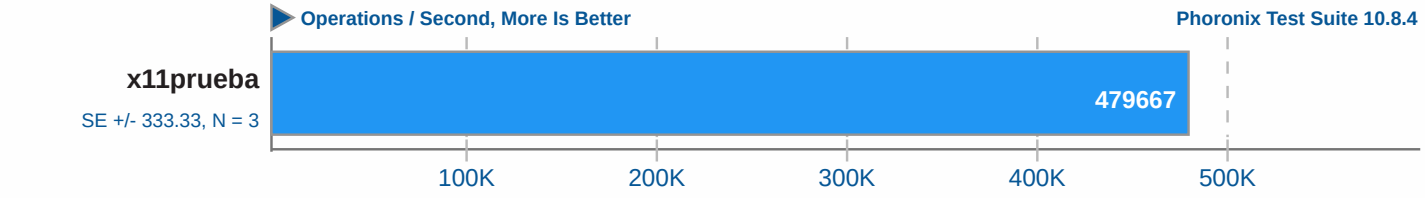
Test: PutImage XY 500x500 Square



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

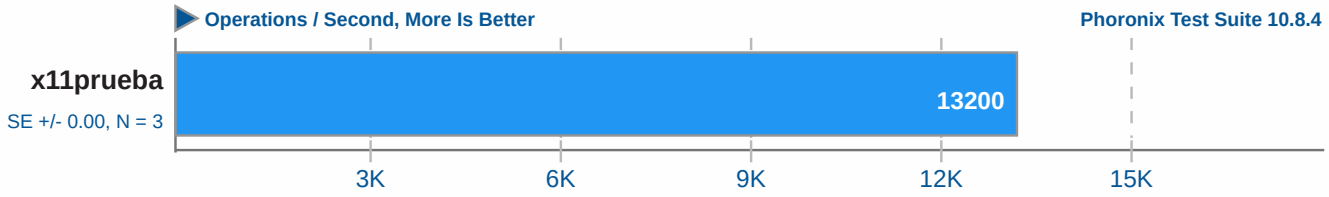
Test: Fill 300 x 300px AA Trapezoid



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

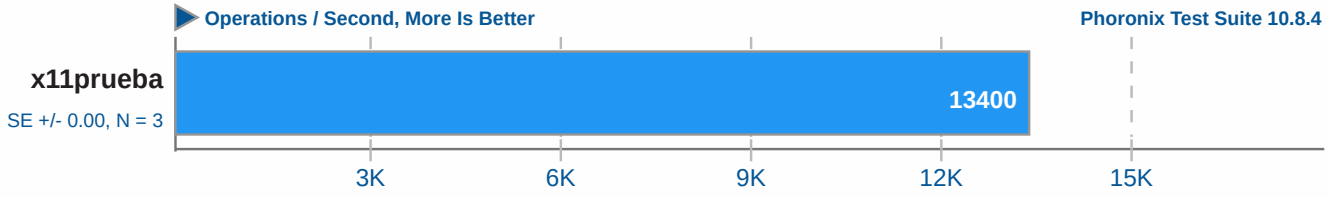
Test: 500px Copy From Window To Window



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

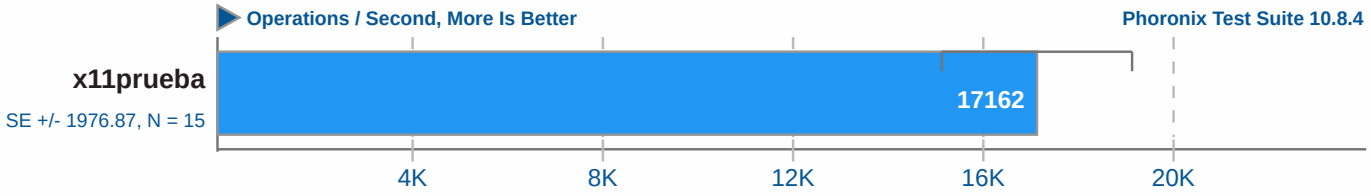
Test: Copy 500x500 From Pixmap To Pixmap



1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm

x11perf 1.5

Test: 500px Compositing From Pixmap To Window



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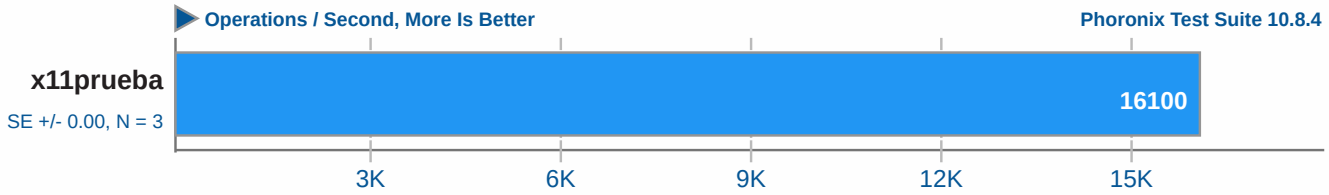
x11perf 1.5

Test: 500px Compositing From Window To Window



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Processor Notes: Scaling Governor: intel_pstate powersave (EPP: balance_performance) - CPU Microcode: 0xf0
Graphics Notes: SNA
Security Notes: gather_data_sampling: Vulnerable: No microcode + ghostwrite: Not affected + indirect_target_selection: Not affected + itlb_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT disabled + mds: Mitigation of Clear buffers; SMT disabled + meltdown: Mitigation of PTI + mmio_stale_data: Mitigation of Clear buffers; SMT disabled + old_microcode: Not affected + reg_file_data_sampling: Not affected + retbleed: Mitigation of IBRS + spec_rstack_overflow: Not affected + spec_store_bypass: Mitigation of SSB disabled via prctl + spectre_v1: Mitigation of usercopy/swapgs barriers and __user pointer sanitization + spectre_v2: Mitigation of IBRS; IBPB: conditional; STIBP: disabled; RSB filling; PBRSB-eIBRS: Not affected; BHI: Not affected + srbds: Mitigation of Microcode + tsa: Not affected + tsx_async_abort: Mitigation of TSX disabled

Testing initiated at 25 August 2025 00:51 by user Fernando.