#### X11PRUEBA

Intel Core i5-8350U testing with a LENOVO 20L7002CUS (N22ET82W 1.59 BIOS) and Intel UHD 620 on Arch Linux via the Phoronix Test Suite.

### x11prueba

Processor: Intel Core i5-8350U @ 3.60GHz (4 Cores / 8 Threads), Motherboard: LENOVO 20L7002CUS (N22ET82W 1.59 BIOS), Chipset: Intel Xeon E3-1200 v6/7th, Memory: 16GB, Disk: SK hynix PC611 NVMe 512GB + 0GB SD/MMC, Graphics: Intel UHD 620, Audio: Realtek ALC257, Network: Intel I219-LM + Intel 8265 / 8275

OS: Arch Linux, Kernel: 6.16.2-arch1-1 (x86\_64), Display Server: X Server 1.21.1.18, OpenGL: 4.6 Mesa 25.2.1-arch1.1, Compiler: GCC 15.2.1 20250813, File-System: ext4, Screen Resolution: 1920x1080

Kernel Notes: Transparent Huge Pages: always

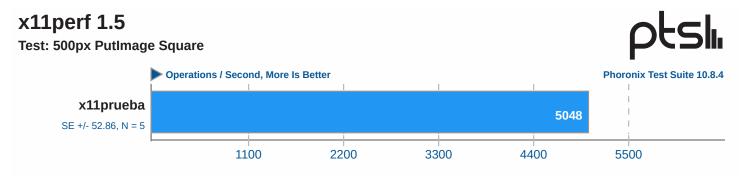
Compiler Notes: -disable-libssp -disable-libstdcxx-pch -disable-werror --enable-\_cxa\_atexit --enable-bootstrap --enable-cet=auto --enable-checking=release --enable-clocale=gnu --enable-default-pie --enable-default-ssp --enable-gnu-indirect-function --enable-gnu-unique-object --enable-languages=ada,c,c++,d,fortran,go,lto,m2,objc,obj-c++,rust,cobol --enable-libstdcxx-backtrace --enable-link-serialization=1 --enable-lto --enable-multilib --enable-plugin --enable-shared --enable-threads=posix --mandir=/usr/share/man --with-build-config=bootstrap-lto --with-linker-hash-style=gnu

Processor Notes: Scaling Governor: intel\_pstate powersave (EPP: balance\_performance) - CPU Microcode: 0xf6
Graphics Notes: SNA

Security Notes: gather\_data\_sampling: Mitigation of Microcode + ghostwrite: Not affected + indirect\_target\_selection: Not affected + itlb\_multihit: KVM: Mitigation of Split huge pages + l1tf: Mitigation of PTE Inversion; VMX: conditional cache flushes SMT vulnerable + mds: Mitigation of Clear buffers; SMT vulnerable + meltdown: Mitigation of PTI + mmio\_stale\_data: Mitigation of Clear buffers; SMT vulnerable + old\_microcode: Not affected + reg\_file\_data\_sampling: Not affected + retbleed: Mitigation of IBRS + spec\_rstack\_overflow: Not affected + spec\_store\_bypass: Mitigation of SSB disabled via prctl + spectre\_v1: Mitigation of usercopy/swapgs barriers and \_\_user pointer sanitization + spectre\_v2: Mitigation of IBRS; IBPB: conditional; STIBP: conditional; RSB filling; PBRSB-eIBRS: Not affected; BHI: Not affected + srbds: Mitigation of Microcode + tsa: Not affected + tsx\_async\_abort: Mitigation of TSX disabled

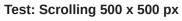
### x11perf

x11perf is a very basic performance/regression test for X.Org.

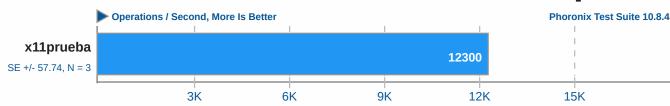


1. (CC) gcc options: -pthread -O2 -IXext -IXft -IXrender -IX11 -IXmuu -Im

# x11perf 1.5





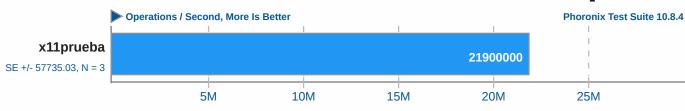


1. (CC) gcc options: -pthread -O2 -IXext -IXft -IXrender -IX11 -IXmuu -Im

## **x11perf 1.5**

Test: Char in 80-char aa line





1. (CC) gcc options: -pthread -O2 -IXext -IXft -IXrender -IX11 -IXmuu -Im

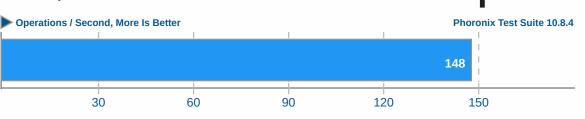
## **x11perf 1.5**

Test: PutImage XY 500x500 Square

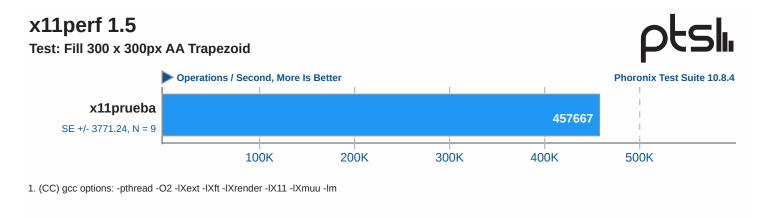
x11prueba

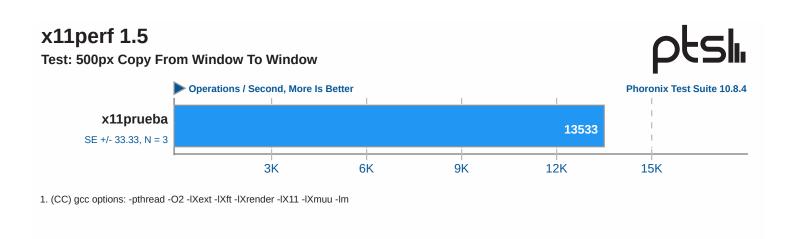
SE +/- 0.33, N = 3

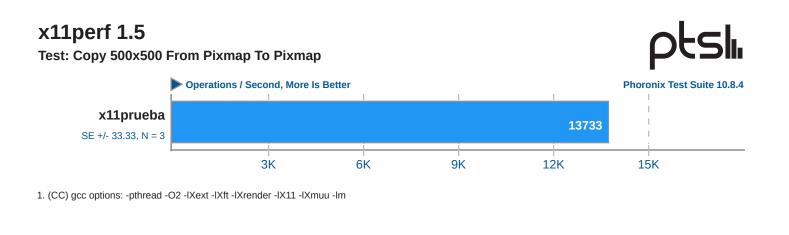


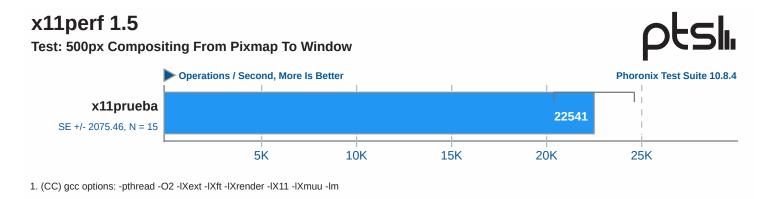


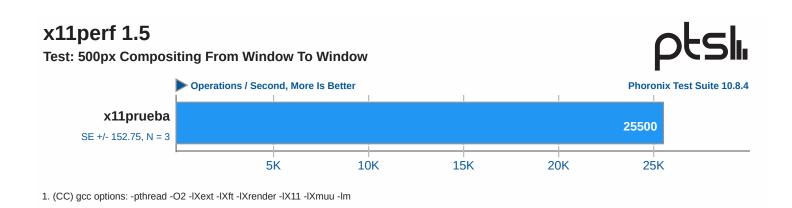
1. (CC) gcc options: -pthread -O2 -lXext -lXft -lXrender -lX11 -lXmuu -lm











### x11prueba

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**Processor Notes:** Scaling Governor: intel\_pstate powersave (EPP: balance\_performance) - CPU Microcode: 0xf6 **Graphics Notes:** SNA

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Testing initiated at 23 August 2025 23:11 by user Fernando.

Phoronix Test Suite 10.8.4 - Generated 23 August 2025 23:50:35