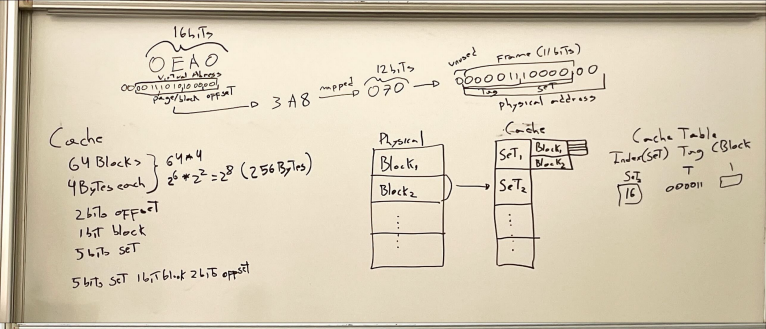

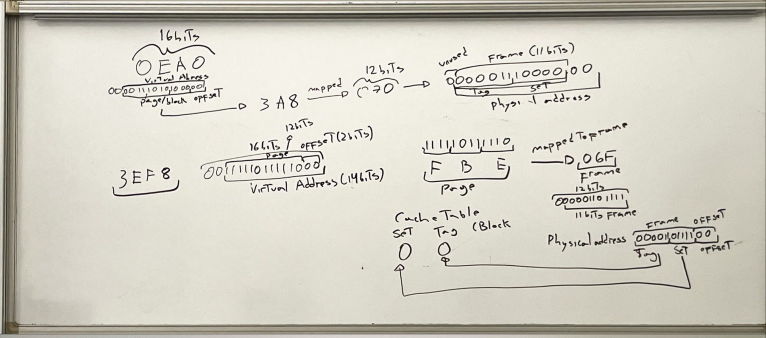

Memory

PROBLEM 1. Given a physical memory with $4KB$ blocks of 16 words/bytes each, and a cache memory of 128 blocks, determine the address coding for both physical and cache memory for each of the following cache organizations:

1. Direct Mapping.
2. Full Associative.
3. 2-Way Set Associative.