

The timing diagram illustrates the operation of the 74164 shift register. The x-axis represents time in microseconds (u), ranging from 0.00u to 3.50u. The y-axis represents the digital signals. The red signals represent the outputs of the shift register, which are loaded with the data values from the green sequence. The blue signal represents the clock input, which is active high and has a period of 1 microsecond. The green sequence shows the data values being shifted into the register, starting with 0 and ending with 2.

d(2)

d(3)

d(4)

d(5)

dec(5,4,3,2,1)

D(9)