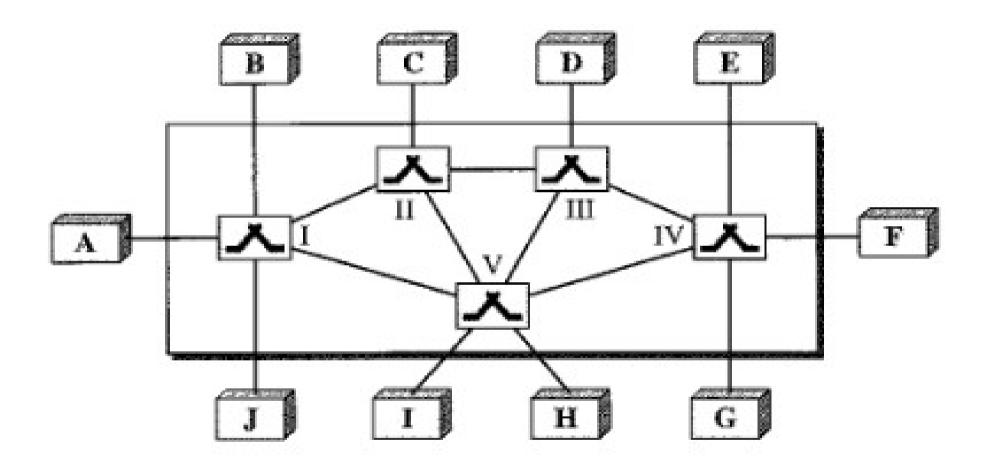
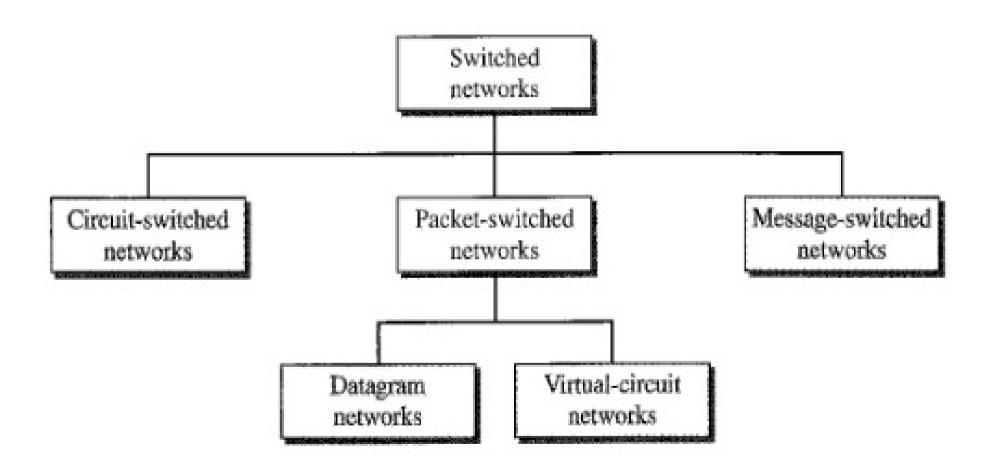
Chapter 8

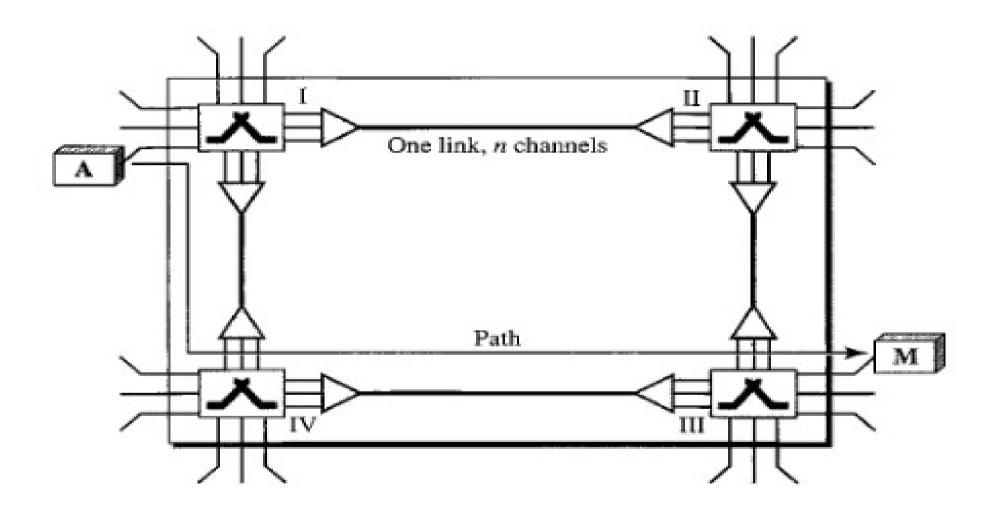
Switching



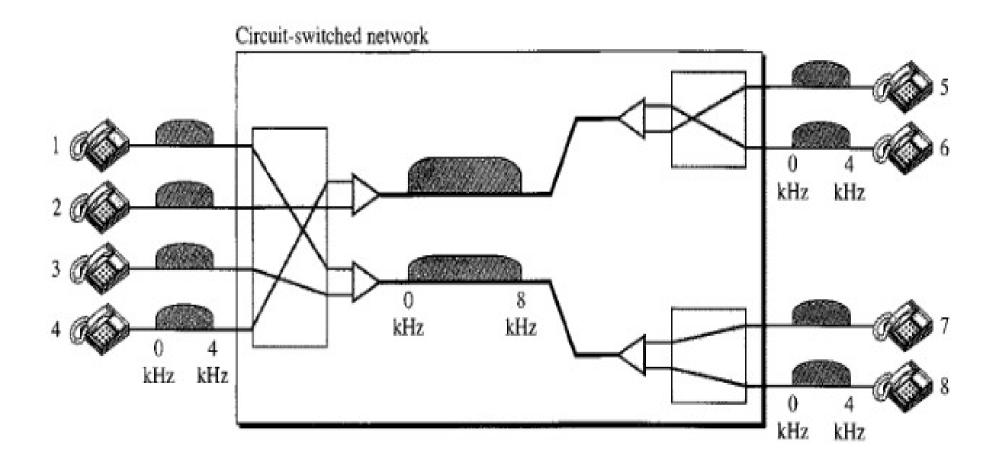
Switching

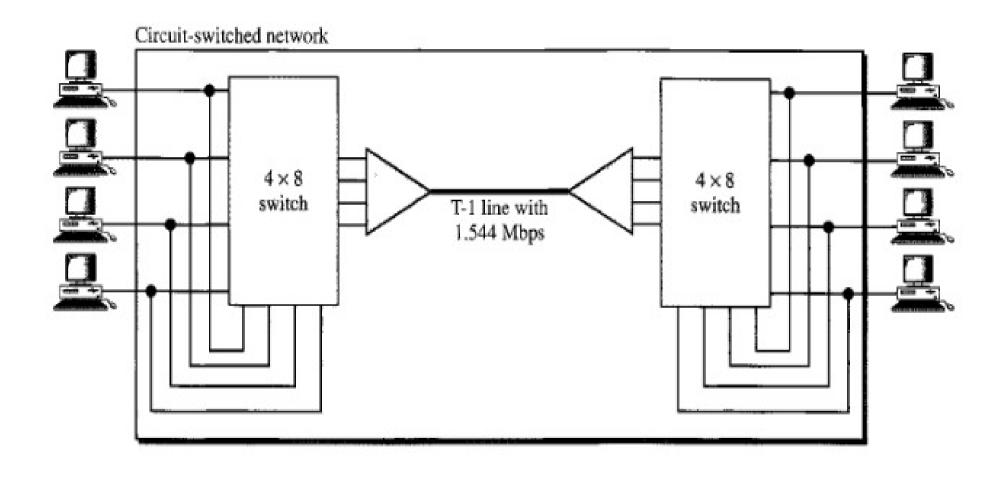


- Set of switches connected by physical links
- Connection between two stations is a dedicated path made of one or more links
- Only one channel in a link is used
- Phases: connection setup, data transfer, connection teardown
- Inefficient dedicated link can be unused
- Minimal delay

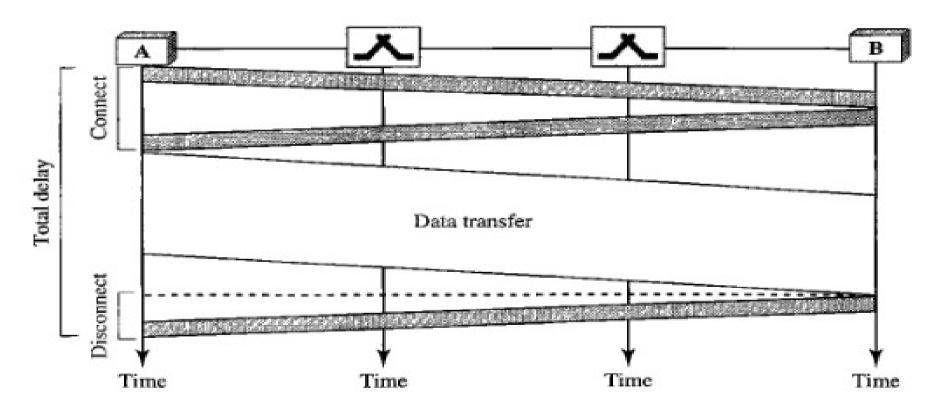


- Takes place at the physical layer
- Resources to be used must be reserved
- Data transferred are a continuous flow
- No addressing involved in the data transfer

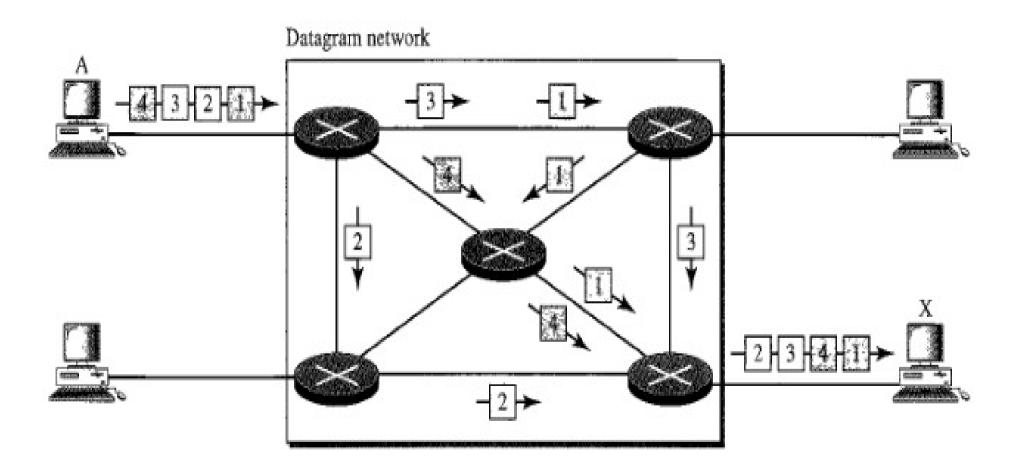




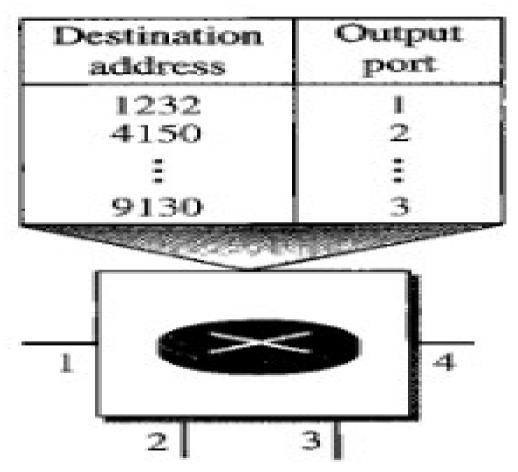
Delays



- In packet-switched networks, messages are divided into packets: fixed or variable size
- No resource allocation for a packet
- Resources are allocated on demand; FCFS
- In a datagram network, each packet is treated independently of all others
- Datagram switching done at the network layer
- Datagrams may arrive out of order or get lost
- Connectionless no information on state of connection is saved



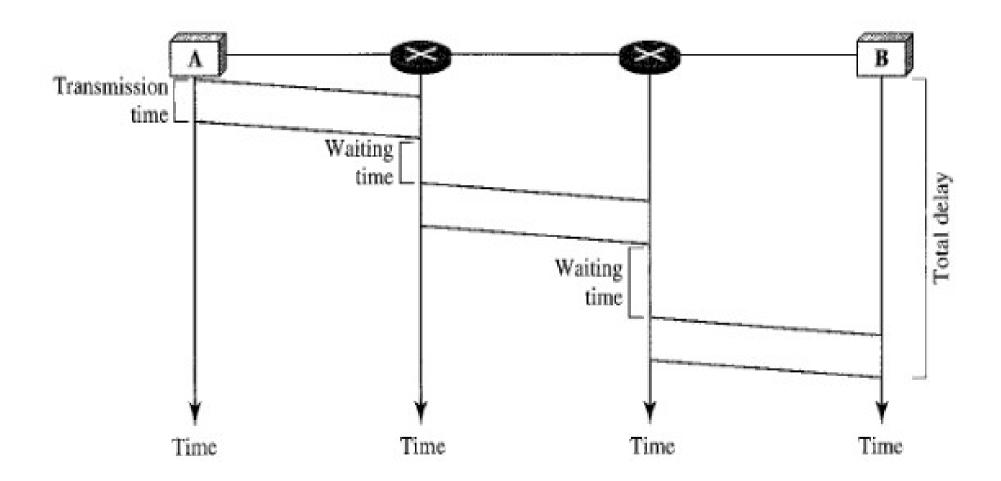
Routing table



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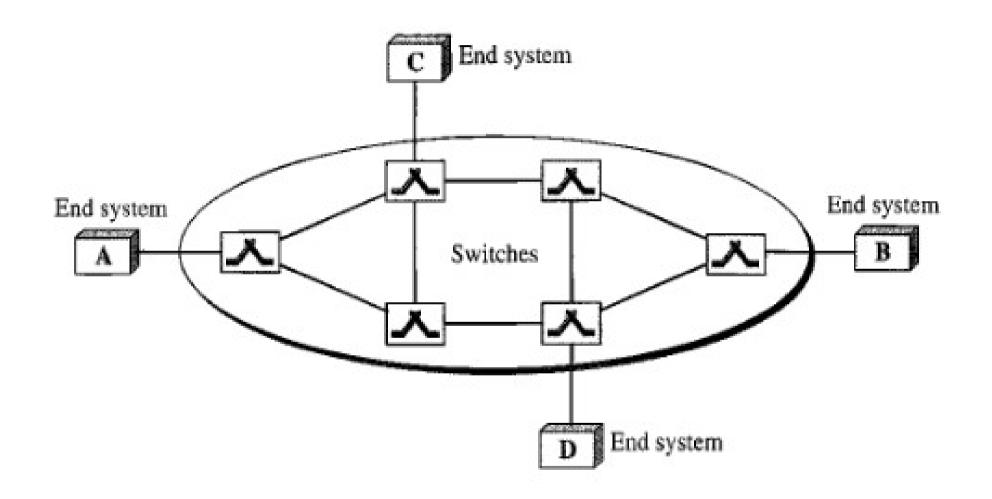
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- Header contains address
- Efficient resource are allocated when there are packets to be transferred
- More delay waiting time in switches

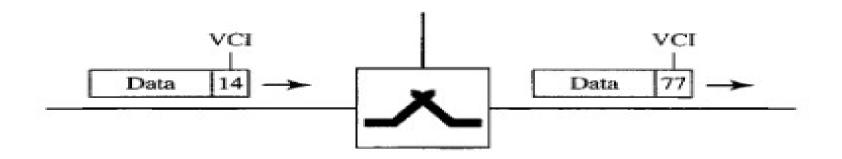


- Cross between circuit-switched network and datagram network
 - With connection setup and teardown phases
 - Resources can be allocated during the setup phase or on demand

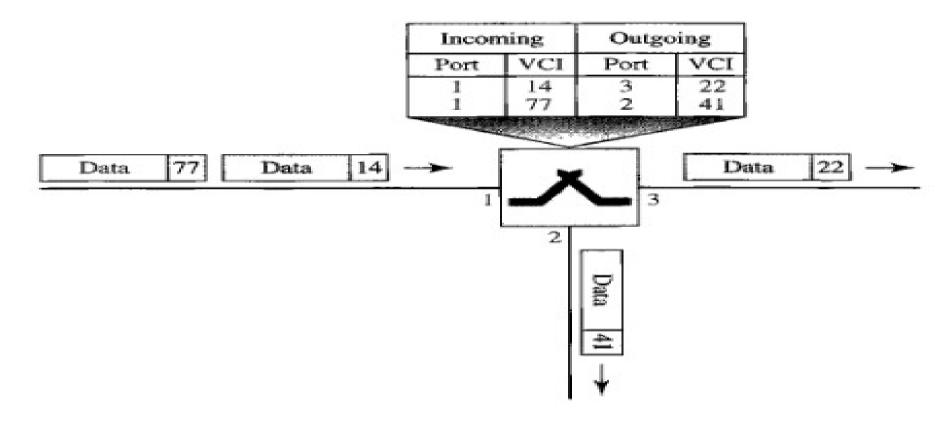
- Packetized data with address in header(local jurisdiction only)
- Packets follow the same path established
- Implemented in the data link layer



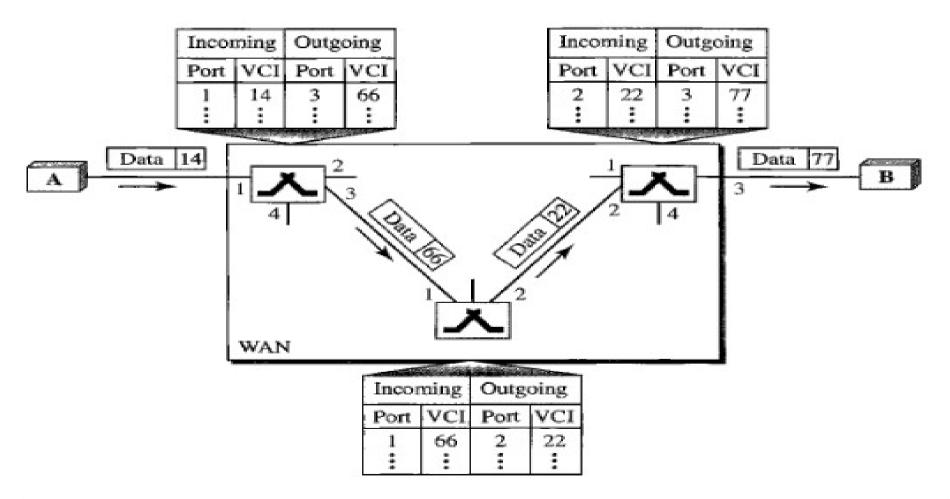
- A source or a destination needs to have a global address – used to create VCI
- Virtual-Circuit Identifier (VCI) switch scope (between two switches)



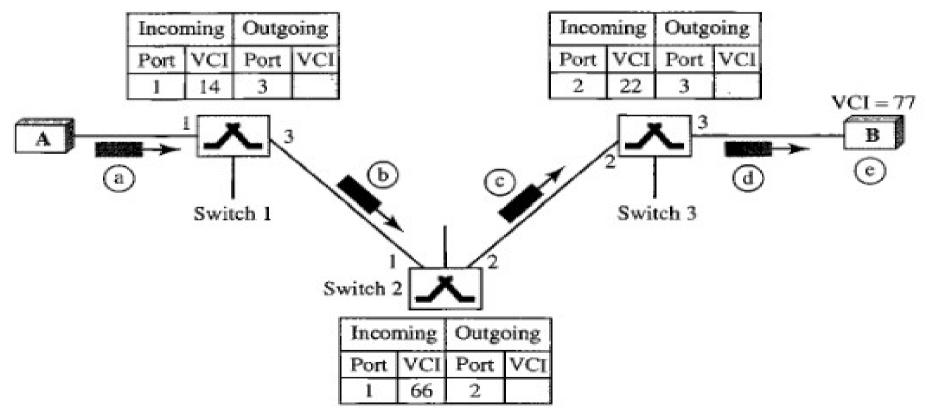
Switch and Tables



Data transfer



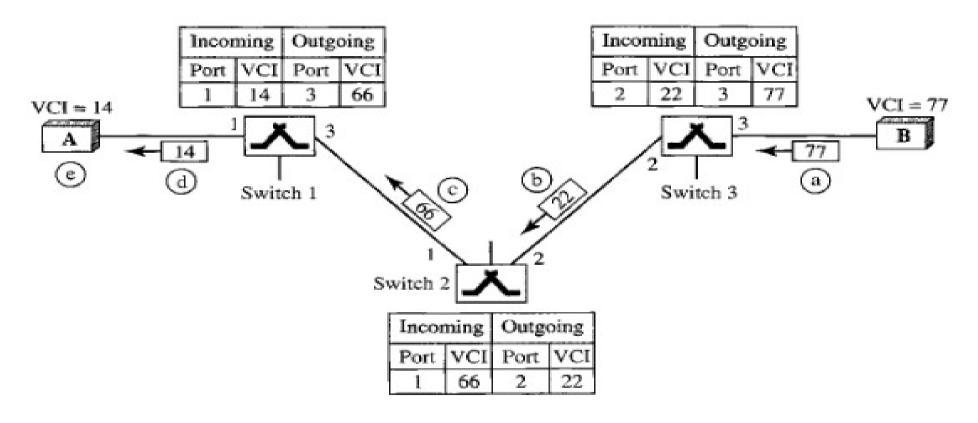
 Setup request, switch acts as packet switch (routing table different from switching table



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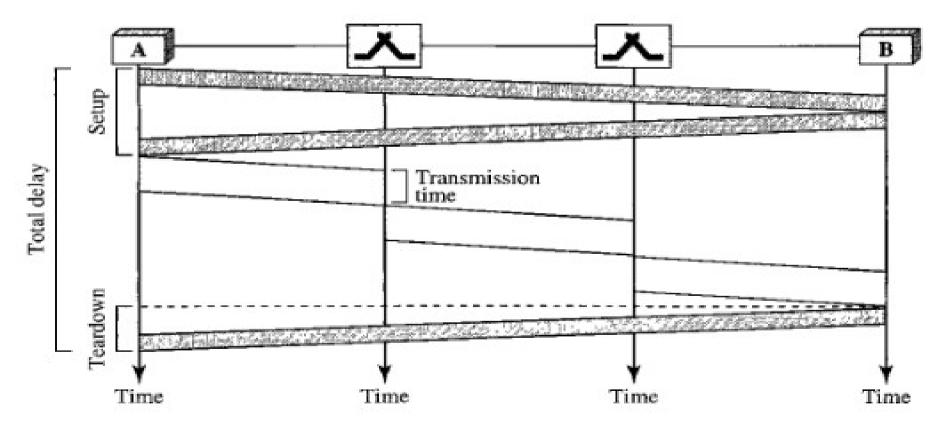
Acknowledgement



Efficiency

 All packets belonging to the same source and destination travel the same path but may arrive at the destination with different delays if resource allocation is on demand

Tot Delay=3*Trans+3*Propa+setup+teardown

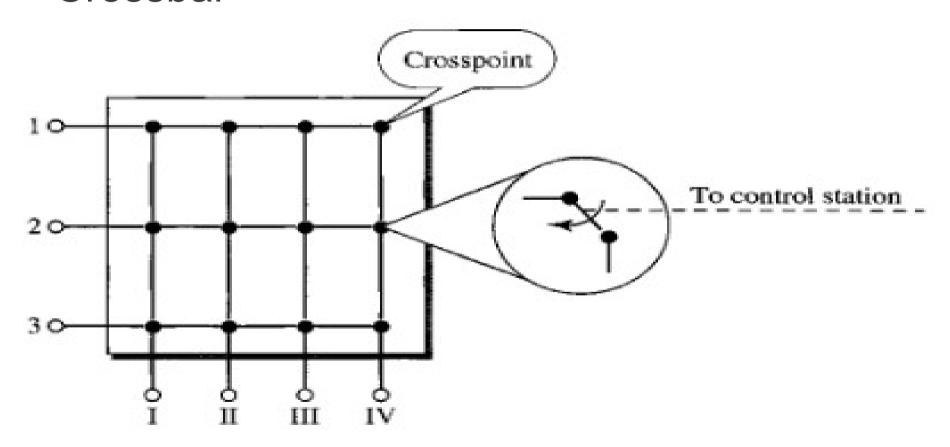


- Space-division switch paths in the circuit are separated spatially
 - Crossbar
 - Multistage
- Time-division switch uses TDM
 - Time-slot interchange
- Time- and Space-Division switch combo

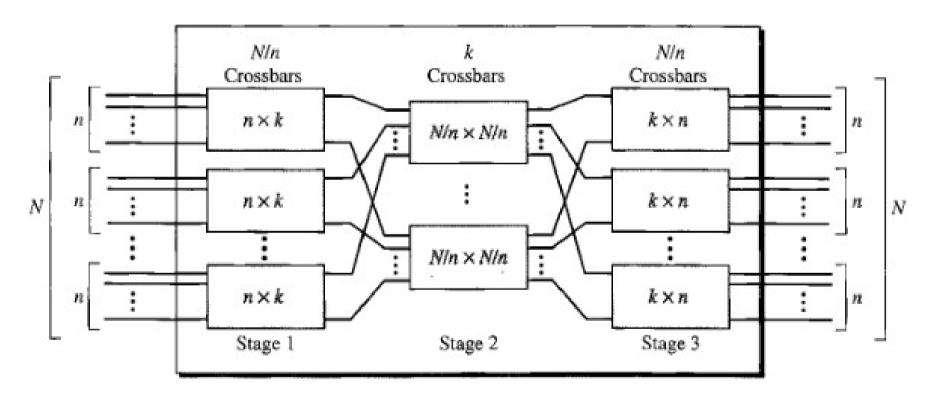
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Time-space-time

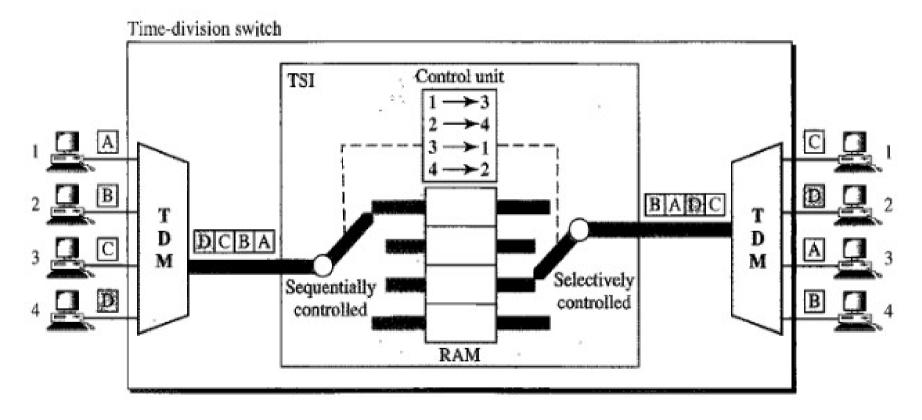
Crossbar



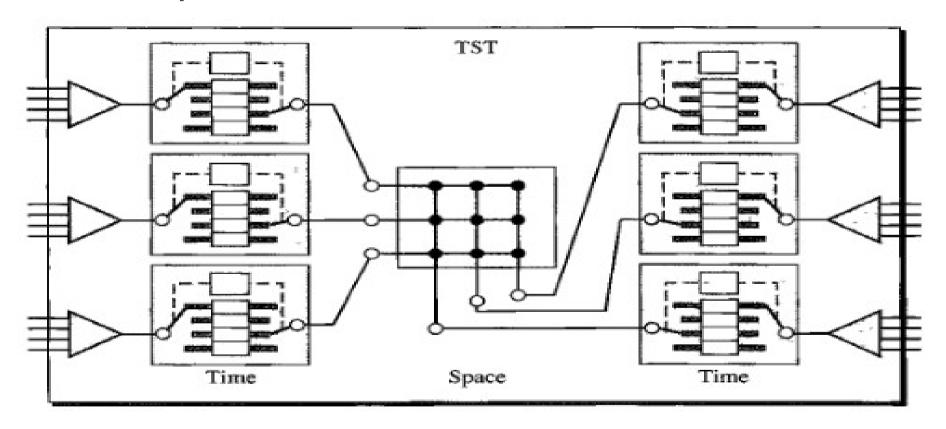
Multistage



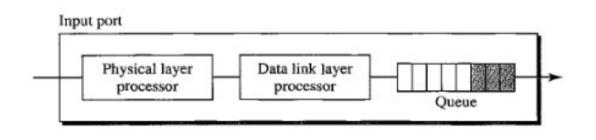
Time-slot interchange

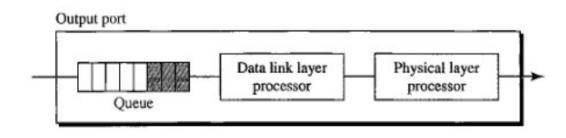


Time-space-time

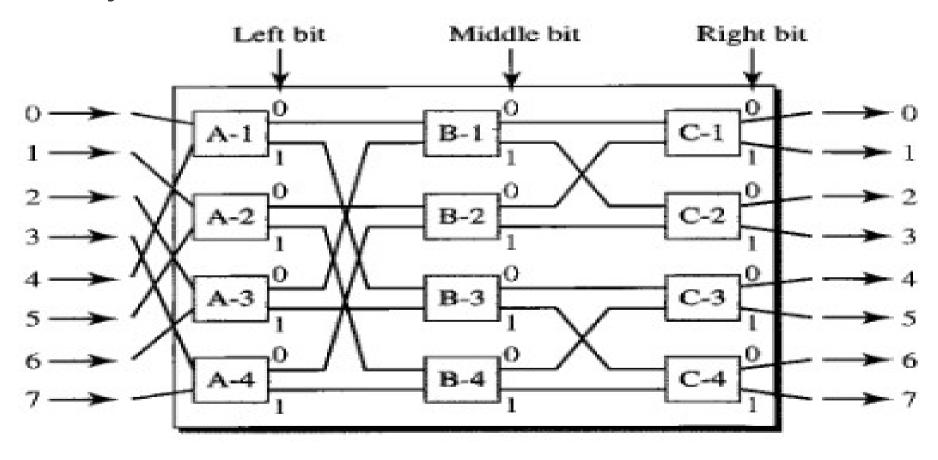


- Four components
 - Input ports
 - Output ports
 - Routing processor
 - Switching fabric
 - Crossbar
 - Banyan
 - Batcher-Banyan Switch

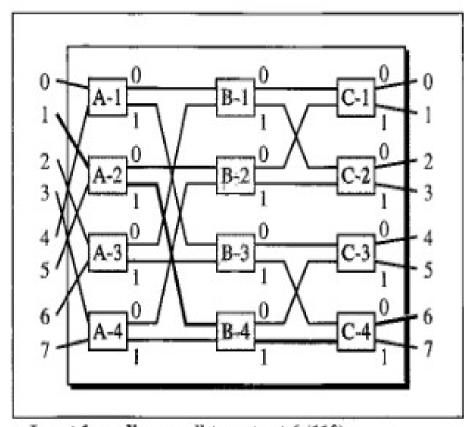


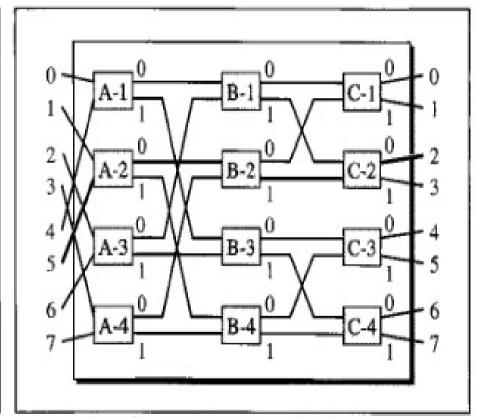


Banyan switch



Banyan switch – problem is collision

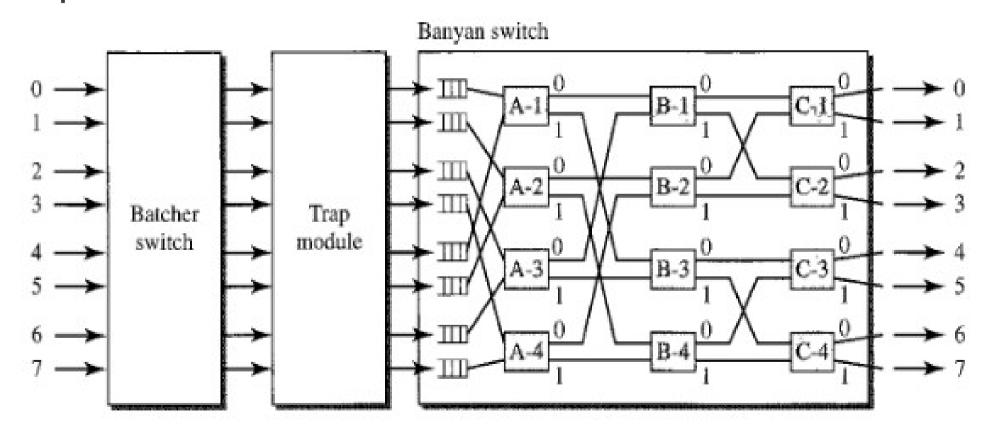




a. Input 1 sending a cell to output 6 (110)

b. Input 5 sending a cell to output 2 (010)

Batcher-Banyan switch – sorts the arriving packets



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Enjoy!:)