

# III. STRUCTURED ASSEMBLY LANGUAGE PROGRAMMING TECHNIQUES

## Control Transfer Instructions



# More on Conditional Jumps

- Instructions that check the eFLAGS register before jumping
- The FLAGS checked by Conditional jumps
  - Carry
  - Parity
  - Zero
  - Sign
  - Overflow flags



# The eFLAGS Register

- A special purpose register
- Certain bits in this register serve as Flags

31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
0	0	0	0	0	0	0	0	0	0	0	ID	VP	VF	AC	VM	RF	0	NT	IOPL	OF	DF	IF	TF	SF	ZF	0	AF	0	PF	1	CF



# The eFLAGS Register

FLAGS	SET (1)	CLEARED (0)
Overflow	OV [overflow]	NV [no overflow]
Sign	NG [negative]	PL [positive]
Zero	ZR [zero]	NZ [not zero]
Parity	PE [even]	PO [odd]
Carry	CY [carry]	NC [no carry]



# Conditional Jumps

```
cmp AX, BX
```

```
je label1
```

➤ does **sub AX, BX** but doesn't modify the first operand

➤ sets the flags according to the subtraction's result

Checks the ZERO Flag



# Sequential Statements

- Fetch-Decode-Execute

CS:EIP is the PC (Program Counter)

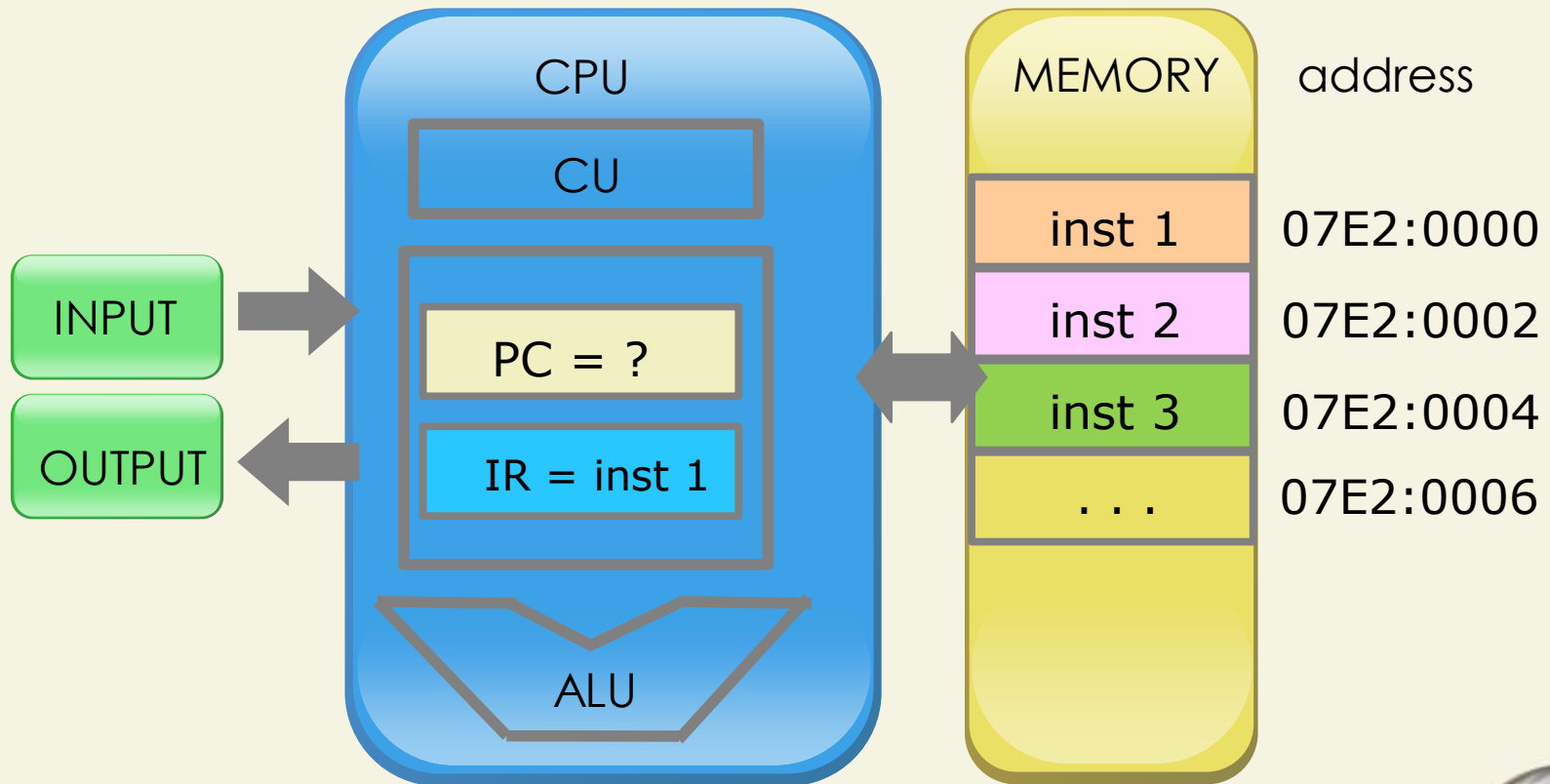
- When the fetched instruction is copied into the instruction register, EIP is automatically incremented by X.

$X = \text{instruction length (in bytes)}$

- Since EIP is automatically INCREMENTED by X, the instructions are executed SEQUENTIALLY by default.



# Recall



# How can we change the default execution?

What will happen to EIP?

High-level code/algorithm:

```
Current instruction  ► if (condition) then do1
    IP points here    ► do1:  code 0
                        code 1
                        code 2
                        else
do2:  code x
      code y
                        code z
```





# JMP Statement

- This instruction takes one operand: a label
- For example:        `jmp doon`

```
dito_ba:  mov eax, 4  
          mov ebx, 1  
          mov ecx, msg1  
          mov edx, len1  
          int 80h
```

**LABEL**

```
doon:    mov eax, 1  
          mov ebx, 0  
          int 80h
```



# JMP Statement

nasm -f elf sample.asm -l sample.lst

```
00000000 E916000000      jmp doon

                                dito_ba:
00000005 B804000000      mov eax, 4
0000000A BB01000000      mov ebx, 1
0000000F B9[00000000]      mov ecx, msg1
00000014 BA11000000      mov edx, len1
00000019 CD80          int 80h

                                doon:
0000001B B801000000      mov eax, 1
00000020 BB00000000      mov ebx, 0
00000025 CD80          int 80h
```



# JMP Statement

machine code/  
opcode of

```
00000000 E916000000 jmp doon
                                dito_ba:
00000005 B804000000 mov eax, 4
0000000A BB01000000 mov ebx, 1
0000000F B9[00000000] mov ecx, msg1
00000014 BA11000000 mov edx, len1
00000019 CD80      int 80h

                                doon:
0000001B B801000000 mov eax, 1
00000020 BB00000000 mov ebx, 0
00000025 CD80      int 80h
```



# JMP Statement

machine code/ opcode of		
00000000	E916000000?	jmp doon
		dito_ba:
00000005	B804000000	mov eax, 4
0000000A	BB01000000	mov ebx, 1
0000000F	B9[00000000]	mov ecx, msg1
00000014	BA11000000	mov edx, len1
00000019	CD80	int 80h
		doon:
0000001B	B801000000	mov eax, 1
00000020	BB00000000	mov ebx, 0
00000025	CD80	int 80h



# JMP Statement

jmp

a value in backwards storage format;  
therefore actual value is: 00000016

```
00000000 E916000000 jmp doon
00000005 B804000000 mov eax, 4
0000000A BB01000000 mov ebx, 1
0000000F B9[00000000] mov ecx, msg1
00000014 BA11000000 mov edx, len1
00000019 CD80 int 80h

doon:
0000001B B801000000 mov eax, 1
00000020 BB00000000 mov ebx, 0
00000025 CD80 int 80h
```

So we have:  
E9 00000016



# JMP Statement

When Instruction Register contains  
CS:EIP will 'point' here

So EIP =  
00000005 h

E9 00000016

```
00000000 E9 16 00 00 00 jmp doon
00000005 B8 04 00 00 00 dito_ba:
0000000A BB 01 00 00 00 mov eax, 4
0000000F B9 00 00 00 00 mov ebx, 1
00000014 BA 11 00 00 00 mov ecx, msg1
00000019 CD 80 mov edx, len1
int 80h

doon:
0000001B B8 01 00 00 00 mov eax, 1
00000020 BB 00 00 00 00 mov ebx, 0
00000025 CD 80 int 80h
```

Note: As the instruction in IR is executed:  
EIP will be:  $EIP + 00000016 = 0000001B$  h



# JMP Statement

When Instruction Register contains  
CS:EIP will 'point' here

So EIP =  
00000005 h

E9 00000016

00000000	E9 16 00 00 00 00	jmp doon
00000005	B8 04 00 00 00 00	dito_ba: mov eax, 4
0000000A	BB 01 00 00 00 00	mov ebx, 1
0000000F	B9 [00000000]	mov ecx, msg1
00000014	BA 11 00 00 00 00	mov edx, len1
00000019	CD 80	int 80h
0000001B	B8 01 00 00 00 00	doon: mov eax, 1
00000020	BB 00 00 00 00 00	mov ebx, 0
00000025	CD 80	int 80h

Note: As the instruction in IR is executed:  
EIP will be:  $EIP + 00000016 = 0000001B$  h





# Addition of hex numbers

Decimal:

$$\begin{array}{r} 19 \\ + 9 \\ \hline 28 \end{array}$$

Hexadecimal:

$$\begin{array}{r} 19 \\ +9 \\ \hline 22 \end{array}$$





# JMP Statement

Reminder:

- The displacement is in Backwards Storage Format.
- A displacement in a jump can either be a positive or a negative value.



# JMP Statement

```
                                dito_ba:
00000000 B804000000          mov eax, 4
00000005 BB01000000          mov ebx, 1
0000000A B9[00000000]        mov ecx, msg1
0000000F BA11000000          mov edx, len1
00000014 CD80                int 80h
00000016 E5E5FFFFFF          jmp dito_ba
                                doon:
0000001B B801000000          mov eax, 1
00000020 BB00000000          mov ebx, 0
00000025 CD80                int 80h
```

**FFFF FFE5**



# JMP Statement

```
                                dito_ba:
00000000 B804000000          mov eax, 4
00000005 BB01000000          mov ebx, 1
0000000A B9[00000000]        mov ecx, msg1
0000000F BA11000000          mov edx, len1
00000014 CD80                int 80h
00000016 E5E5FFFFFF          jmp dito_ba
                                doon:
0000001B B801000000          mov eax, 1
00000020 BB00000000          mov ebx, 0
00000025 CD80                int 80h
```

The value is negative, so this jumps to a label 'above'

FFFF FFE5  
F = 1111 (binary)  
sign-bit ←



# JMP Statement

