

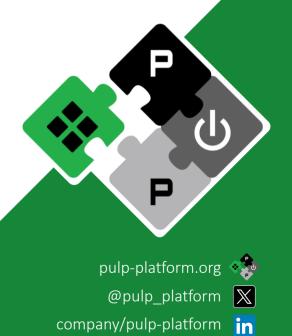
Exploring Outer Product and Gustavson Dataflows for SpMM acceleration

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Open Source Hardware, the way it should be!



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Motivation



GEMM is a fundamental operation for many workloads, especially ML

- Activations (ReLU, etc.) and pruning introduce many zeros.
- Event-based sensors produce intrinsically sparse data.

Ignoring sparsity wastes cycles, bandwidth, and energy.

- Processing zeros ⇒ unnecessary latency and power
- Making the core sparsity-aware is costly.

Idea: moving the "intelligence" inside the streamer

- No modifications to the engine which remains agnostic to the sparsity
- Scalable
- Less latency and memory traffic

Our study case:

Matrix X is sparse and encoded in bitmap format, while matrix Y is dense



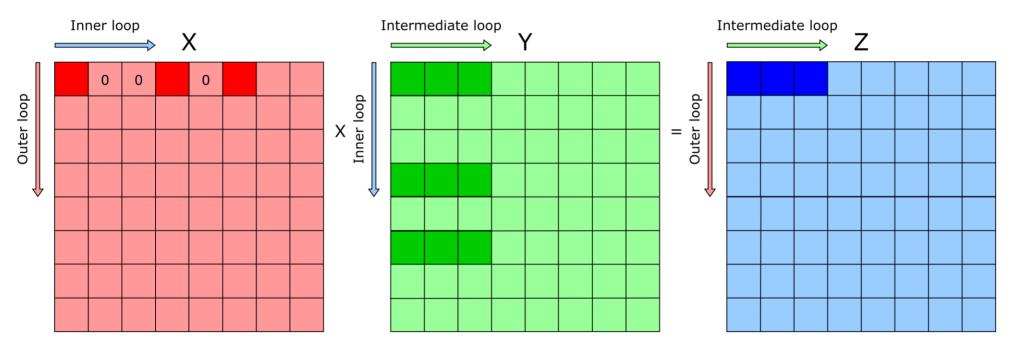


The idea



Avoid multiplications by zero

- If the element in the row of X is a zero, I can skip the correspondent row of Y
- This way I avoid unnecessary loads and multiplications that consume BW, energy and cycles



Example with Gustavson Z







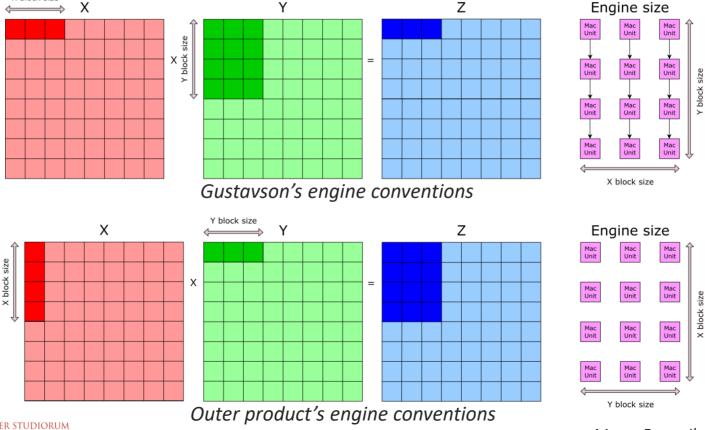
Conventions used for the engine size



- The engine size will depend on the chosen portions of matrices X and Y
 - In Gustavson the same X block will be shared with every row of the Y block

• In outer product the elements of the two blocks are combined to obtain a matrix chunk of partial

results





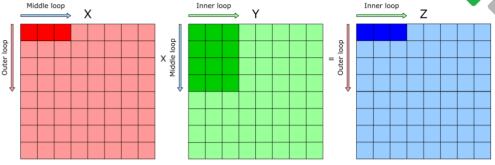


Gustavson options

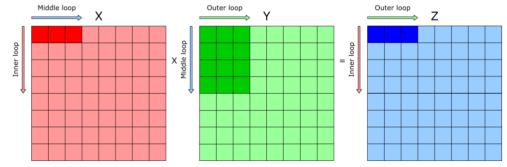
There are three options

- X reuse: the elements of X are reused as much as possible and not changed in the inner loop.
- Y reuse: the elements of Y are reused as much as possible and not changed in the inner loop.
- Z reuse: the elements of Z are "reused" as much as possible meaning that, they are fully computed before being stored back

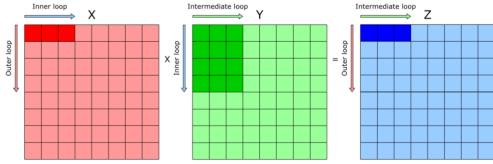
Gustavson X reuse:



Gustavson Y reuse:



Gustavson Z reuse:



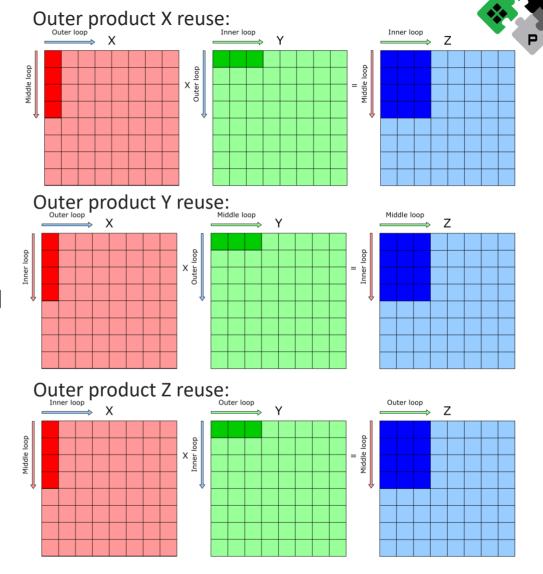




Outer product options

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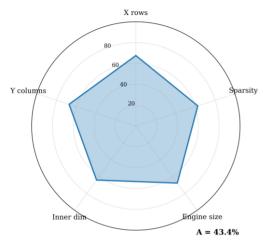




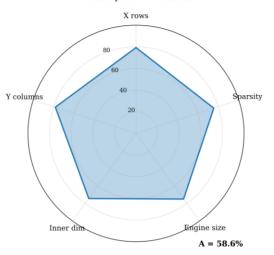
Dataflows comparison –BW consumption



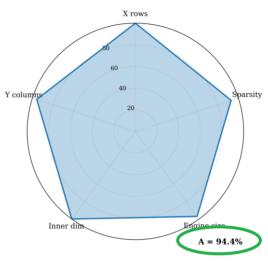
BW consumption - S Gustavson X



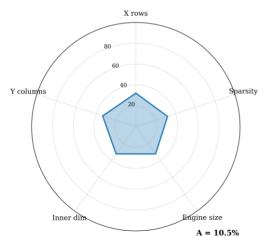
BW consumption - S Gustavson Y



BW consumption - S Gustavson Z



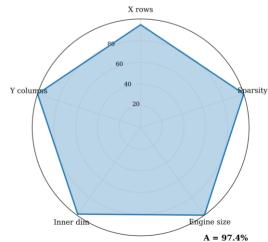
BW consumption - S OuterProd X



BW consumption - S OuterProd Y



BW consumption - S OuterProd Z

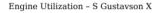


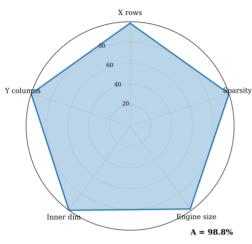




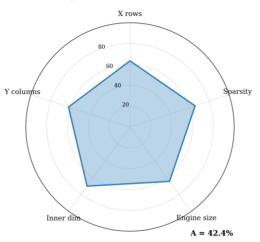
Dataflows comparison – Engine utilization



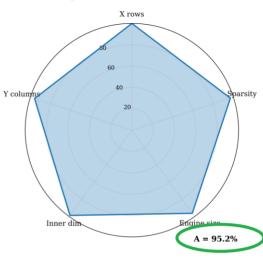




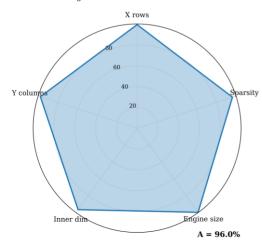
Engine Utilization - S Gustavson Y



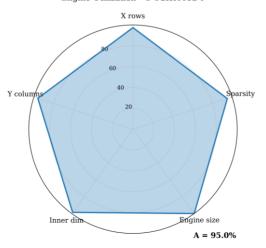
Engine Utilization - S Gustavson Z



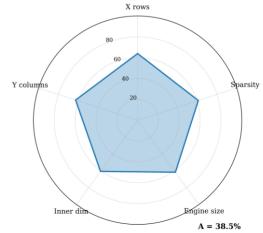
Engine Utilization - S OuterProd X



Engine Utilization - S OuterProd Y

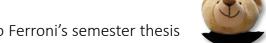


Engine Utilization - S OuterProd Z



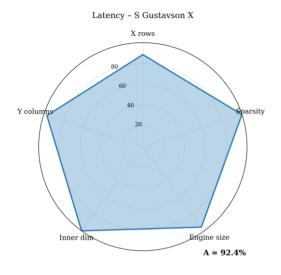


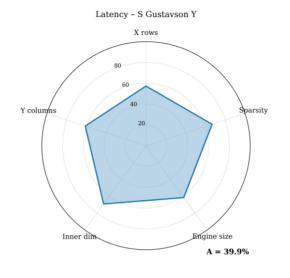


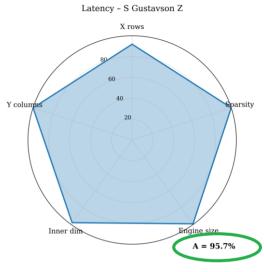


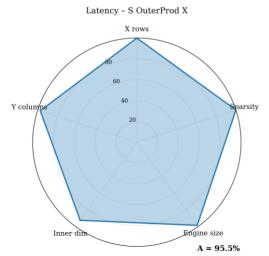
Dataflows comparison –Latency

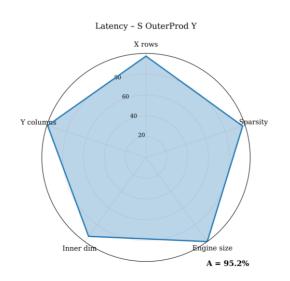


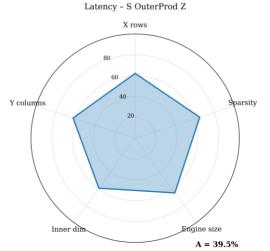














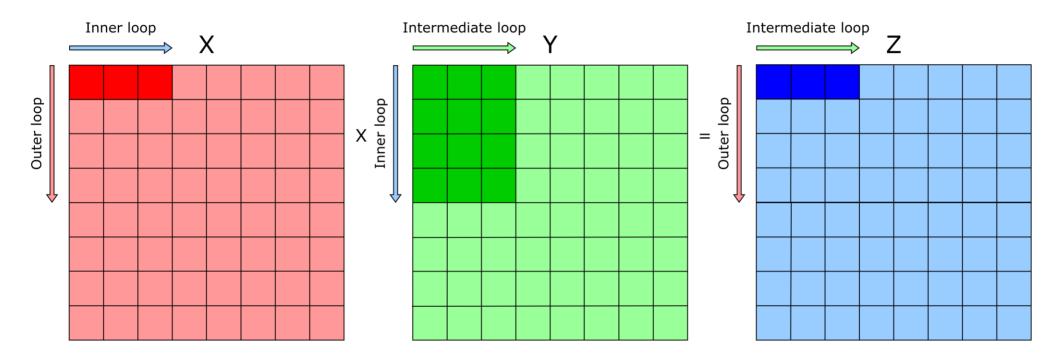


Final choice



Overall the gustavson dataflow seems to be the best

• It makes sense because you never load chunks of Z from memory

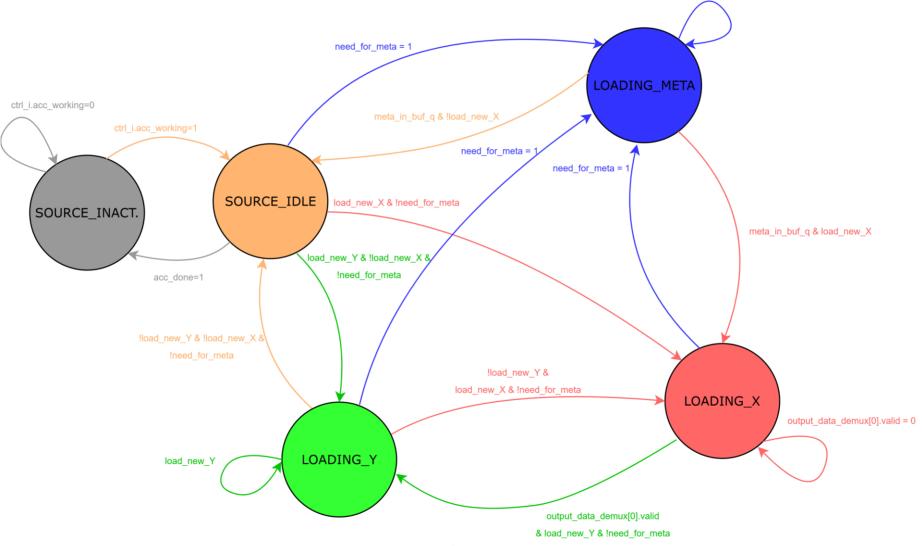


Gustavson dataflow with Z stationary



Streamer behavior —Loading part





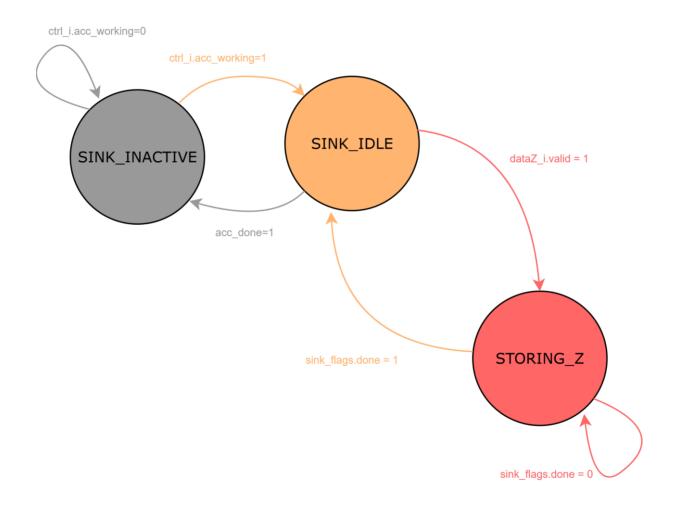




meta in buf q = 0

Streamer behavior –Storing part





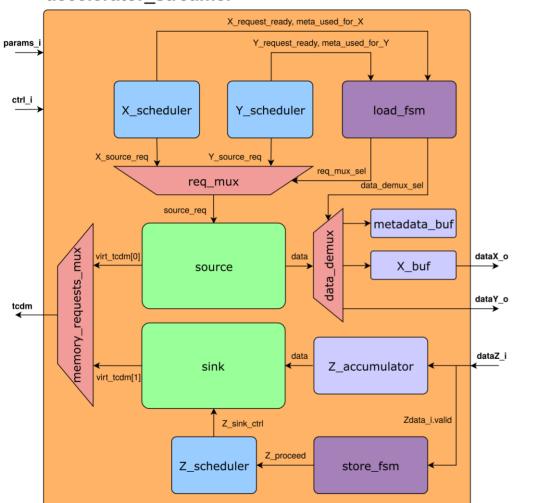


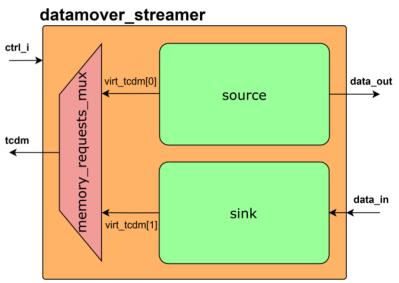


Accelerator's streamer architecture VS datamover one



accelerator_streamer





Non-sparsity-aware streamer architecture



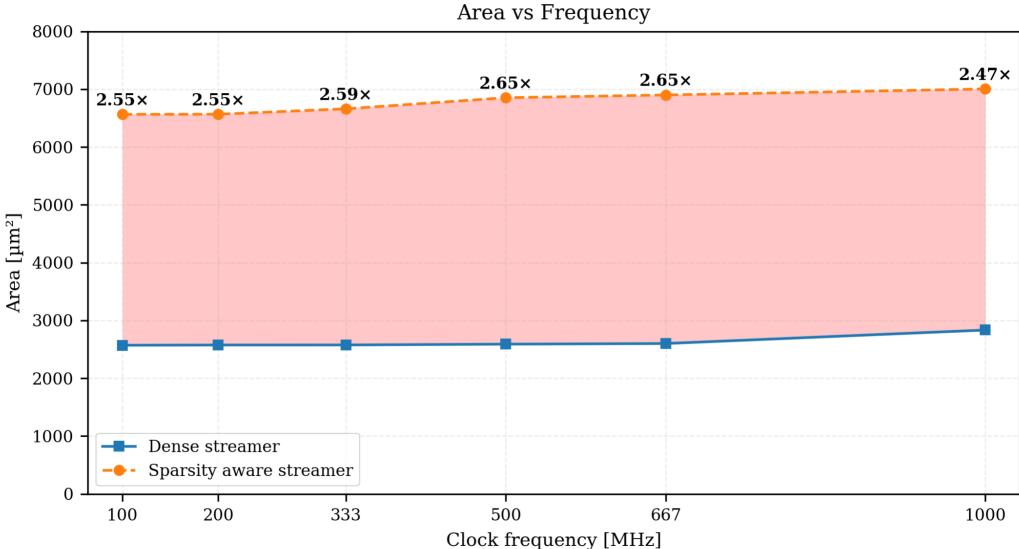






Synthesis results- A VS f plot









Synthesis results- Area contributions



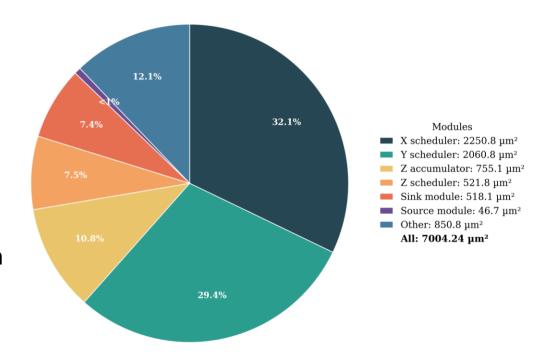
Adding intelligence is expensive

- The biggest area contributions come from the schedulers
- There is also an accumulator for Z that has a register buffer as wide as a whole block of Z; in this case 256bits.

Different function of the source module

 In the datamover streamer the source module was of comparable size with the sink module since they both did strided accesses. In my streamer this is no longer true.

Area of the streamer at 1GHz clock frequency

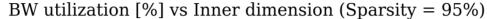


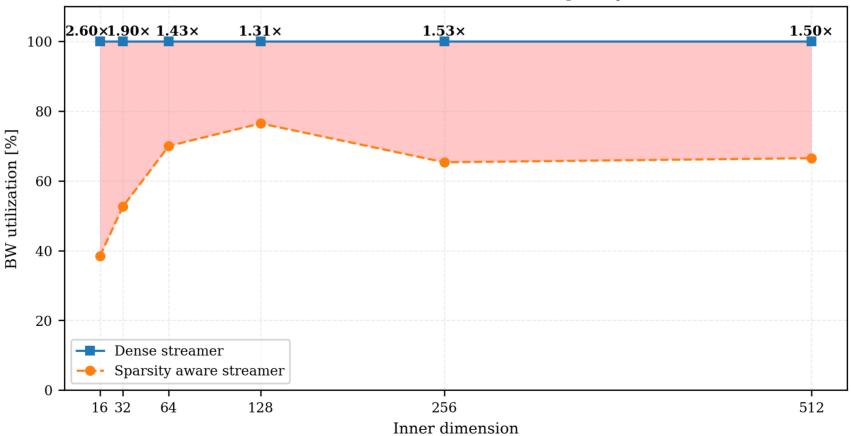




RTL results –Inner dimension (BW utilization)





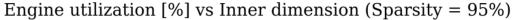


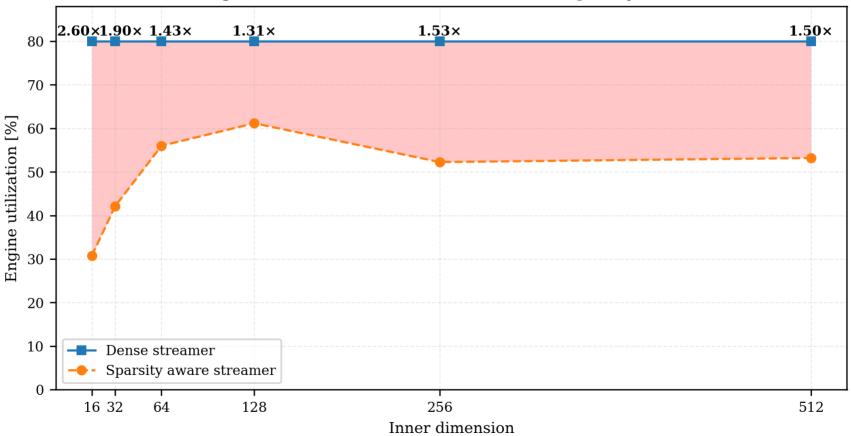




RTL results –Inner dimension (engine utilization)









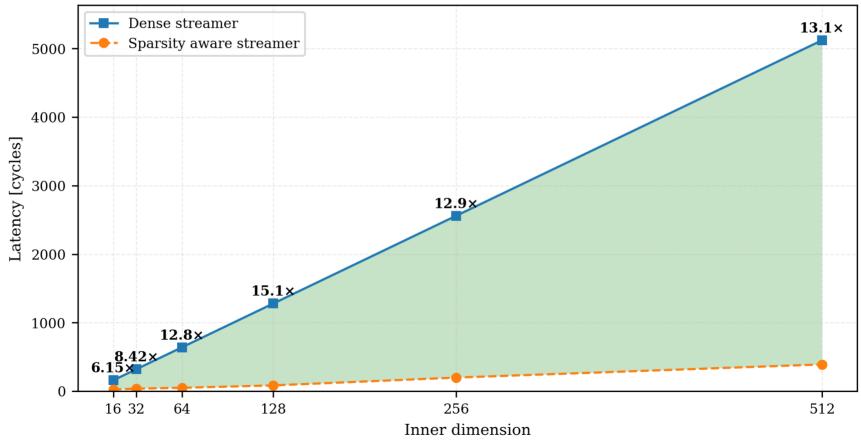


RTL results –Inner dimension (latency)



All the results are acquired with an engine size of 4 MAC units, with an X matrix having two rows, a Y matrix having 16 columns and a metadata chunk of 128bits

Latency [cycles] vs Inner dimension (Sparsity = 95%)

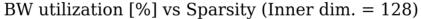


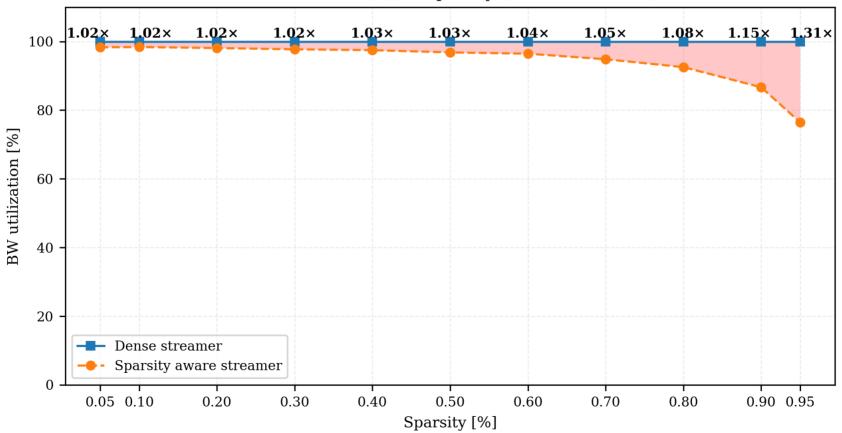




RTL results – Sparsity (BW utilization)







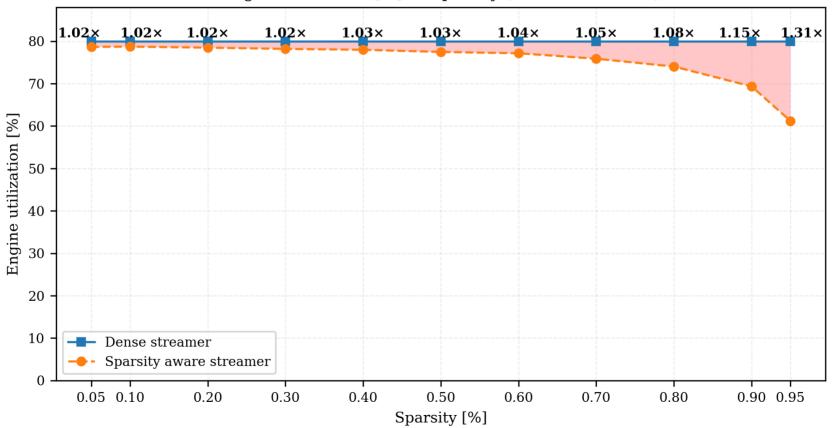




RTL results –Sparsity (engine utilization)





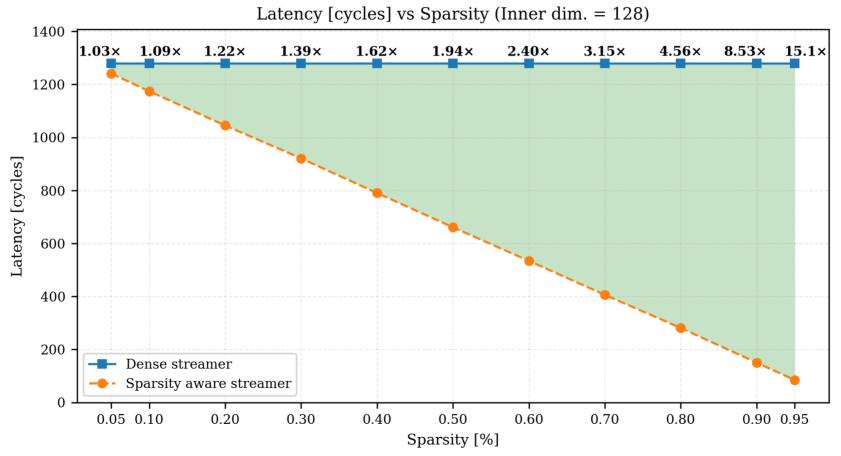






RTL results –Sparsity (latency)









- GEMM is a fundamental operation
- We can exploit sparsity to accelerate it
- Problem: might be complicated to modify existing systems
- Solution: a flexible and portable accelerator whose streamer is smart enough to manage sparsity
- This costs in terms of area

PULP Platform

Open Source Hardware, the way it should be!





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