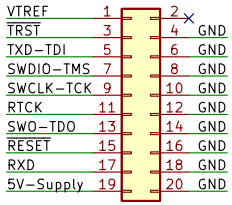
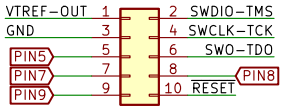
J-LINK INPUT HEADER



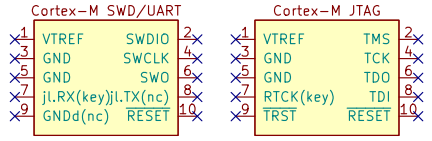
SWD/JTAG OUTPUT HEADERS

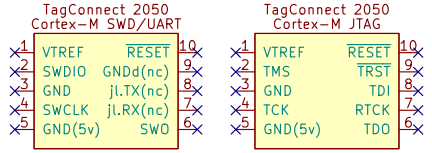


0.1" (2.45mm) pitch

0.05" (1.27mm) pitch

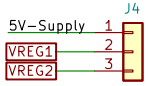
HEADER REFERENCES FOR TARGET BOARD





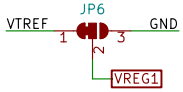
The connectors above slightly differ from the pinout of the default J-Link connectors. The original pinout is put in brackers: (key) (this is normally not populated), (nc) and (5v). SEE JUMPERS JP1-2-3-4 TO RECONFIGURE THESE PINS IF NECESSARY.

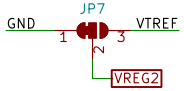
DC-DC CONVERTER HEADER



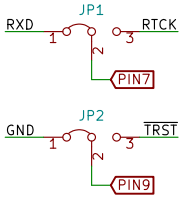
(not plugged in = disabled)

DC-DC CONVERTER PINOUT SELECTION



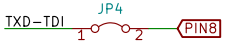


SWD/JTAG MODE SELECTION

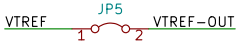


JP1 & JP2:
1-2 = SWD
2-3 = JTAG

PIN 8 DISCONNECT

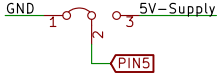


INLINE CURRENT MEASURING JUMPER



The target gets power from the adapter through VTREF(-OUT) if the DC-DC converter is plugged in.

PIN 5 MODE SELECTION



BEST PRACTICES

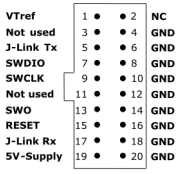
Use the 0.05" pinheader for 'regular' development and the TagConnect cable for fast, on-location reprogramming.

GNDdetect (GNDd) can be used by the target board to detect if a debugger is present. (pin 9 is in SWD mode connected to GND)

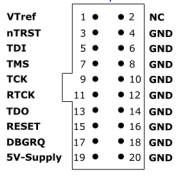
Put a 100 kΩ pullup resitor on the SWDIO line and a 10 kΩ pullup resistor on the RXD line of the target board.

If RTCK is not available on the target board it should be connected to GND.

J-Link 20-pin SWD + VCOM



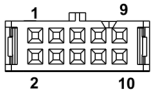
J-Link 20-pin JTAG



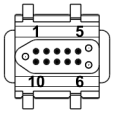
DBGRQ = NC inside J-Link

TagConnect 2050

END VIEW
2x5 0.1" PITCH
IDC CONNECTOR



END VIEW
10 PIN
TAG-CONNECT



adapter.brechtve.be
Brecht Van Eeckhoudt

Sheet: /
File: jlink-tagConnect-adapter-v2.sch

Title: Universal J-Link Adapter

Size: A4	Date: 2019-11-28	**Rev: v2.0**
KiCad E.D.A. kicad 5.1.5-52549c584ubuntu19.04.1		Id: 1/1