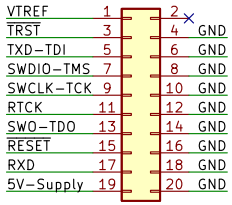
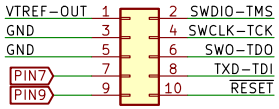
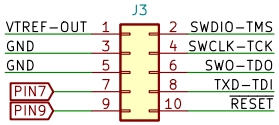
J-LINK INPUT HEADER



SWD/JTAG OUTPUT HEADERS

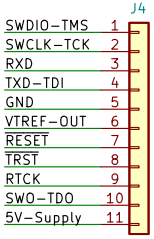


0.1" (2.45mm) pitch

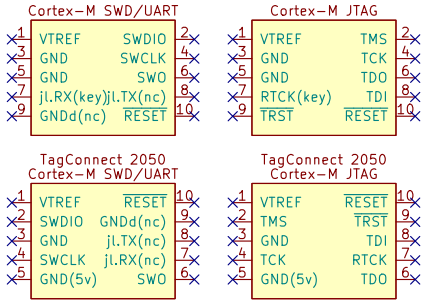


0.05" (1.27mm) pitch

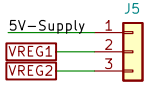
SWD/JTAG BREAKOUT HEADER



HEADER REFERENCES FOR TARGET BOARD

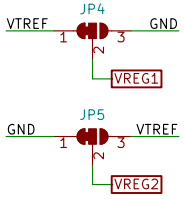


DC-DC CONVERTER HEADER

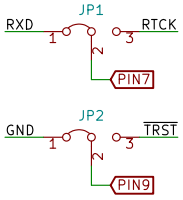


(not plugged in = disabled)

DC-DC CONVERTER PINOUT SELECTION

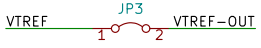


SWD/JTAG MODE SELECTION



Both jumpers:
1-2 = SWD-mode
2-3 = JTAG-mode

INLINE CURRENT MEASURING JUMPER



BEST PRACTICES

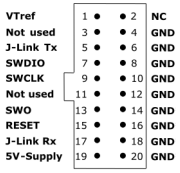
Use the 0.05" pinheader for 'regular' development and the TagConnect header for fast on-location reprogramming.

GNDdetect (GNDd) can be used by the target board to detect if a debugger is present. (pin 9 in SWD mode connected to GND)

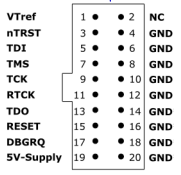
Put a 100 kΩ pullup resitor on the SWDIO line and a 10 kΩ pullup resistor on the RXD line on the target board.

If RTCK is not available on the target board it should be connected to GND.

J-Link 20-pin SWD + VCOM



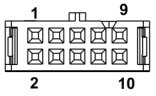
J-Link 20-pin JTAG



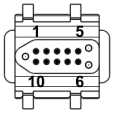
DBGRQ = NC inside J-Link

TagConnect 2050

END VIEW
2x5 0.1" PITCH
IDC CONNECTOR



END VIEW
10 PIN
TAG-CONNECT



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H1 M3 H2 M3 H3 M3 H4 M3

Sheet: /
File: jlink-tagConnect-adapter-v2.sch

Title: J-Link / Tag-Connect adapter board

Size: A4	Date: 2019-11-25	Rev: v2.0
KiCad E.D.A. kicad 5.1.5-52549c584ubuntu19.04.1		Id: 1/1