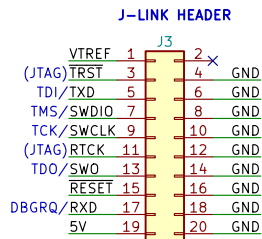


VTref	1	•	2	NC
Not used	3	•	4	GND
J-Link Tx	5	•	6	GND
SWDIO	7	•	8	GND
SWCLK	9	•	10	GND
Not used	11	•	12	GND
SWO	13	•	14	GND*
RESET	15	•	16	GND*
J-Link Rx	17	•	18	GND*
5V-Supply	19	•	20	GND*



Black: SWD
Blue: JTAG

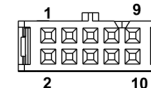
TAG-CONNECT HEADER



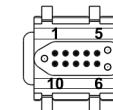
Modified 9-pin J-Link SWD/JTAG header

VTREF	1	10	SWDIO/TMS
GND	2	9	SWCLK/TCK
GND	3	8	SWO/TDO
RXD	4	7	TXD/TDI
TRST	5	6	RESET

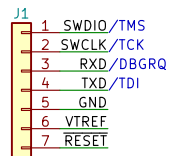
END VIEW
2x5 0.1" PITCH
IDC CONNECTOR



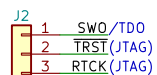
END VIEW
10 PIN
TAG-CONNECT



SWD/JTAG BREAKOUT HEADER

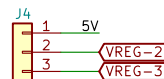


ADDITIONAL BREAKOUT HEADER

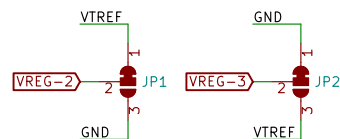


NOTE: RTCK is the only SWD/JTAG pin not connected to the Tag-Connect header. Perhaps in a future board revision this can be put on pin 3 (now second GND)

DC-DC CONVERTER HEADER (not plugged in = disabled)

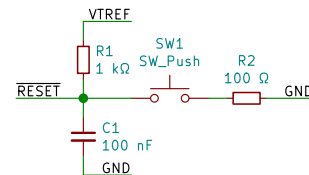


SOLDER BRIDGES FOR DC-DC CONVERTER PINOUT SWITCHING



VIN - OUT - GND (Linear regulator)
VIN - GND - OUT (Switch Mode PSU)

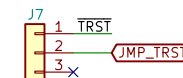
RESET BUTTON



JUMPER RXD



JUMPER TRST (JTAG)



Both jumpers on 2-3 = SWD-mode

Brecht Van Eeckhoudt

Sheet: /

File: jlink-tagConnect-adapter.sch

Title: J-Link / Tag-Connect adapter board

Size: A4

Date: 18/09/2019

Rev: 1.0

KiCad E.D.A. kicad 5.1.4-e60b26684ubuntu19.04.1

Id: 1/1