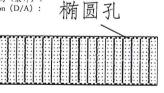




L/F Direction (D/A) :



实物图: Chip photo:

特殊说明 Special Instructions:

DB注意:

DB注意: 1、以随工单要求按顺序装片,如:先装A芯片,装B芯片时必须 装在有A芯片的框架,反之先装B芯片,装A芯时B芯已经装片; 2、控制溢股,为WB预管焊线位置; WB注意:

1、数字为PAD不打线个数;

注意:基岛全镀银

							1				
说明 Instructions	粘片胶类型 Epoxy type	芯片名称 Die name	芯片尺寸 Die Size	最小焊盘尺寸 Min BPO (μm²)	最小焊盘间距 Min BPP(μm)	铝垫厚度(μm) Pad Thickness	焊盘下是否有电路 Circuit under Pad	划片道宽度 Street line (μm)	品圆尺寸 Wafer Size	是否是 Low-K If low-k?	减薄厚 (µm) Nafet Thicks
A芯: DIE A	导电胶 (conductivity) S210	HS5143	490*602(um²) 19.29*23.71(mil²)	60*60	75	1.0	是/Yes	60	8	否/N0	300
B芯: DIE B	导电胶 (conductivity) S210	TX6216	340+225(um²) 13.39*8.86(mil²)	75*75	115	2.75	是/Yes	60	8	否/N0	300
C芯: DIE C		ž.			,				AC.		
制图目期 Create Date		2024/1/31		生效日期 Effective Date					客户确认 Customer		
拟 Prepar		42 Jone. 2-1	审核 Checked by	283 m	2471	批准 Approved by					

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