
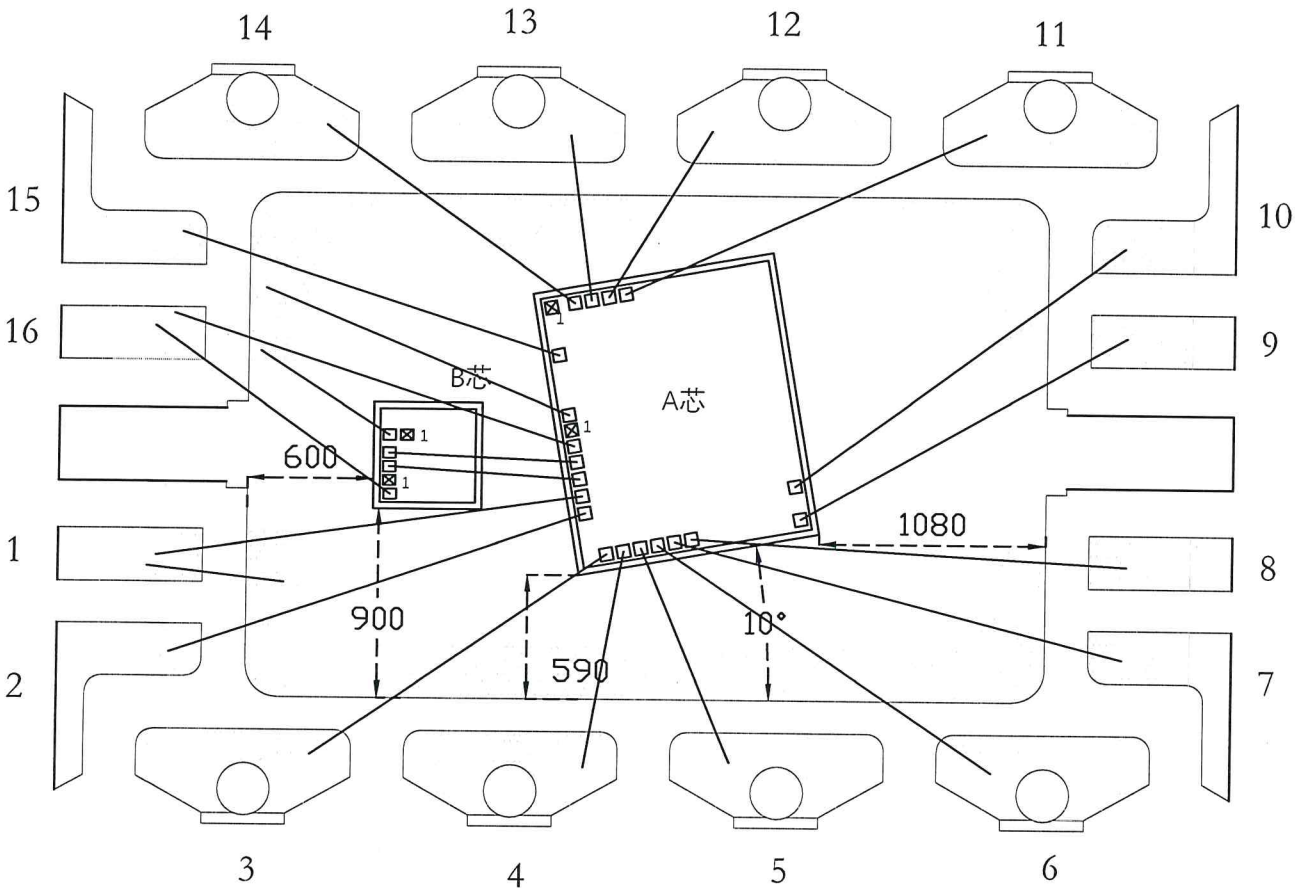
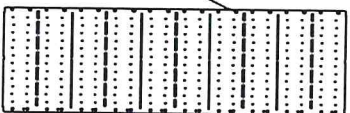


<div> 池州华宇电子科技股份有限公司 CHI ZHOU HISEMI ELECTRONICS TECHNOLOGY CO.,LTD</div>				客户代码 Customer No.	008	线号 Drawing No.	HY-PX-008-791 A			
焊线图纸 Bonding Diagram				产品名称 Product Type	HS23P56E		封装外型 PKG Type	SOP16L(12R)		
焊线种类 Wire Type	焊线直径(μm) Wire Diameter	焊线根数 NO. of wire	焊线总长(μm) Total wire length	最长线长(μm) Longest wire length	最短线长(μm) Shortest wire length	塑封料型号(绿色环保) Compound Type (Green)		LF载体尺寸 LF Pad Size		
合金丝 Ag	20	22	39721	2345	645	首选(Preferred): CEL-1702HF 备选(Optional): EME-G630AY		SOP16L-12R (94*150mil ²) (2390*3810um ²)		
客户图号 Customer drawing NO.										



框架传送方向 (装片): L/F Direction (D/A):			实物图: Chip photo:			特殊说明 Special Instructions:					
<div>椭圆孔</div> 						<div>DB注意:</div> <div>1.以随工单要求按顺序装片,如:先装A芯片,装B芯片时必须装在有A芯片的框架,反之先装B芯片,装A芯片时B芯片已经装片;</div> <div>2.控制溢胶,为WB预留焊线位置。</div> <div>WB注意:</div> <div>1.数字为不打线pad点个数。</div> <div>2.植球点在A芯</div>					

说明 Instructions	粘片胶类型 Epoxy type	芯片名称 Die name	芯片尺寸 Die Size	最小焊盘尺寸 Min BPO (μm ²)	最小焊盘间距 Min BPP(μm)	铝垫厚度(μm) Pad Thickness	焊盘下是否有电路 Circuit under Pad	划片道宽度 Street line (μm)	晶圆尺寸 Wafer Size	是否是 Low-K If low-k?	减薄厚度 (μm) Wafer Thickness
A芯: DIE A	导电胶 (conductivity) S210	HS5133	1100*1292(um ²) 43.31*50.87(mil ²)	60*60	60	1	是/Yes	60	8	否/NO	300
B芯: DIE B	导电胶 (conductivity) S210	P24C08D	456*445(um ²) 17.95*17.52(mil ²)	60*50	65	1.2	是/Yes	60	8	否/NO	200
C芯: DIE C											
编制 Prepared by	2024.9.3		制图日期 Create Date	2024/8/31		生效日期 Effective Date	客户确认签字/盖章: Customer Signature				
研发审核 R&D Check	2024.9.3		产品工程审核 PE Check			批准 Approved by					

*温馨提示: 图纸为产品下线生产的唯一依据, 请您认真确认, 我司依据您回签后的图纸生产, 如因图纸错误会产生不可估量损失, 谢谢!

*warm tips: the drawing is the only basis for the production of the product. Please confirm it carefully. Our company will produce the drawings according to the drawings you have signed back, such as drawing mistakes, which will produce incalculable loss. Thank you

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