
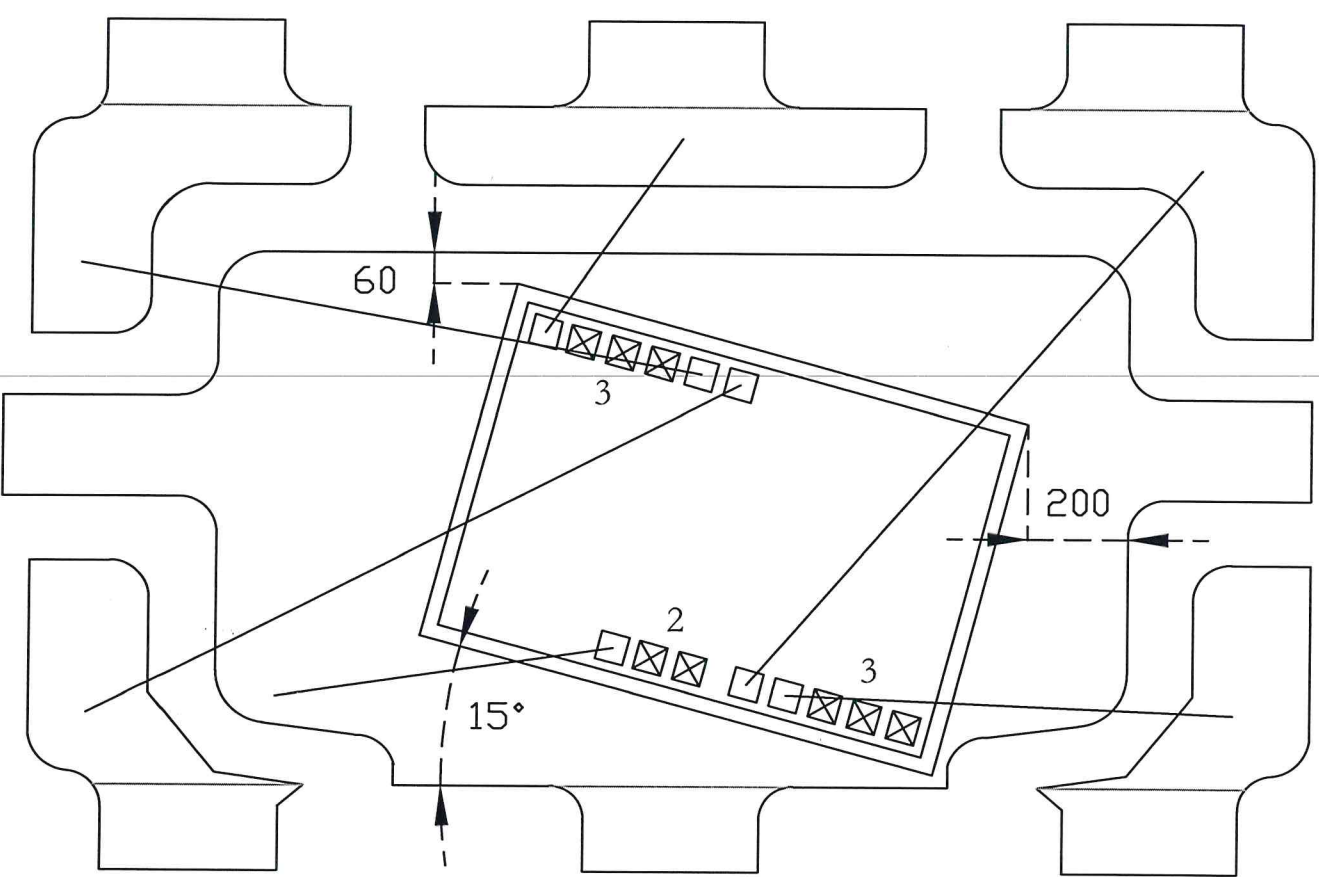


<div><div>池州华宇电子科技股份有限公司 CHI ZHOU HISEMI ELECTRONICS TECHNOLOGY CO.,LTD</div></div>				客户代码 Customer No.	008	线图号 Drawing No.	HY-PX-008-773 A			
焊线图纸 Bonding Diagram				产品名称 Product Type	HS23P2710UN		封装外型 PKG Type	SOT26 (14R)		
焊线种类 Wire Type	焊线直径(μm) Wire Diameter	焊线根数 NO. of wire	焊线总长(μm) Total wire length	最长线长(μm) Longest wire length	最短线长(μm) Shortest wire length	塑封料型号(绿色环保) Compound Type (Green)		LF载体尺寸 LF Pad Size		
合金丝 Ag	20	6	7455	1450	469	首选(Preferred): CEL-8240HF 备选(Optional):		SOT23-6L-14R (42×72mil²) 1060*1820um²		
客户图号 Customer drawing NO.										

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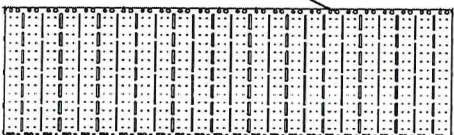
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框架传送方向(装片): L/F Direction (D/A): 椭圆孔				实物图: Chip photo:			特殊说明 Special Instructions:			
							DB注意: 芯片顺时针旋转15° WB注意: 数字为不打线pad个数。			
注意: 基岛全镀银										

说明 Instructions	粘片胶类型 Epoxy type	芯片名称 Die name	芯片尺寸 Die Size	最小焊盘尺寸 Min BPO (μm²)	最小焊盘间距 Min BPP(μm)	铝垫厚度(μm) Pad Thickness	焊盘下是否有电路 Circuit under Pad	划片道宽度 Street line (μm)	晶圆尺寸 Wafer Size	是否低K Low-K If low-k 2	减薄厚度 (μm) Wafer Thickness
A芯: DIE A	绝缘胶 (Non-conductive) 84-3JT	HS5113	999.4*665(μm²) 39.34*26.18(mil²)	57*57	78	1	是/YES	60	8	否/NO	230
B芯: DIE B											
C芯: DIE C											
拟制 Prepared by	张时俊 2024.6.22		制图日期 Create Date	2024/6/22		生效日期 Effective Date	客户确认签字/盖章: Customer Signature				
研发审核 R&D Check	[Signature] 2024.6.22		产品工程审核 PE Check			批准 Approved by					

*温馨提示: 图纸为产品下线生产的唯一依据, 请您认真确认, 我司依据您回签后的图纸生产, 如图纸错误会产生不可估量损失, 谢谢!
*warm tips: the drawing is the only basis for the production of the product. Please confirm it carefully. Our company will produce the drawings according to the drawings you have signed back, such as drawing mistakes, which will produce inestimable loss. Thank you

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